

Interface IP

MIPI D-PHY Transmitter/Receiver for Samsung 28nm FD-SOI

Overview

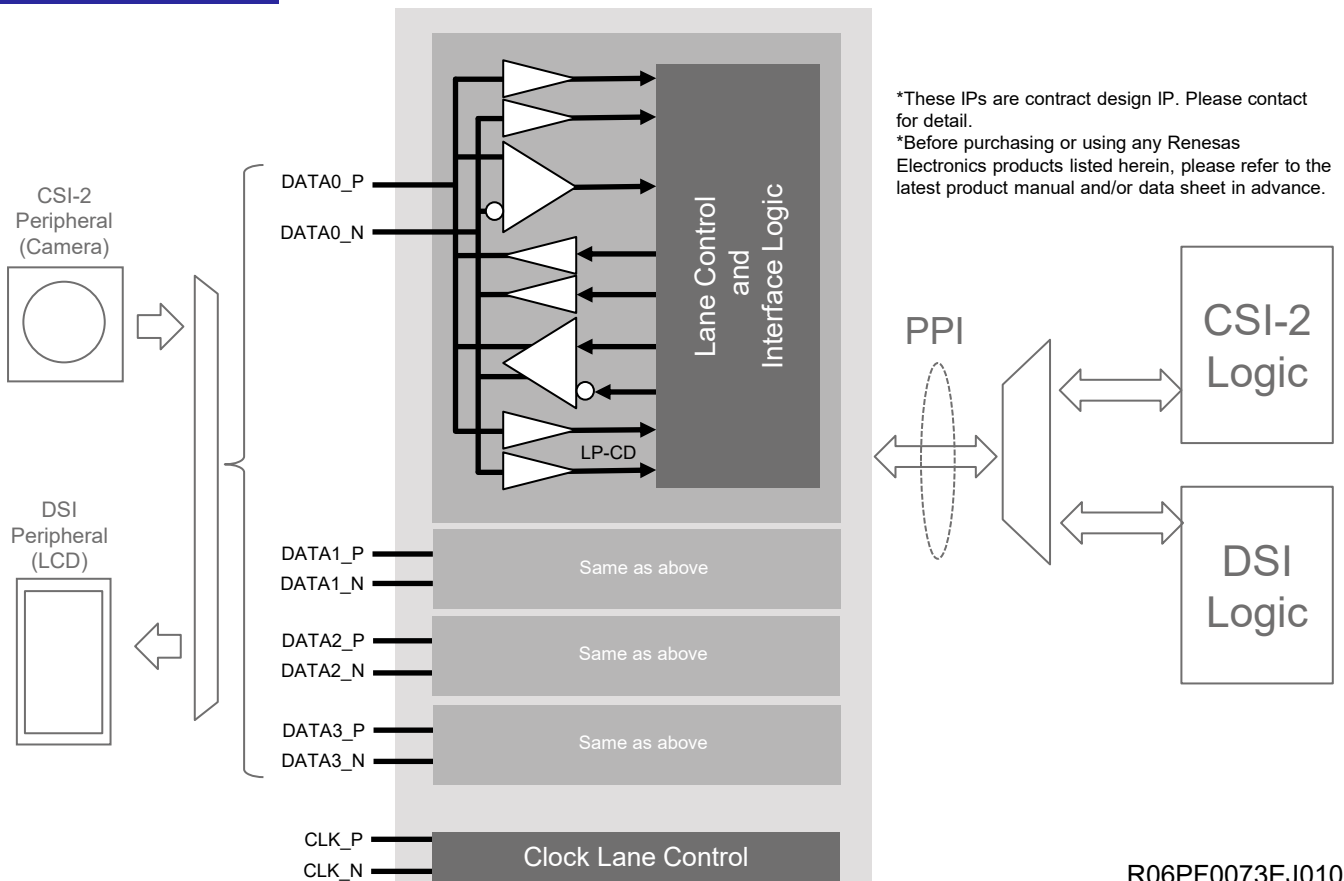
The Renesas MIPI® D-PHY™ Transmitter/Receiver is useful 4 Data Lanes transmitter/receiver hard macro for DSISM/CSI-2® of Samsung 28nm FD-SOI process.

Features

- Renesas MIPI D-PHY Transmitter/Receiver can be used for analog Transmitter/Receiver of following interface .
 - MIPI alliance Specification for D-PHY Version 2.1 15 December 2016.
 - MIPI alliance Specification for DSI(Display Serial Interface).
 - MIPI alliance Specification for CSI-2(Camera Serial Interface 2).
- Technology is Samsung 28nm FD-SOI 8M.
- Supply voltage can be applied 1.1V for core voltage, 1.8V for IO voltage.
- Maximum data rate of each channel is 1.5Gbps at High-speed mode.

Block Diagram

MIPI D-PHY Transmitter/Receiver



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