



This document regroups all links necessary for the user to develop an application

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Mouseover "L" in "Link"

RZ/G3S Easy Download Guide

Step-1	Information for engineers	Contents Type	Contents Name	RZ/G3S Link	Note	Access levels
1-1	RZ/G Series 32/64-bit MPU (Wiki)	Link summary	Introduction,Boards,Software and Documentation	Link	-	Non limitation
1-2	Contact Support	QA Ticket	Technical Q&A	Link	-	Non limitation

Step-2	Introduction	Contents Type	Contents Name	RZ/G3S Link	Note	Access levels
2-1	Hardware Information	Document	RZ/G3S Datasheet	Link	-	Non limitation
2-2	Introduction for products	Document	RZ Family Microprocessors	Link	-	Non limitation
		Document	Renesas RZ/G3S Group	Link	-	Non limitation
		Video	60's Video	Link	-	Non limitation
		Document	RZ Family Product Part Number Guide	Link	-	Non limitation

Step-3	Launch of board and evaluation environment	Contents Type	Contents Name	RZ/G3S Link	Note	Access levels
3-1	User's Manual	Document	User's Manual: Hardware	Link	-	Non limitation
		Document	Additional document for User's Manual(SDHI)(Requires LoA)	Please contact Distl/Sales	LoA*1	Please contact Distl/Sales
3-2	How to start up the board	Document	SMARC Module Board User's Manual	Link	-	Non limitation
		Document	SMARC Carrier Board User's Manual	Link	-	Non limitation
		Document	Evaluation Board Kit(EVK) Quick Start Guide	Link	-	Non limitation
		Document	RZ/G Series Performance Guide	Link	-	Non limitation
3-3	Board information / how to get	Info/purchase	Evaluation Kit WEB Page	Link	-	Non limitation
3-4	Evaluation environment start-up method Verified Linux Package (VLP)	Document	RZ/G Verified Linux Package V3.0.7 Release Note	Link	-	Non limitation
		Document	Linux Start-up Guide	Link	-	Non limitation
		Document	RZ/G3S Group BSP Manual Set	Link	EULA*2	MyRenesas
		Document	RZ/G Proprietary SW Package Handling	Please contact Distl/Sales	-	Please contact Distl/Sales
3-5	Evaluation environment start-up method Multi-OS Package	Document	Release Note for Multi-OS Package v2.3.0	Link	-	Non limitation
		Document	RZ/G3S AWO Example Project Start-up Guide	Link	-	Non limitation
		Document	Getting Started with Flexible Software Package	Link	-	Non limitation
		Document	Setting GPIO with Flexible Software Package	Link	-	Non limitation
		Document	FSP Documentation	Link	-	Non limitation
		Document	Release Note for Flexible Software Package	Link	-	Non limitation
3-6	RZ Security Solution Overview	Document	RZ Security Solution Overview	Link	-	Non limitation
3-7	How to get various software	Software	RZ/G3S Pre-built Images	Link	-	Non limitation
		Web Link	RZ/G Verified Linux Package [5.10-CIP]	Link	-	Non limitation
		Web Link	RZ/G3S Group Multi-OS Package	Link	EULA*2	MyRenesas
		Web Link	Security Package	Link	SLA*3	Secure Access
		Tool	Smart Configurator	Link	-	Non limitation
		Document	Smart Configurator for RZ User's Guide	Link	-	Non limitation

Step-4	Custom board development	Contents Type	Contents Name	RZ/G3S Link	Note	Access levels
4-1	Documents and Simulation Models required for custom board design and development	Document	PCB Design and Verification Flow (for DDR-IF)	Please contact Distl/Sales		Please contact Distl/Sales
		Document	Board circuit design checklist	Link	-	Non limitation
		Document	PCB Design Guidelines for MIPI-CSI, MIPI-DSI, USB2.0 and PCI Express Gen2	Link	-	Non limitation
		Document,Tool	PCB Design Guideline for LDDR4, DDR Config Generation Tool	Link	TCU*5	Secure Access
		Document,Model	PCB verification guide for Core VDD, PDN model	Link	-	Non limitation
		Document,Model	PCB verification guide for LDDR4, PKG model	Link	TCU*5	Secure Access
		Design Data	SMARC Module Board Design Data(Schematics,Layout,BOM)	Link	Disclaimer*4	MyRenesas
		Design Data	Carrier Board Design Data(Schematics,Layout,BOM)	Link	Disclaimer*4	MyRenesas
		Model	IBIS	Link	TCU*5	Secure Access
		File	BSDL	Link	Disclaimer*4	MyRenesas
		Document	Guide to Using RGMII in Making an Ethernet-IF Connection	Link	-	Non limitation
		Document	Power Consumption Measurement	Link	-	Non limitation
		Document	RZ Series Current Measurement Program(Application Note)	Link	-	MyRenesas
		Tool	RZ Series Current Measurement Program(Tool)	Link	Disclaimer*4	MyRenesas
		Document	Reference power consumption guide	Link	-	Non limitation
		Document	Thermal Management Guideline	Link	-	Non limitation
		Document	Mechanical Handling Guideline	Link	-	Non limitation
		Document	Mount Conditions Reflow Soldering Conditions	Link	-	Non limitation
		Document	Mount Conditions Baking Conditions	Link	-	Non limitation
		Document	Semiconductor Package Mount Manual	Link	-	Non limitation
		Document	Lifetime Guideline	Link	-	Non limitation
		Device Info	Power Management IC	Link	-	Non limitation
		Device Info	Clock Generator IC	Link	-	Non limitation
		Document	R9A08G045XXGBG Reliability Report	TBD		Non limitation
		Document	Power calculation sheet and guide	Please contact Distl/Sales	-	Please contact Distl/Sales

*1: Letter of Acknowledgement
*2: End User Licence Agreement
*3: Software Licence Agreement
*4: Disclaimer Letter
*5: Terms and Conditions of Use

[Additional Information] Please refer to the software content archive list if necessary.
[補足情報 必要に応じてソフトウェアコンテンツ アーカイブ一覧をご参照ください。]

RZ/G Software package archive list (1H2025)

5.10-CIP

Product	Libraly Nick Name	Version	Package Name
RZ/G3S	Verified Linux Package	v3.0.6	RZ/G Verified Linux Package v3.0.6(RTK0EF0045Z0021AZJ-v3.0.6.zip)
		v3.0.6-update1	RZ/G Verified Linux Package v3.0.6-update1(RTK0EF0045Z0021AZJ-v3.0.6-update1.zip)
		v3.0.6-update2	RZ/G Verified Linux Package v3.0.6-update2(RTK0EF0045Z0021AZJ-v3.0.6-update2.zip)
		v3.0.6-update3	RZ/G Verified Linux Package v3.0.6-update3(RTK0EF0045Z0021AZJ-v3.0.6-update3.zip)
	Board Support Package(BSP)	v1.0	https://www.renesas.com/en/document/swo/rzg3s-board-support-package-rtk0ef0045z95001zj-v100zip?r=25462026