

ISL73007SEHEV2Z

The ISL73007SEHEV2Z evaluation board (shown in [Figure 3](#)) features the [ISL73007SEH](#) buck regulator. The IC is a small foot print radiation hardened POL designed for critical low power applications.

The ISL73007SEH is operational over 3V to 18V integrating both high-side and low-side power FETs and switches at a default 500kHz frequency. The switching frequency can also be programmed from 300kHz up to 1MHz using an external resistor. The ISL73007SEH uses constant-frequency peak current mode control architecture for fast loop transient response. The ISL73007SEH can use either its internal compensation at the 500kHz default switching frequency or an external Type II compensation at other frequencies to stabilize the loop as determined by specific design and performance requirements. When integrating both P-channel and N-channel power devices with the option of internal compensation, a minimum of external components are required, thereby reducing the BOM count and complexity of the design.

The ISL73007SEHEV2Z evaluation board and this accompanying manual provide a quick and easy method to evaluate the ISL73007SEH part, see the [ISL73007SEH datasheet](#) for information about the operation, function, and performance of the device.

Features

- Optimized for 3.3V to 1.2V conversion, externally set 1MHz switching frequency, external slope and compensation configuration
- 3A output current

Specifications

The ISL73007SEHEV2Z evaluation board is by default externally configured for immediate evaluation with minimum components and connections, which allows for other conditions to be evaluated with user-modification of components and connections.

The electrical ratings of the ISL73007SEHEV2Z evaluation board are shown in [Table 1](#).

Table 1. Electrical Ratings

Parameter	Rating
PVIN Supply Voltage	2.9V - 12V
DC Output Voltage	1.2V
Operating Frequency	1MHz
Output Current	3A
Temperature	-55°C to +125°C

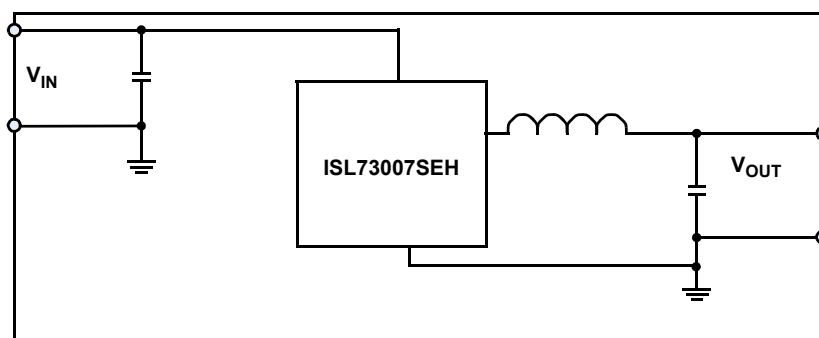


Figure 1. Block Diagram

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1. Functional Description

The ISL73007SEHEV2Z evaluation board has a default configuration of 3.3V to 1.2V conversion with a 3A maximum output current and contains the ISL73007SEH voltage regulator IC in the external configuration for the 1MHz switching frequency, and for control loop and slope compensations. [Figure 1](#) shows the ISL73007SEHEV2Z evaluation board block diagram. Photographs of the ISL73007SEHEV2Z are shown in [Figure 3](#) and [Figure 4](#).

The ISL73007SEHEV2Z evaluation board provides access to the pins of the IC device and convenient pads for connecting test equipment. For more information, see the schematic ([Figure 5](#)), PCB layers ([Figure 6](#) through [Figure 11](#)), and [Bill of Materials](#). Performance data taken using the ISL73007SEHEV2Z and basic lab equipment is shown in [Figure 12](#) through [Figure 19](#).

1.1 Operational Characteristics

The ISL73007SEHEV2Z only requires a single voltage supply > 2.9V connected to the PVIN pad to operate, outputting 1.2V on the VOUT pad with a 3A output current capability. Optimized for a nominal PVIN voltage of 3.3V, the input operating voltage at which the IC turns on is set by the resistor divider (R_1 and R_2) on the ENABLE pin. **Important:** Do not exceed 5V on the ENABLE pin.

1.2 Setup and Configuration

The following equipment is recommended for testing the board:

- 5V power supply
- 100MHz oscilloscope

Complete the following steps to configure and use the board:

1. Configure the board as shown in [Figure 2](#).
2. Connect and turn on a 3.3V power supply to the PVIN pad.
3. Using the oscilloscope to look at VIN and VOUT waveforms and to also observe the behavior of the LX phase node located on pins 13 and 14 of the IC package. Proper probe grounding must be practiced when observing clean waveforms.
4. Output current loading can be externally added at the VOUT and GND pads for loaded output evaluations. A DVM(s) can be employed to monitor the input and output voltages and currents.

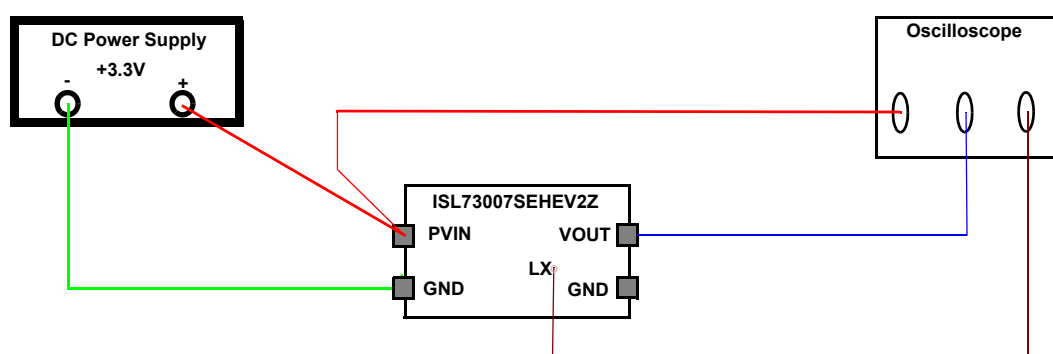


Figure 2. ISL73007SEH Basic Evaluation Test Setup Block Diagram

2. Board Design

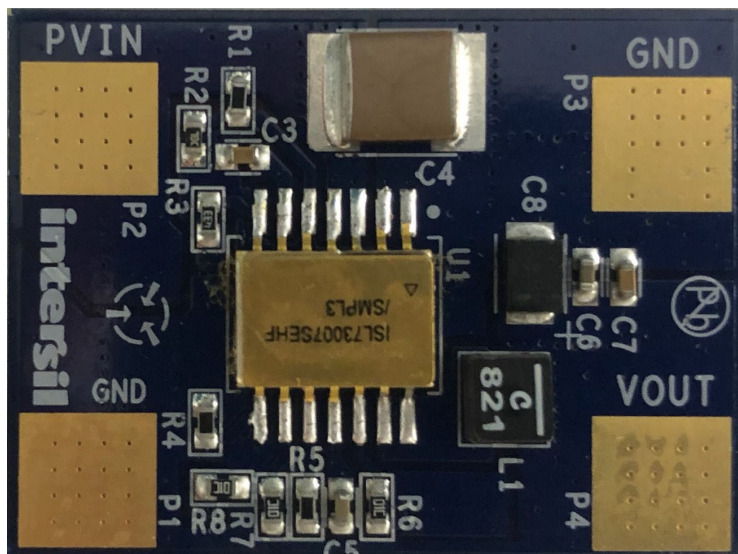


Figure 3. ISL73007SEHEV2Z Evaluation Board (Top)

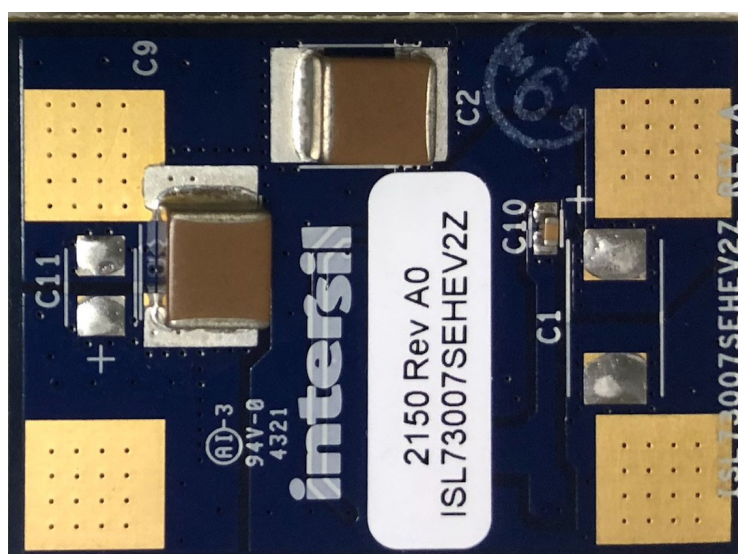


Figure 4. ISL73007SEHEV2Z Evaluation Board (Bottom)

2.1 Basic Layout

The ISL73007SEH is located in the center of the board and is labeled U1. Connect the input power across the PVIN and GND pads. The output voltage then appears across the V_OUT and GND pads near the output inductor (L_1) and the output capacitance (C_8), respectively. Additional C_{IN} is provided by C_2 (flip side) and C_3 . The other passive components near the PVIN pad are for setting the EN POR, slope compensation, and VCC decoupling. The passive components between the GND and V_OUT pads on the edge of the PCB are for switching frequency and output voltage setting, compensating the external loop error amplifier and PGOOD pull-up. Consult the schematic in (Figure 5) for more details.

2.2 Layout Guidelines

PCB design is critical to reduce parasitic inductances with critical components being closely placed to the IC. The critical components in order would be the loop compensation RC, the slope resistor, and the low ESR ceramic input capacitors. Avoid placing any traces or components under the LX shapes to avoid noise coupling from the switching node.

2.3 Schematic Diagrams

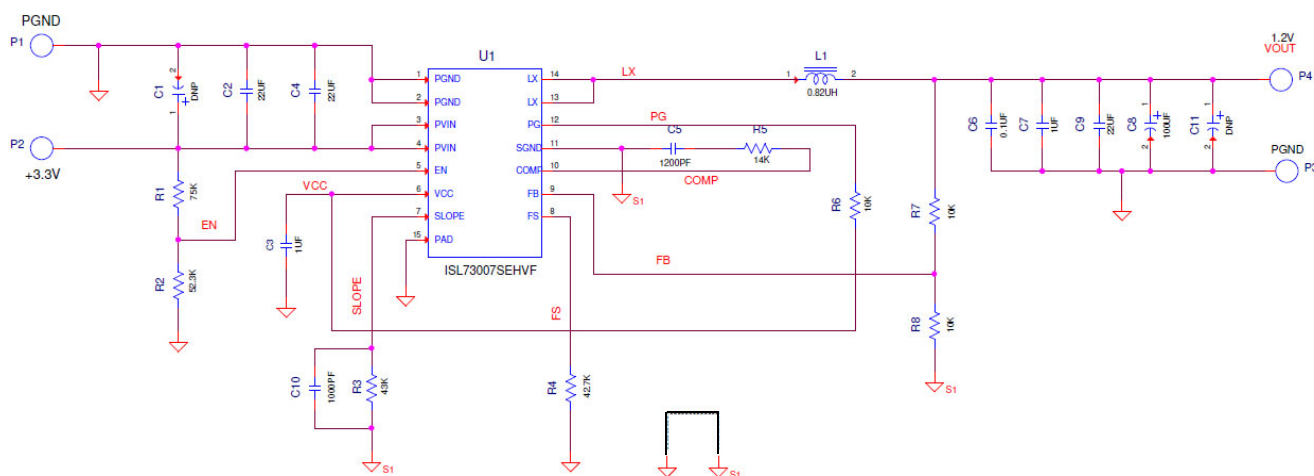


Figure 5. Schematic

2.4 Bill of Materials

Qty	Reference Designator	Description	Manufacturer	Manufacturer Part
1	U1	IC-RAD HARD 3A POL REGULATOR, 14P, CFP, ROHS	Renesas Electronics	ISL73007SEHF/PROTO
1		PWB-PCB, ISL73007SEHEV2Z, REVA, ROHS	Imagineering Inc	ISL73007SEHEV2ZREVAPCB
3	C2, C4, C9	CAPACITOR, SMD, 2220, 22 μ F, 25V, 20%, X7R, ROHS	TDK	C5750X7R1E226M
2	C3, C7	CAP-AEC-Q200, SMD, 0603, 1 μ F, 25V, 10%, X7R, ROHS	Murata	GCM188R71E105KA64D
1	C5	CAP, SMD, 0603, 1200pF, 50V, 1%, C0G/NP0, ROHS	Murata	GRM1885C1H122FA01D
1	C6	CAP, SMD, 0603, 0.1 μ F, 25V, 10%, X7R, ROHS	Yageo	CC0603KRX7R8BB104
1	C10	CAP, SMD, 0603, 1nF, 25V, 10%, X7R, ROHS	Yageo	CC0603KRX7R8BB102
1	C8	CAP-TANT, SMD, 1411, 100 μ F, 6V, 20%, 15m Ω at 100MHz, ROHS	Kemet	T520B107M006ATE015
1	L1	COIL-PWR INDUCT, SMD, 4x4mm, 0.82 μ H.20%, 9.4A, 8.6m Ω , ROHS	Coilcraft	XGL4020-821MEC
1	R3	RES, SMD, 0603, 43k, 1/10W, 0.1%, TF, ROHS	Panasonic	ERA-3AEB433V
3	R6, R7, R8	RES, SMD, 0603, 10k, 1/10W, 1%, TF, ROHS	Venkel	CR0603-10W-1002FT
1	R4	RES, SMD, 0603, 42.7k, 1/10W, 1%, TF, ROHS	Venkel	CR0603-10W-4272FT
1	R5	RES, SMD, 0603, 14k, 1/10W, 1%, TF, ROHS	Venkel	CR0603-10W-1402FT
1	R2	RES, SMD, 0603, 52.3k, 1/10W, 1%, TF, ROHS	Yageo	RC0603FR-052321L
1	R1	RES, SMD, 0603, 75k, 1/10W, 1%, TF, ROHS	Venkel	CR0603-10W-7502FT

2.5 Board Layout

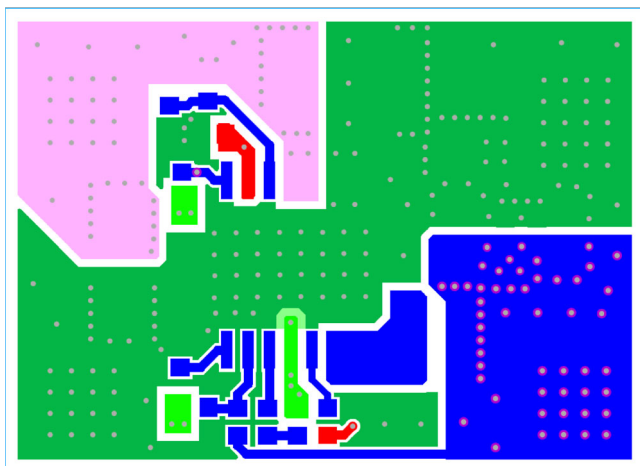


Figure 6. Top Layer

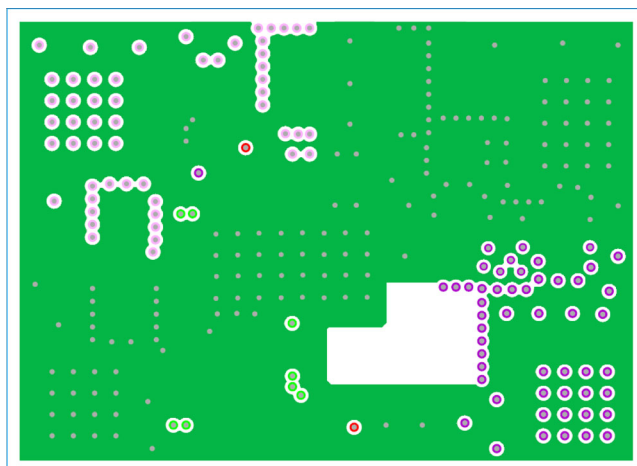


Figure 7. Layer 2

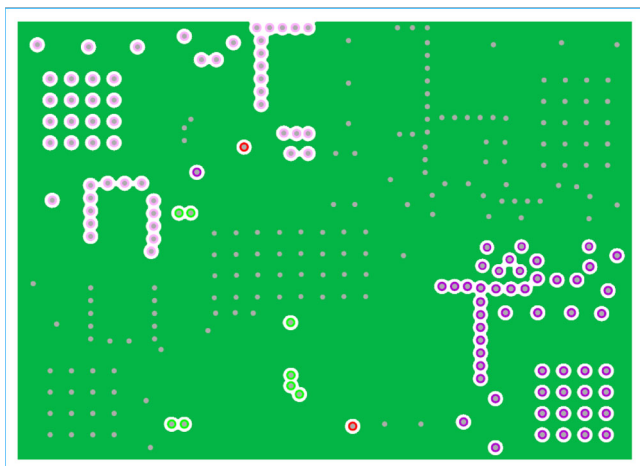


Figure 8. Layer 3

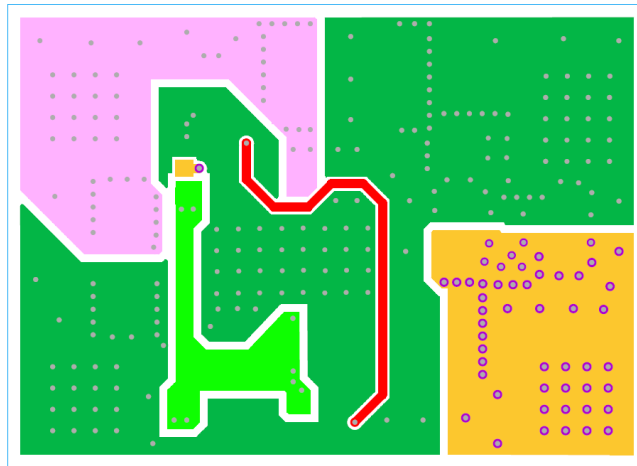


Figure 9. Bottom Layer

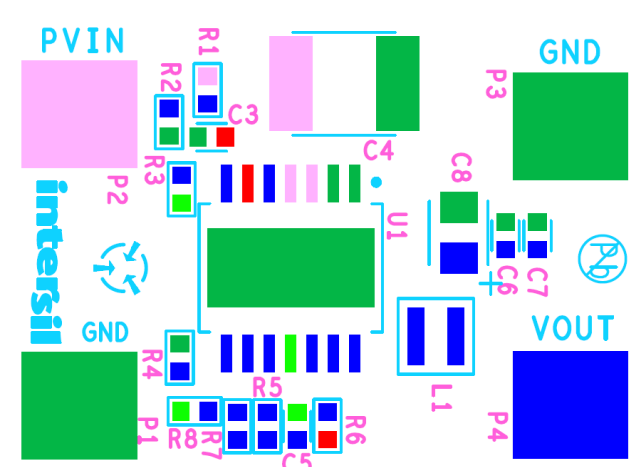


Figure 10. Top Silk Layer

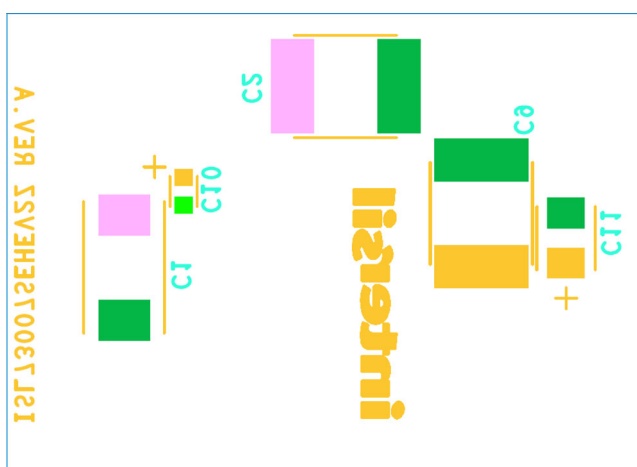


Figure 11. Bottom Silk Layer

3. Typical Performance Graphs

Unless otherwise noted, $P_{VIN} = 3.3V$; $V_{OUT} = 1.2V$, $f_{SW} = 1MHz$, $T_A = \text{Room Ambient}$. Some results with modified board.

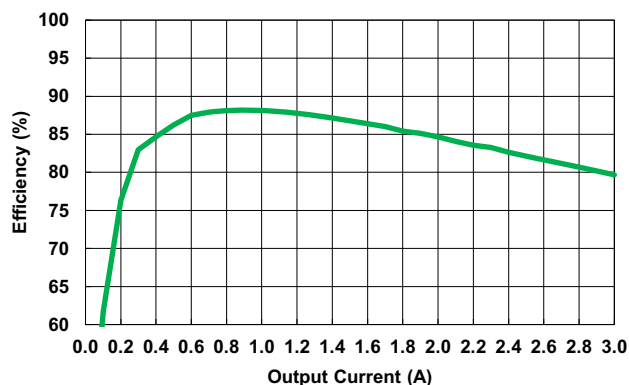


Figure 12. Efficiency

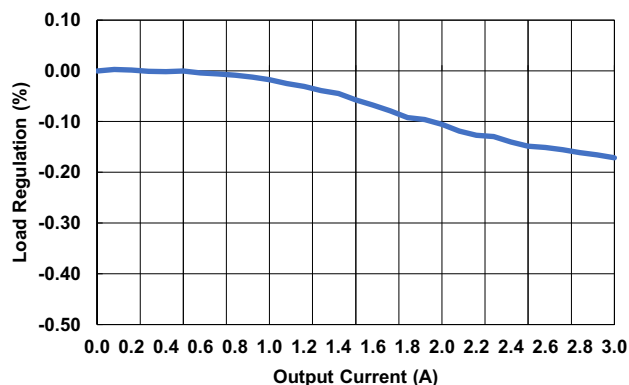


Figure 13. Load Regulation

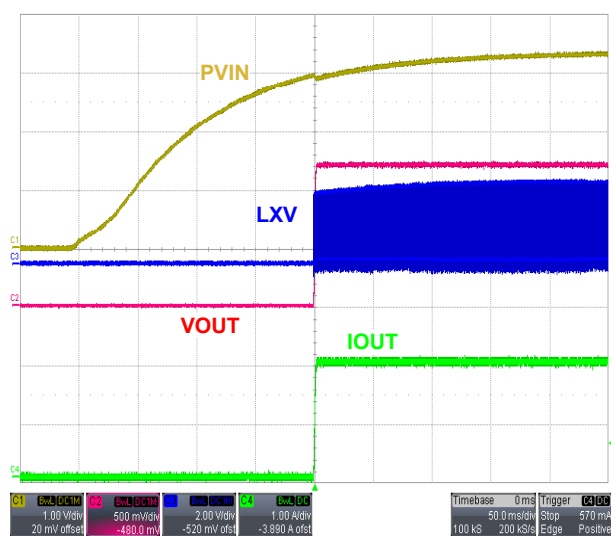


Figure 14. Turn-on, 0.6Ω load

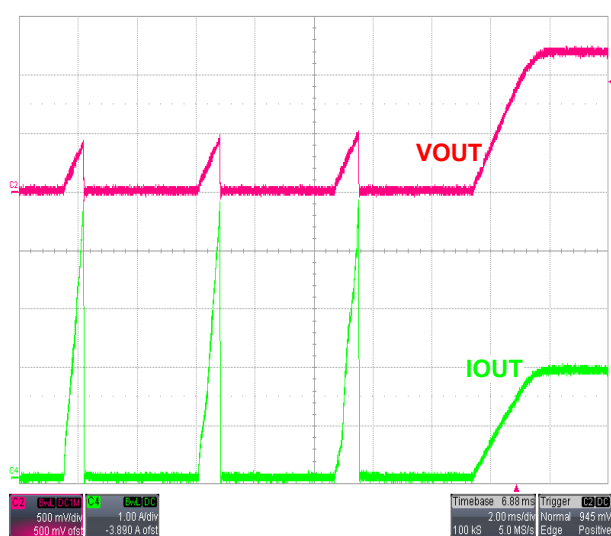


Figure 15. Over Current into Restart through Hiccup

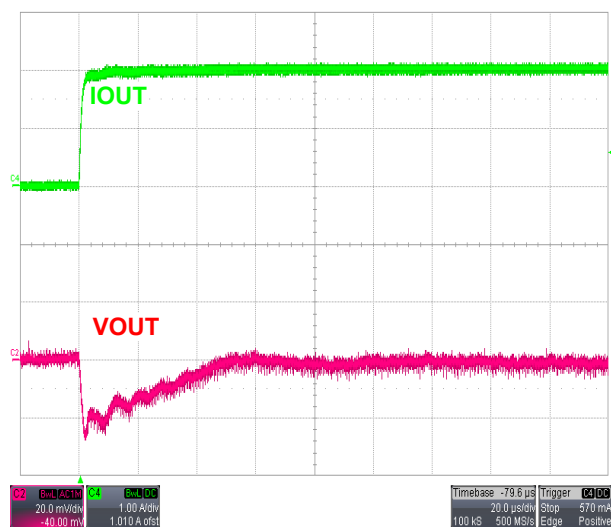


Figure 16. +2A Output Current Transient

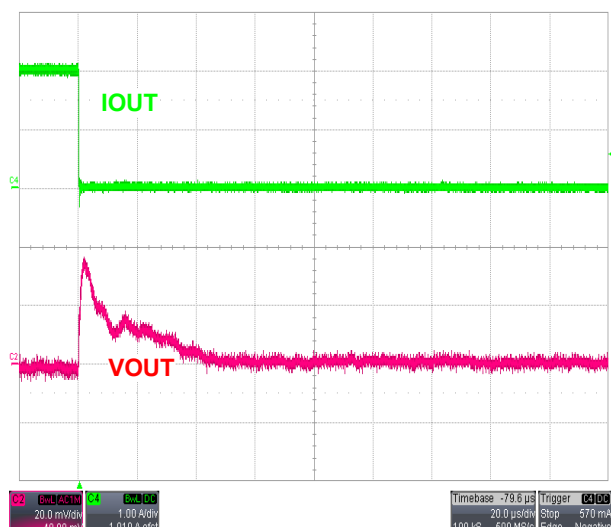


Figure 17. -2A Output Current Transient

Unless otherwise noted, $P_{VIN} = 3.3V$; $V_{OUT} = 1.2V$, $f_{SW} = 1MHz$, $T_A = \text{Room Ambient}$. Some results with modified board. (Cont.)

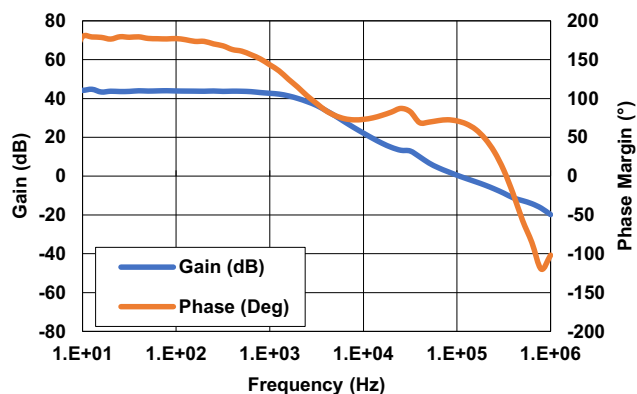


Figure 18. Gain/Phase BODE Plot, $I_{OUT} = 2A$

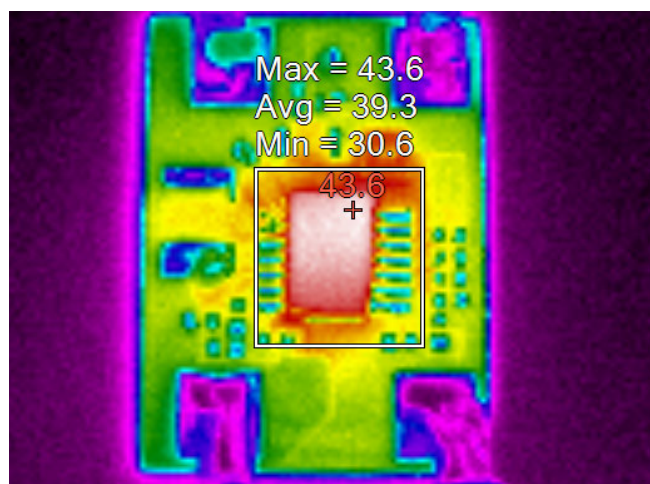


Figure 19. Package Temperature at $I_{OUT} = 3A$

4. Ordering Information

Part Number	Description
ISL73007SEHEV2Z	Radiation Hardened ISL73007SEH buck regulator evaluation board

5. Revision History

Revision	Date	Description
1.00	Mar 23, 2022	Initial release

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