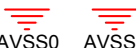


R7S910018 Evaluation board RTK7910018C00000BE SCHEMATICS (RZ/T1, BGA320)

TITLE	PAGE
Index	1
RZ/T1-1	2
RZ/T1-2, Serial-flash	3
NOR-flash, SDRAM	4
EtherCAT	5
Audio-codec, USB, LAN	6
Serial(USB), DBGIF	7
DSMIF, PMOD, CAN, SW	8
Power	9
Application Header	10

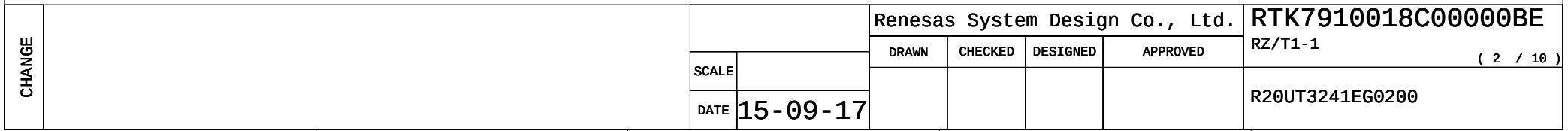
Note:

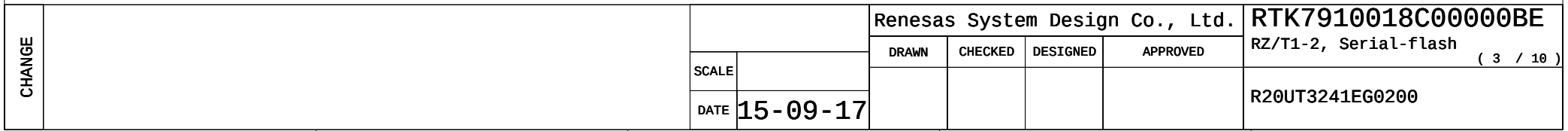
-  Digital GND (VSS)
-  Digital GND for USB
-  Analog GND
-  Analog GND for ADC
-  Analog GND for ADC Reference
-  Analog GND for PLL
-  Not mounted (Do Not Fit)

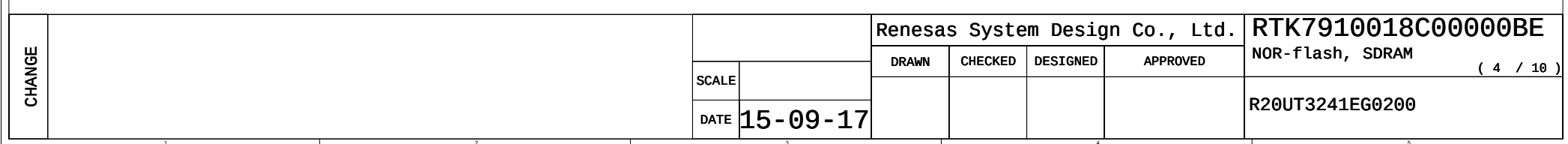
VIN = System Power (5V or 7V-12V input)
D5V = Digital 5V
D3.3V = Digital 3.3V for External device
A3.3V = Analog 3.3V for External device
D1.2V = Digital 1.2V
A1.2V = Analog 1.2V
VCCQ33 = 3.3V for RZ/T1 I/O & USB
AVCC = Analog 3.3V for ADC
VDD = Digital 1.2V for RZ/T1 Core
AVDD = Analog 1.2V for PLL & USB

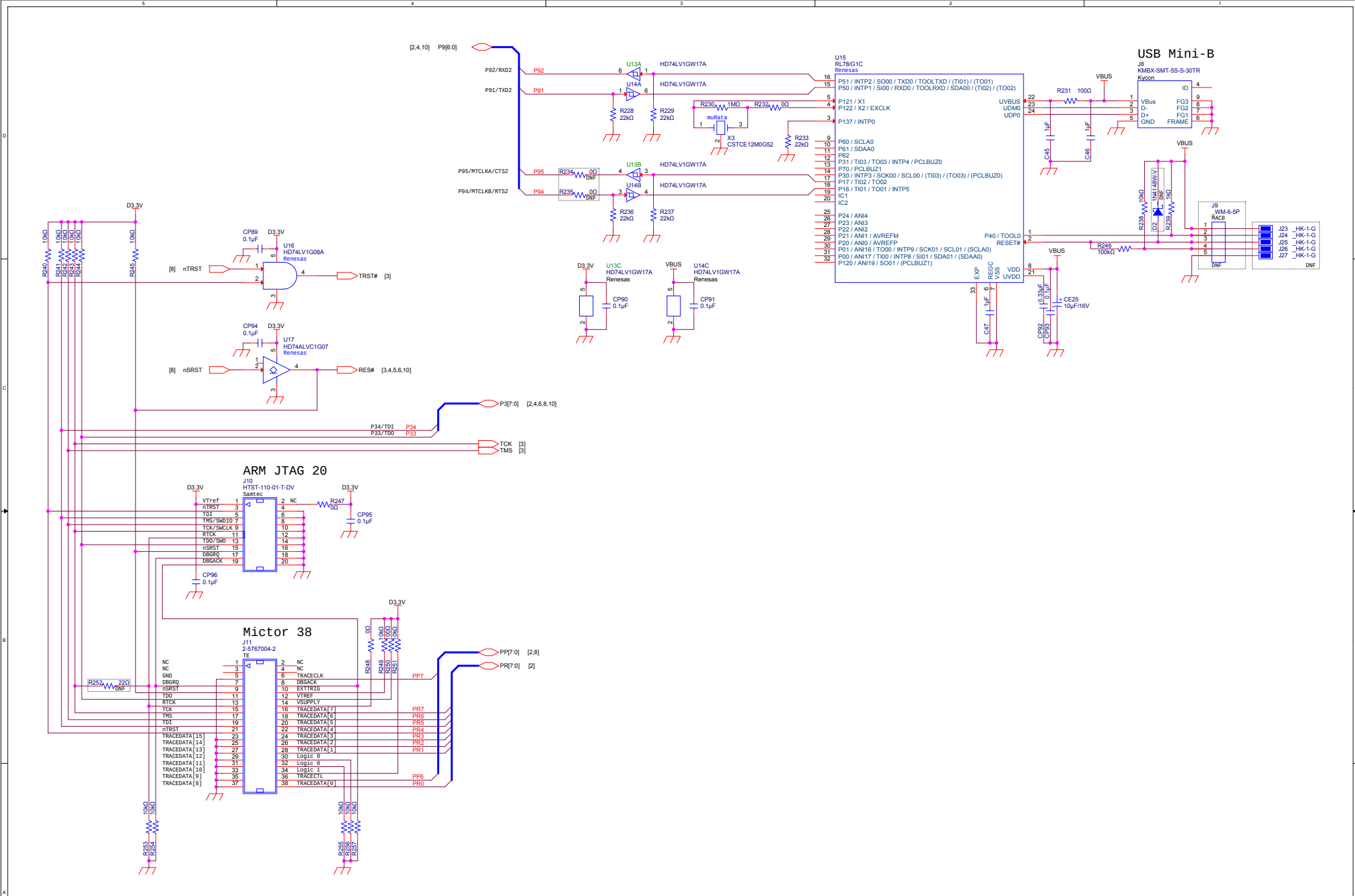
R = Fixed Resistors
RA = Resistor Array
C = Ceramic Caps
CE = Tantalum Electrolytic Caps
CP = Decoupling Caps

CHANGE				Renesas System Design Co., Ltd.				RTK7910018C00000BE
		SCALE		DRAWN	CHECKED	DESIGNED	APPROVED	INDEX (1 / 10)
		DATE	15-09-17					R20UT3241EG0200



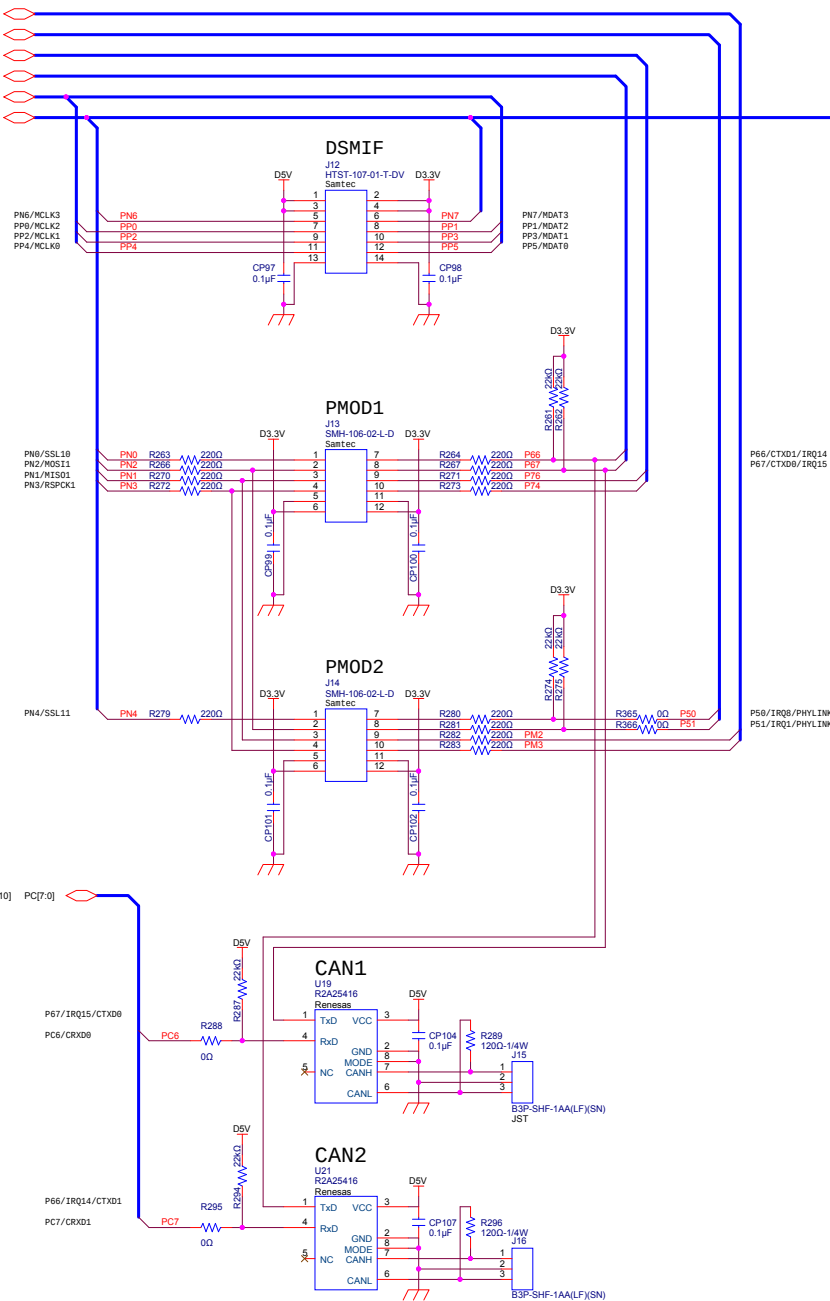




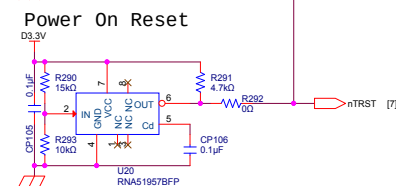
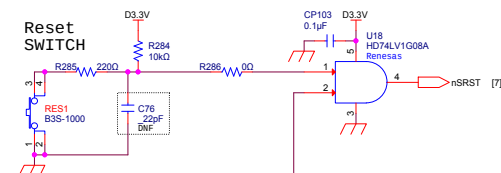
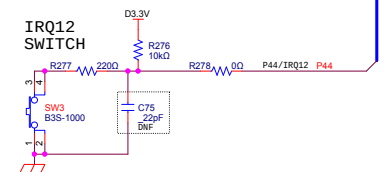
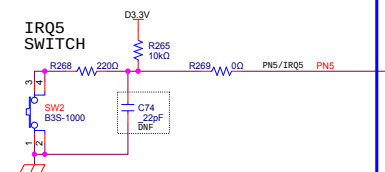
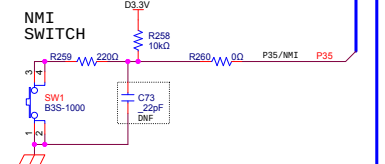


CHANGE				Renesas System Design Co., Ltd.				RTK7910018C00000BE
				DRAWN	CHECKED	DESIGNED	APPROVED	Serial (USB), DBGIF
								(7 / 10)
		SCALE						
		DATE	15-09-17					R20UT3241EG0200

[2.5,6] PM[7:0]
[2.5] P5[4:0]
[2.10] P7[7:0]
[2.3,10] P6[7:0]
[2.7] PP[7:0]
[2.6,10] PN[7:0]



[2.4,6,10] P4[7:0]
[2.4,6,7,10] P3[7:0]



CHANGE

SCALE

DATE 15-09-17

Renesas System Design Co., Ltd.

DRAWN

CHECKED

DESIGNED

APPROVED

RTK7910018C00000BE

DSMIF, PMOD, CAN, SW

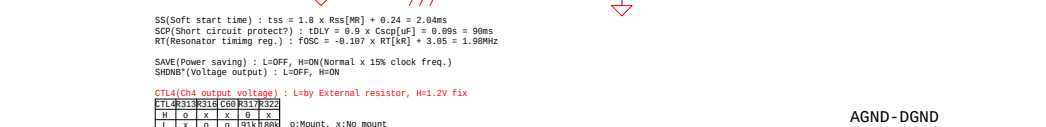
(8 / 10)

R20UT3241EG0200

J17

1. 2.7V, 12V, 48V, 120V, 240V

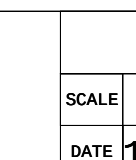
JP2



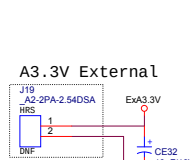
MH1



R325 00

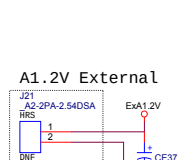


J18
_A2-2PA-2.54DSA ExD3.3

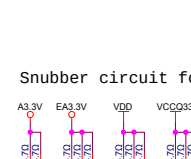


J19
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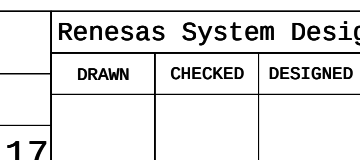
J20
A2-2PA-2.54DSA



J21
_A2-2PA-2.54DSA



A3.3V EA3.3V VDD VCCQ33 D1.2V VBUS A1.2V



CHANGE				Renesas System Design Co., Ltd.				RTK7910018C000000BE
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