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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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Evaluation Board Information

μ PC8178TB **1.0 GHz Silicon MMIC Evaluation Board**

- Evaluation Board Pattern Layout
- Circuit Description
- Circuit Current and Power Gain Data
- 1 dB Gain Compression Output Power Data
- Isolation Data
- Input and Output Return Loss Data

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The characteristics of high-frequency devices in particular vary depending on the external components and mounting pattern used.

Customers are requested to confirm all characteristics when designing a system based in part or wholly on the information in this document.

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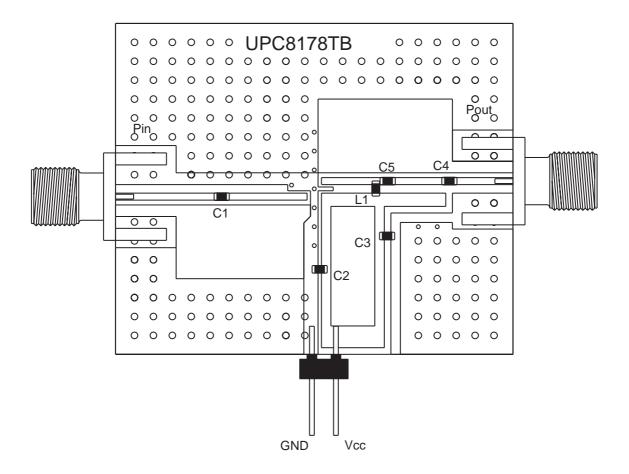
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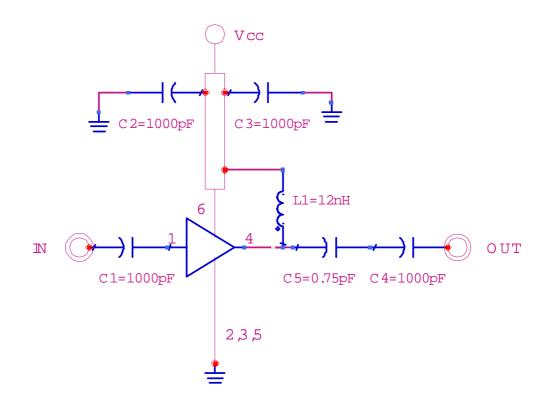
Evaluation Board Pattern Layout

uPC8178TB f = 1.0 GHz



Circuit Description

uPC8178TB f=1.0GHz



KC-8178TB

@ f = 1.0 GHz, Vcc = 3.0 V

Symbol	lcc	GP	PO(1 dB)	ISL	RLin	RLout
Unit	mA	dB	dBm	dB	dB	dB
Conditions	No signal	P _{in} = -30 dBm	_	Pin = -30 dBm	Pin = -30 dBm	$P_{in} = -30 \text{ dBm}$
TYP.	1.90	11.0	-4.0	39.0	7.0	—
1	1.76	11.2	-4.8	37.2	7.0	18.8
2	1.75	10.9	-5.5	38.0	7.3	10.5
3	1.77	11.2	-4.6	36.4	7.1	17.2
4	1.75	11.1	-5.0	37.8	6.8	19.3
5	1.77	10.9	-5.7	37.6	7.3	10.0
AVE.	1.78	11.1	-4.9	37.7	7.1	15.1

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