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瑞萨电子公司网址：<http://www.renesas.com>

2010年4月1日
瑞萨电子公司

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低电压 CMOS 逻辑 HD74ALVC 系列 封装的热阻

1. TSSOP 的热阻

在 HD74ALVC 系列中，TSSOP 的减额曲线以及此时的热阻 (θ_{j-a}) 分别如图 1 和表 1 所示。

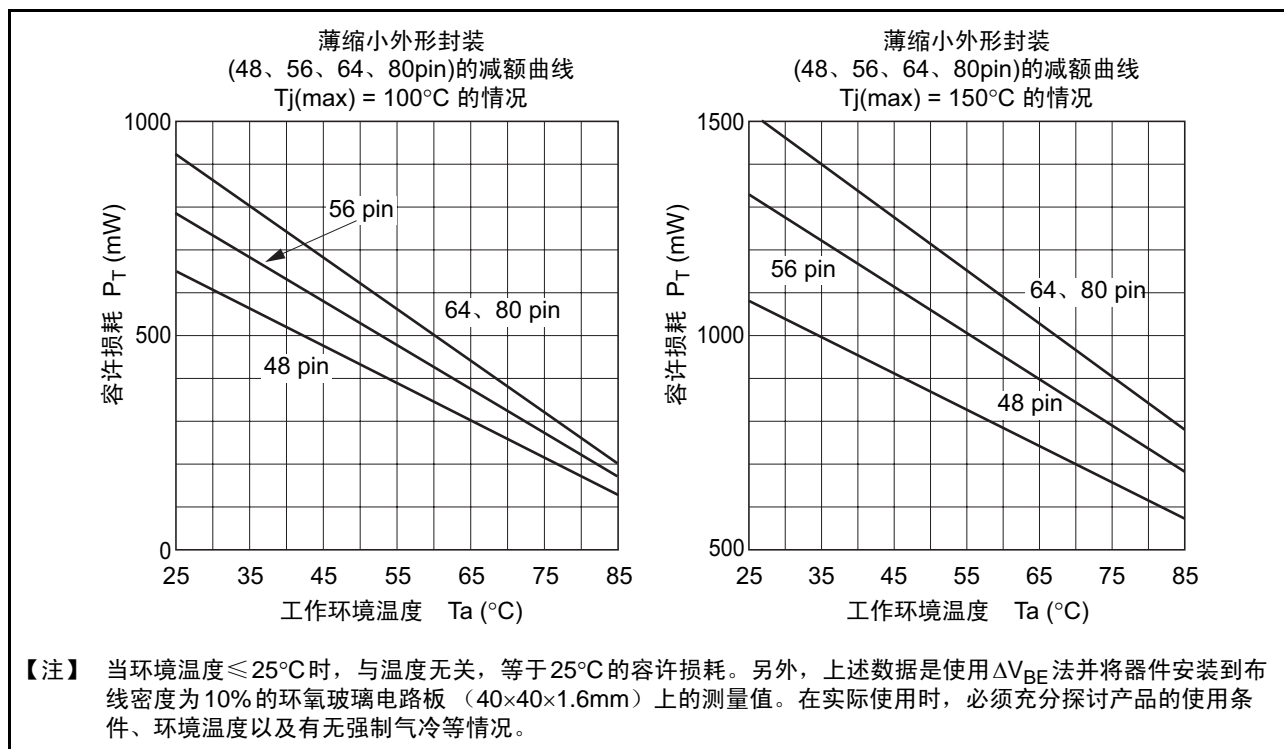


图 1 TSSOP 的减额曲线

表 1 TSSOP 封装的热阻

引脚个数	风速	减额因子	热阻	容许损耗 $T_a = 25^{\circ}\text{C}$	
				$T_j(\text{max})=150^{\circ}\text{C}$	$T_j(\text{max})=100^{\circ}\text{C}$
48	0m/s	$8.7\text{mW}/^{\circ}\text{C}$	$115^{\circ}\text{C}/\text{W}$	1087mW	652mW
56	0m/s	$10.5\text{mW}/^{\circ}\text{C}$	$95^{\circ}\text{C}/\text{W}$	1316mW	790mW
64	0m/s	$12.1\text{mW}/^{\circ}\text{C}$	$82^{\circ}\text{C}/\text{W}$	1512mW	907mW
80	0m/s	$12.1\text{mW}/^{\circ}\text{C}$	$82^{\circ}\text{C}/\text{W}$	1512mW	907mW

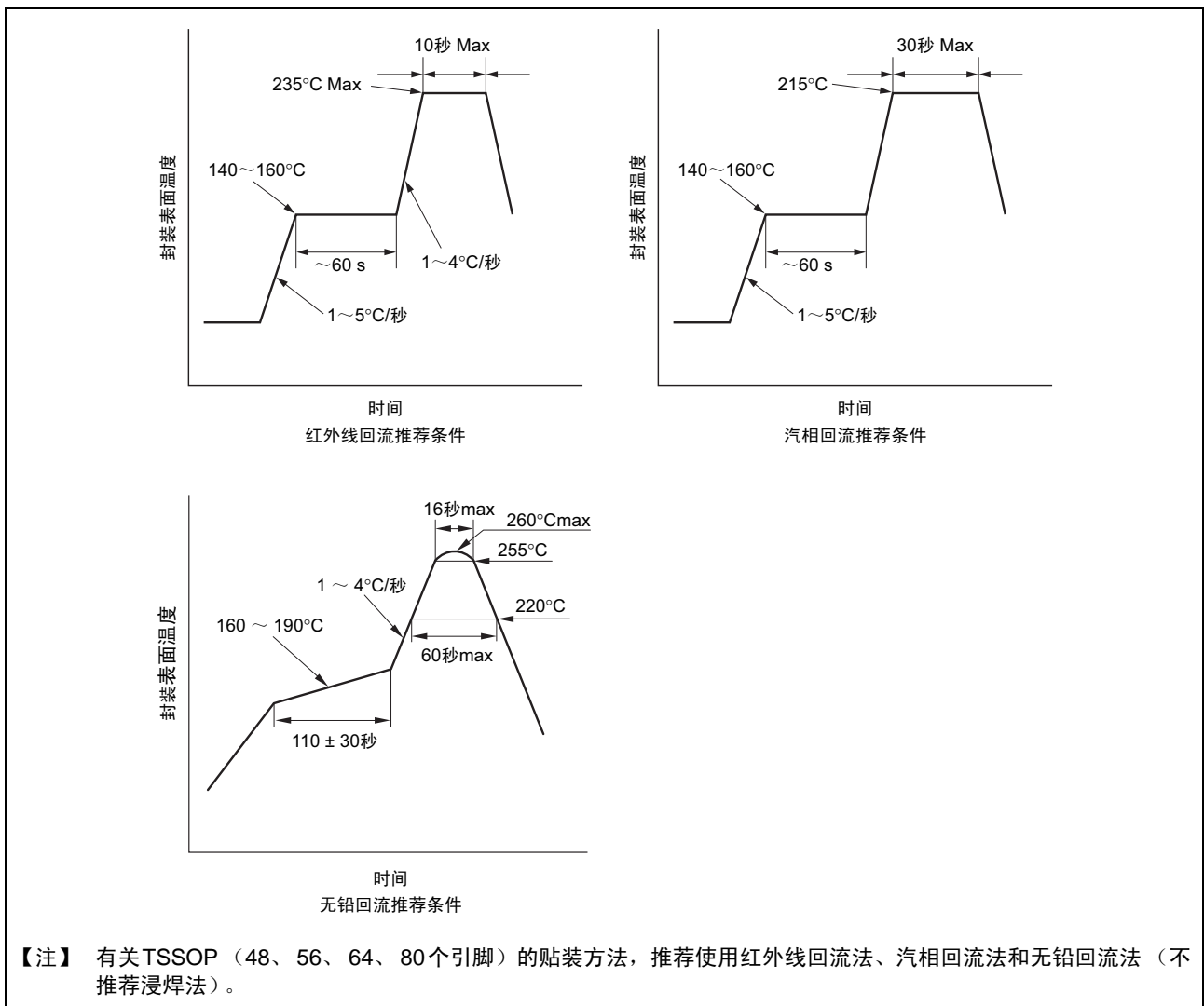
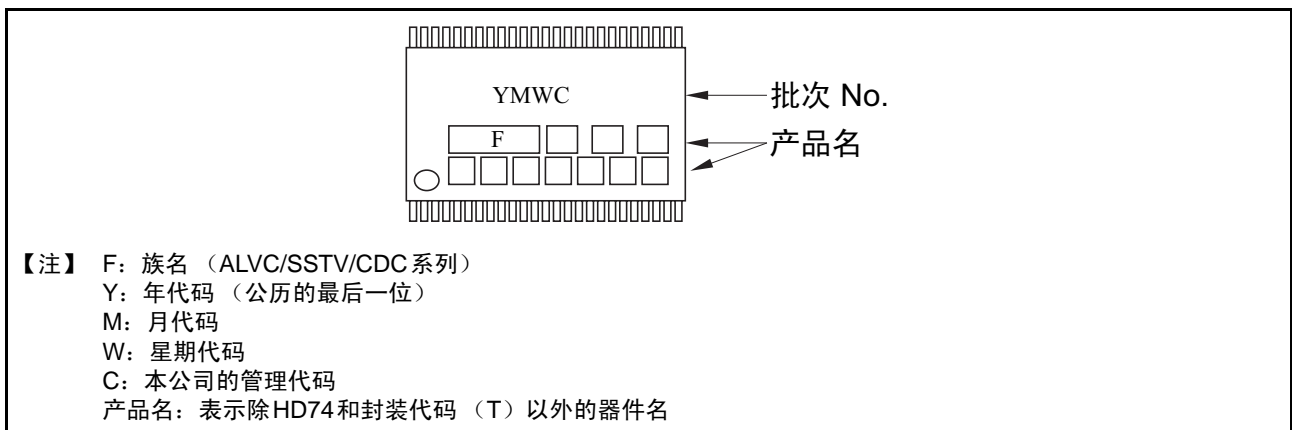


图2 TSSOP的贴装方法

2. 封装上的标记



修订记录

Rev.	发行日	修订内容	
		页	修订处
1.00	2008.03.25	一	初版发行

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