### Old Company Name in Catalogs and Other Documents

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <a href="http://www.renesas.com">http://www.renesas.com</a>

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<a href="http://www.renesas.com">http://www.renesas.com</a>)

Send any inquiries to http://www.renesas.com/inquiry.



|--|

# 740 FAMILY MASK ROM CONFIRMATION FORM SINGLE-CHIP MICROCOMPUTER M37151MF-XXXFP RENESAS

ſ		Date:	
	₹	Section head signature	Supervisor signature
	Receipt		
	æ		

Note: Please fill in all items marked \*.

		Company		TEL		Submitted by	Supervisor
*	Customer	name		( )	ance ature		
-		Date issued	Date :		Issua		

#### \*1. Confirmation

Specify the name of the product being ordered.

Three EPROMs are required for each pattern if this order is performed by EPROMs.

One floppy disk is required for each pattern if this order is performed by a floppy disk.

-1	Ordering	h,	EDDOM:
	 roraenna	υv	EPROIVIS

If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM (hexadecimal notation)

EPROM type (indicate the type used)

27C101									
EPROM ac 000016 000F162 1000162	Product nameASCII code: ///3735/MF Data ROM								
FFFF16,	(60K) (SDROM								
13BFF <sub>16</sub>									

(1) Set "FF16" in the shaded area.

### 740 FAMILY MASK ROM CONFIRMATION FORM SINGLE-CHIP MICROCOMPUTER M37151MF-XXXFP **RENESAS**

(2)	Write the ASCII codes that indicate the product name of "M37151MF-" to addresses 000016 to 000F16.										
	Addresses 0000 <sub>16</sub> to 000F <sub>16</sub> store the pro	oduct name.	Address								
	ASCII codes 'M37151MF-' are listed on t	he right.	000016	'M' = 4D <sub>16</sub>	000816	'-' = 2D <sub>16</sub>					
	The addresses and data are in hexadecia	mal notation.	000116	'3' = 33 <sub>16</sub>	6 000916						
			000216	'7' = <b>37</b> 16	000A <sub>16</sub>	FF <sub>16</sub>					
			000316	'1' = 31 <sub>16</sub>	000B <sub>16</sub>	FF <sub>16</sub>					
Note:	If the name of the product contained in the	e EPROMs does	000416	'5' = <b>35</b> 16	000C <sub>16</sub>	FF <sub>16</sub>					
	not match the name on the mask ROM co	onfirmation form,	000516	'1' = 31 <sub>16</sub>	000D16	FF <sub>16</sub>					
	the ROM processing is disabled. Please	e make sure the	000616	'M' = 4D <sub>16</sub>	000E16	FF <sub>16</sub>					
	data is written correctly.		000716	'F' = 46 <sub>16</sub>	000F <sub>16</sub>	FF <sub>16</sub>					
respor care m The su	ill produce masks based on the mas nsibility for errors only if the mask ROI nust be taken to verify the mask file in ubmitted floppy disk must be 3.5-inch appy disk.	M data on the pro	oducts we pro loppy disk.	oduce differs fron	n this mask file	Thus, extreme					
	File code Mask file name				nal notation) al or less than e	ight characters					
2. Mark	specification										
N.A L -	en esta esta esta esta esta esta esta esta	(l ( <b>.</b>		. ( )		0					

#### \*2

Mark specification must be submitted using the correct form for the type of package being ordered. Fill the appropriate mark specification form (42P2R for M37151MF-XXXFP) and attach to the mask ROM confirmation form.

#### **\***3. Comments

# 740 FAMILY MASK ROM CONFIRMATION FORM SINGLE-CHIP MICROCOMPUTER M37151MF-XXXFP RENESAS

Font data must be stored in the proper OSD ROM address according to the following table.

#### OSD ROM address of character font data

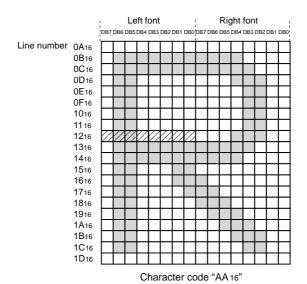
OSD ROM address bit	AD16	AD15	AD14	AD13	AD12	AD11	AD10	AD9	AD8	AD7	AD6	AD5	AD4	AD3	AD2	AD1	AD0
Line number / Character code / Font bit	1	0	0		Line number						Cha	racter	code				Font bit

Line number = 0A16 to 1D16

Character code = 0016 to FF16 (Do not set 7F16 and 8016.)

Font bit = 0 : Left font 1 : Right font

Example) The font data "60" (shaded area  $\bigcirc$ ) of the character code "AA 16" is stored in address 1 0 0 1 0 1 0 1 0 1 0 1 0 0 0 2 =1255416.



Notes 1: The 80-byte addresses corresponding to the character code "7F16" and "8016" in OSD ROM are the test data storning area. Set "FF16" to the area (We stores the test data to this area and the different data from "FF16" is stored for the actual products.)

The test data storing area:

addresses 11000 16+(4+2n)X10016+FE16 to1100016+(5+2n)X10016+0116(n=0 to 19)

addresses 114FE 16 to 11501 16 addresses 116FE 16 to 11701 16 ... addresses 138FE 16 to 13901 16 addresses 13AFE 16 to 13B01 16

2: The character code "0916" is used for "transparent space" when displaying Closed Caption. Therefore, set "0016" to the 40-byte addresses corresponding to the character code "0916." The transparent space font data storing area: addresses 11000 16+(4+2n)×10016+1216 to 1100016+(4+2n)×10016+1316 (n=0 to 19)

addresses 11412 16 and 11413 16 addresses 11612 16 and 11613 16 . addresses 13812 16 and 13813 16 addresses 13A12 16 and 13A13 16