

Option List HD6432126R,HD6432127R

Please check off the appropriate applications by , or within and enter the necessary information.

(1) ROM size

- | | |
|--------------------------|---------------------------|
| <input type="checkbox"/> | HD6432126R(W) : 32-kbytes |
| <input type="checkbox"/> | HD6432127R(W) : 64-kbytes |

(2) System Oscillator(OSC1,OSC2)

- | | | | |
|--------------------------|--------------------|-----|-----|
| <input type="checkbox"/> | Crystal oscillator | f = | MHz |
| <input type="checkbox"/> | External clock | f = | MHz |

Date of order	
Customer	
Department	
Name	
ROM code Name	
LSI number (Renesas entry)	

(3) Power Supply Voltage/Maximum Operating Frequency

- | | |
|--------------------------|--|
| <input type="checkbox"/> | V _{cc} = 4.5V to 5.5V (20 MHz max.) |
| <input type="checkbox"/> | V _{cc} = 4.0V to 5.5V (16 MHz max.) |
| <input type="checkbox"/> | V _{cc} = 2.7V to 5.5V (10 MHz max.) |
| <input type="checkbox"/> | V _{cc} = 4.0V to 5.5V (10 MHz max, Low EMI Noise version. Only Single-Chip Mode.) |

Notes 1. Please select the power supply voltage/operating frequency version according to the power supply voltage used.

Example: For use at V_{cc} = 4.5V to 5.5V/f = 10MHz
select V_{cc} = 4.5V to 5.5V(20MHz max.).

2. The power supply voltage and maximum operating frequency of the selected version should also be entered on the Single-Chip Microcomputer Ordering Specifications sheet.

(4) I²C Bus Option

- | | |
|--------------------------|-------------------------------|
| <input type="checkbox"/> | I ² C bus used |
| <input type="checkbox"/> | I ² C bus not used |

Notes 1. The "I²C bus used" option includes all cases where data transfer is performed via the SCL and SDA pins using the on-chip I²C bus interface function (hardware module). If the I²C bus interface function (hardware module) is used, various bus interface with different bus specifications and names are also included in "I²C bus used".

2. When "I²C bus not used" is selected, values cannot be set in registers relating to the I²C bus interface (ICCR,ICSR,ICDR,ICMR). These register always read H'FF. With emulators, ZTAT and F-ZTAT versions, the "I²C bus used" option is selected. If the "I²C bus not used" option is selected, it is essential to ensure that "I²C bus interface related registers are not accessed.

For the Microcomputer Family item in 1.Basic Specifications in the Single-Chip Microcomputer Ordering Specifications Sheet *, please specify the appropriate item from the table below according to the combination of items 1 and 4 above. If the "I²C bus used" option is selected, please also specify this in Special Specifications(Product Specifications, Mark Specifications, etc....) in 1.Basic specifications.

* Please contact the relevant sales department for information on the Single-Chip Microcomputer Ordering Specifications Sheet.

	I ² C	I ² C bus used	I ² C bus not used
ROM Size			
32-kbytes		HD6432126RW	HD6432126R
64-kbytes		HD6432127RW	HD6432127R