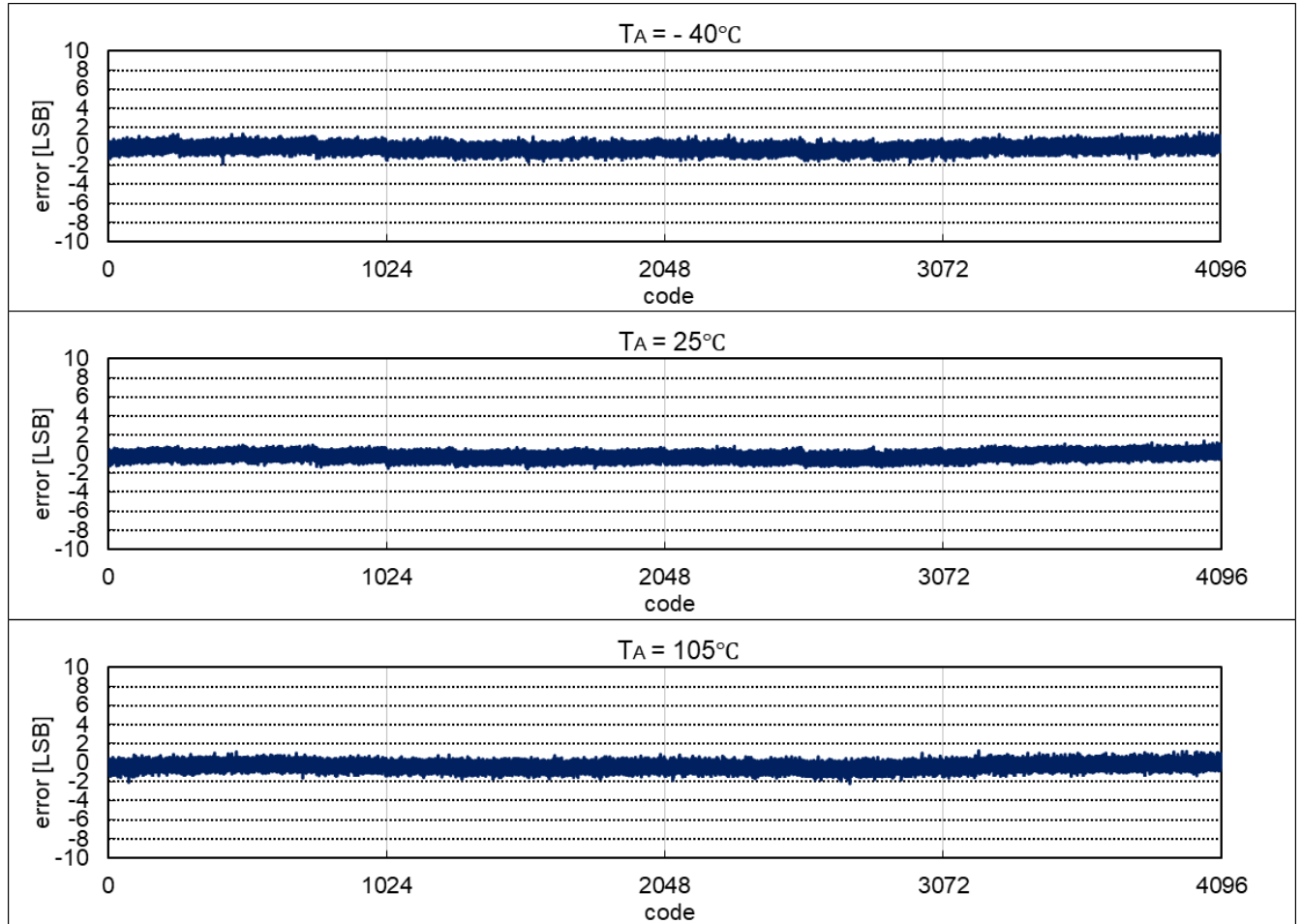


$V_{DD} = 5.0\text{ V}$   
 $T_A = -40^\circ\text{C}, +25^\circ\text{C}, +105^\circ\text{C}$   
CPU : HS mode, RUN  
 $f_{CLK} = 32\text{ MHz}$  (High-speed OCO)  
  
reference voltage (+) =  $AV_{REFP} = 5.0\text{ V}$ , reference voltage (-) =  $AV_{REFM} = 0\text{ V}$   
 $f_{AD} = 32\text{ MHz}$   
conversion time =  $2.0625\text{ }\mu\text{s}$   
mode : Normal mode 1

Prepared on August 27th, 2025

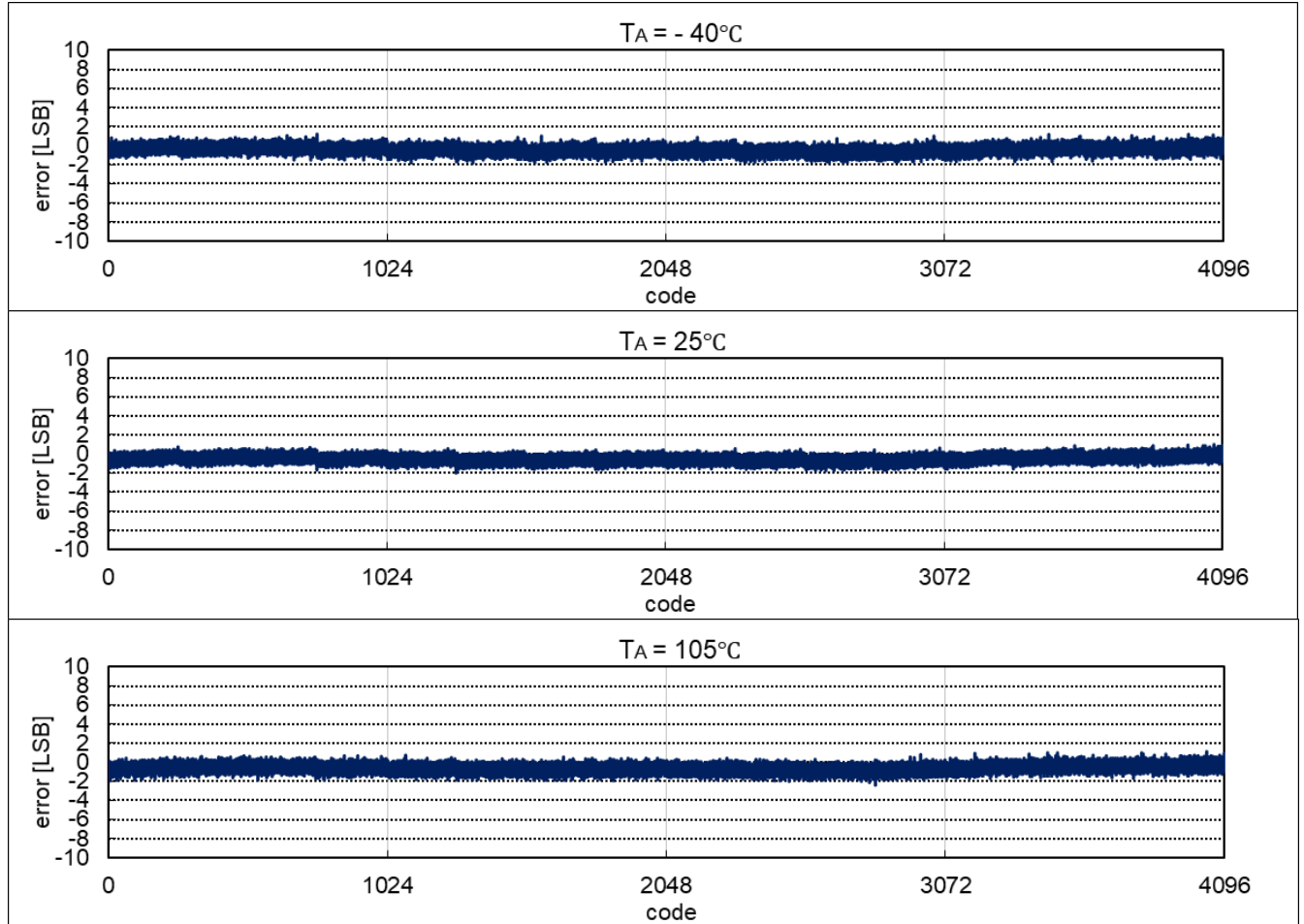


The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

$V_{DD} = 5.0\text{ V}$   
 $T_A = -40^\circ\text{C}, +25^\circ\text{C}, +105^\circ\text{C}$   
CPU : HS mode, HALT  
 $f_{CLK} = 32\text{ MHz}$  (High-speed OCO)

reference voltage (+) =  $AV_{REFP} = 5.0\text{ V}$ , reference voltage (-) =  $AV_{REFM} = 0\text{ V}$   
 $f_{AD} = 32\text{ MHz}$   
conversion time =  $2.0625\text{ }\mu\text{s}$   
mode : Normal mode 1

Prepared on August 27th, 2025

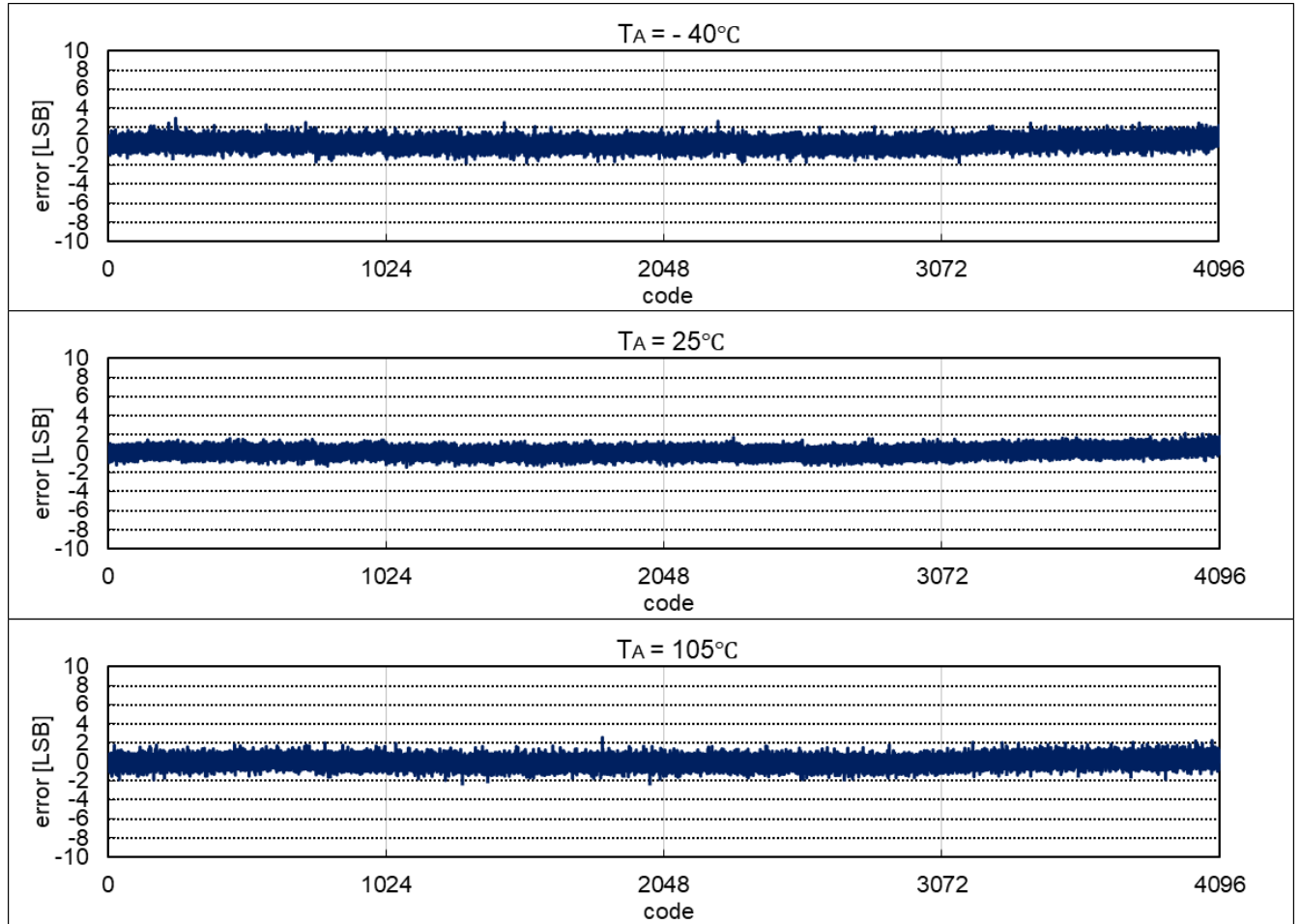


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$V_{DD} = 3.0\text{ V}$   
 $T_A = -40^\circ\text{C}, +25^\circ\text{C}, +105^\circ\text{C}$   
CPU : HS mode, RUN  
 $f_{CLK} = 32\text{ MHz}$  (High-speed OCO)

reference voltage (+) =  $AV_{REFP} = 3.0\text{ V}$ , reference voltage (-) =  $AV_{REFM} = 0\text{ V}$   
 $f_{AD} = 32\text{ MHz}$   
conversion time =  $2.0625\text{ }\mu\text{s}$   
mode : Normal mode 1

Prepared on August 27th, 2025

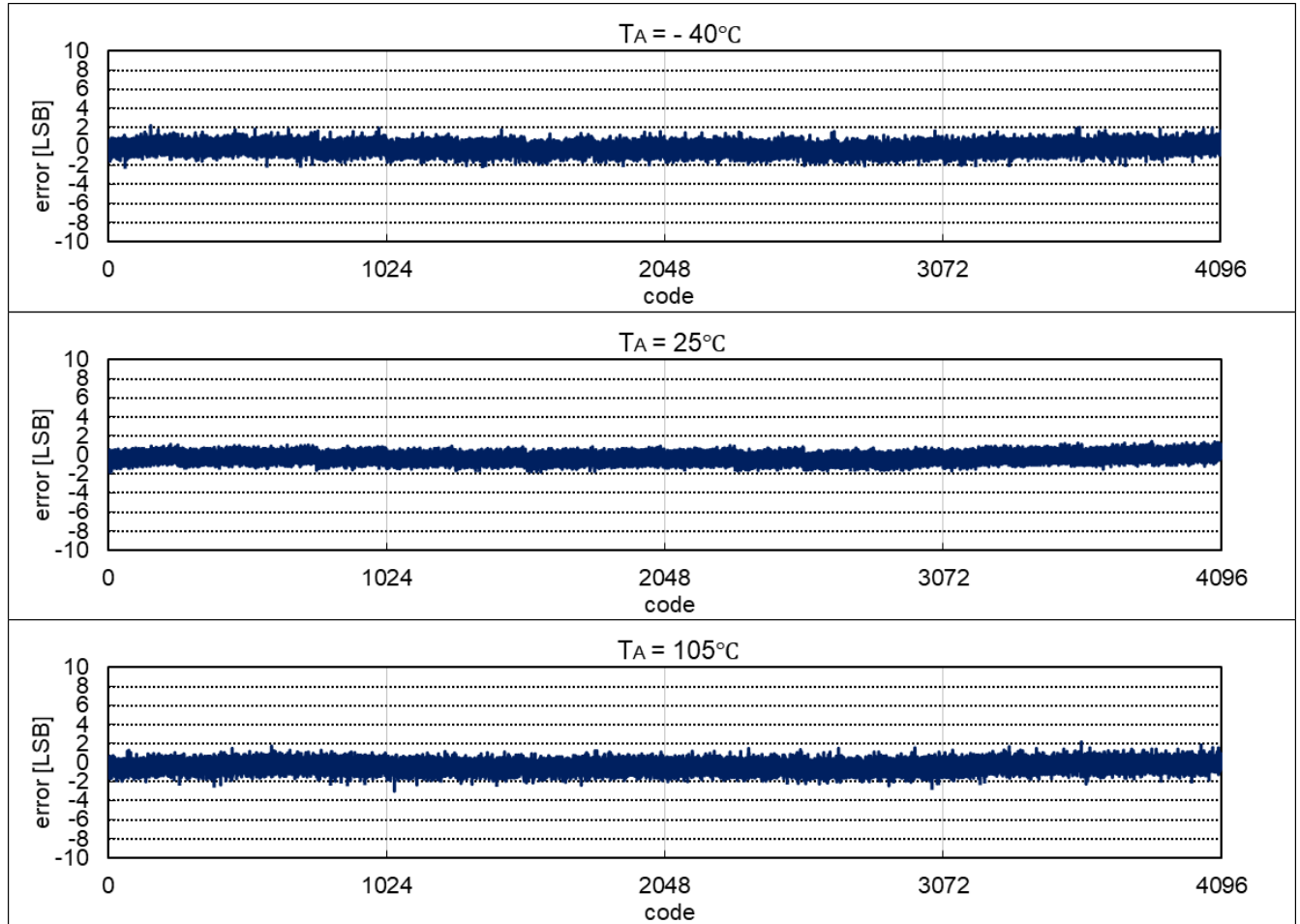


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$V_{DD} = 3.0\text{ V}$   
 $T_A = -40^\circ\text{C}, +25^\circ\text{C}, +105^\circ\text{C}$   
CPU : HS mode, HALT  
 $f_{CLK} = 32\text{ MHz}$  (High-speed OCO)

reference voltage (+) =  $AV_{REFP} = 3.0\text{ V}$ , reference voltage (-) =  $AV_{REFM} = 0\text{ V}$   
 $f_{AD} = 32\text{ MHz}$   
conversion time =  $2.0625\text{ }\mu\text{s}$   
mode : Normal mode 1

Prepared on August 27th, 2025

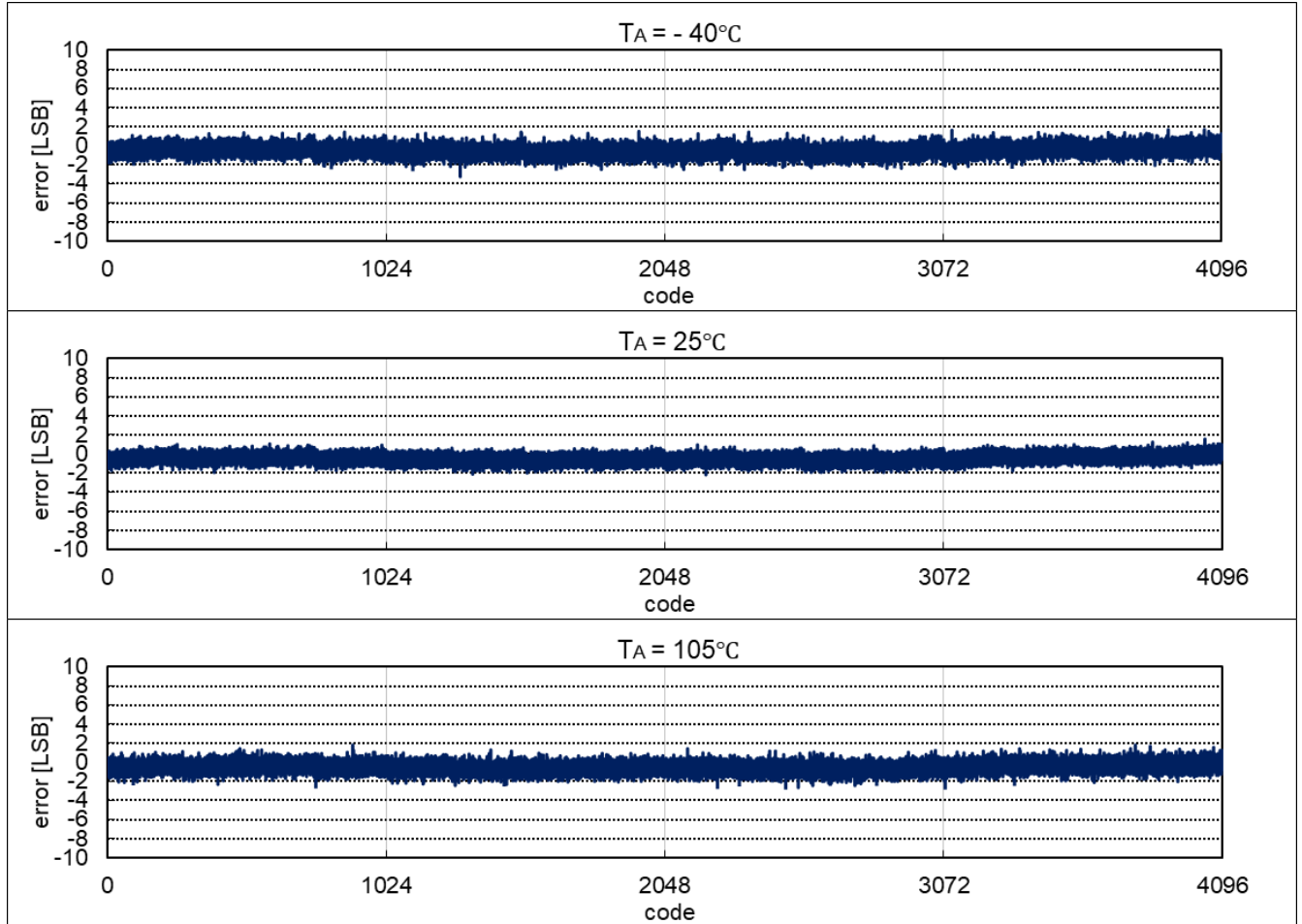


The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

$V_{DD} = 3.0\text{ V}$   
 $T_A = -40^\circ\text{C}, +25^\circ\text{C}, +105^\circ\text{C}$   
CPU : LS mode, RUN  
 $f_{CLK} = 24\text{ MHz}$  (High-speed OCO)

reference voltage (+) =  $AV_{REFP} = 3.0\text{ V}$ , reference voltage (-) =  $AV_{REFM} = 0\text{ V}$   
 $f_{AD} = 24\text{ MHz}$   
conversion time =  $2.75\text{ }\mu\text{s}$   
mode : Normal mode 1

Prepared on August 27th, 2025

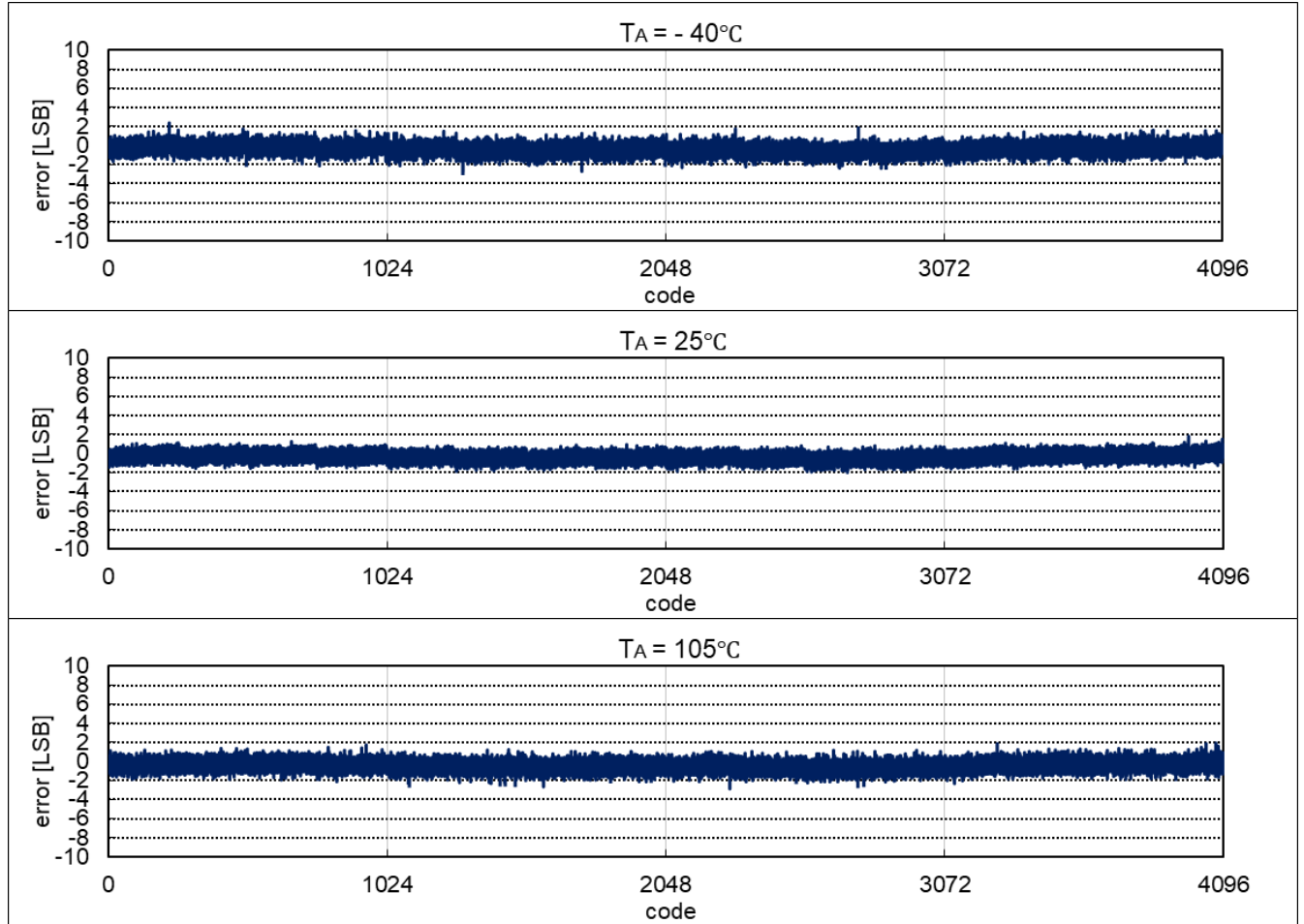


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$V_{DD} = 3.0\text{ V}$   
 $T_A = -40^\circ\text{C}, +25^\circ\text{C}, +105^\circ\text{C}$   
CPU : LS mode, HALT  
 $f_{CLK} = 24\text{ MHz}$  (High-speed OCO)

reference voltage (+) =  $AV_{REFP} = 3.0\text{ V}$ , reference voltage (-) =  $AV_{REFM} = 0\text{ V}$   
 $f_{AD} = 24\text{ MHz}$   
conversion time =  $2.75\text{ }\mu\text{s}$   
mode : Normal mode 1

Prepared on August 27th, 2025

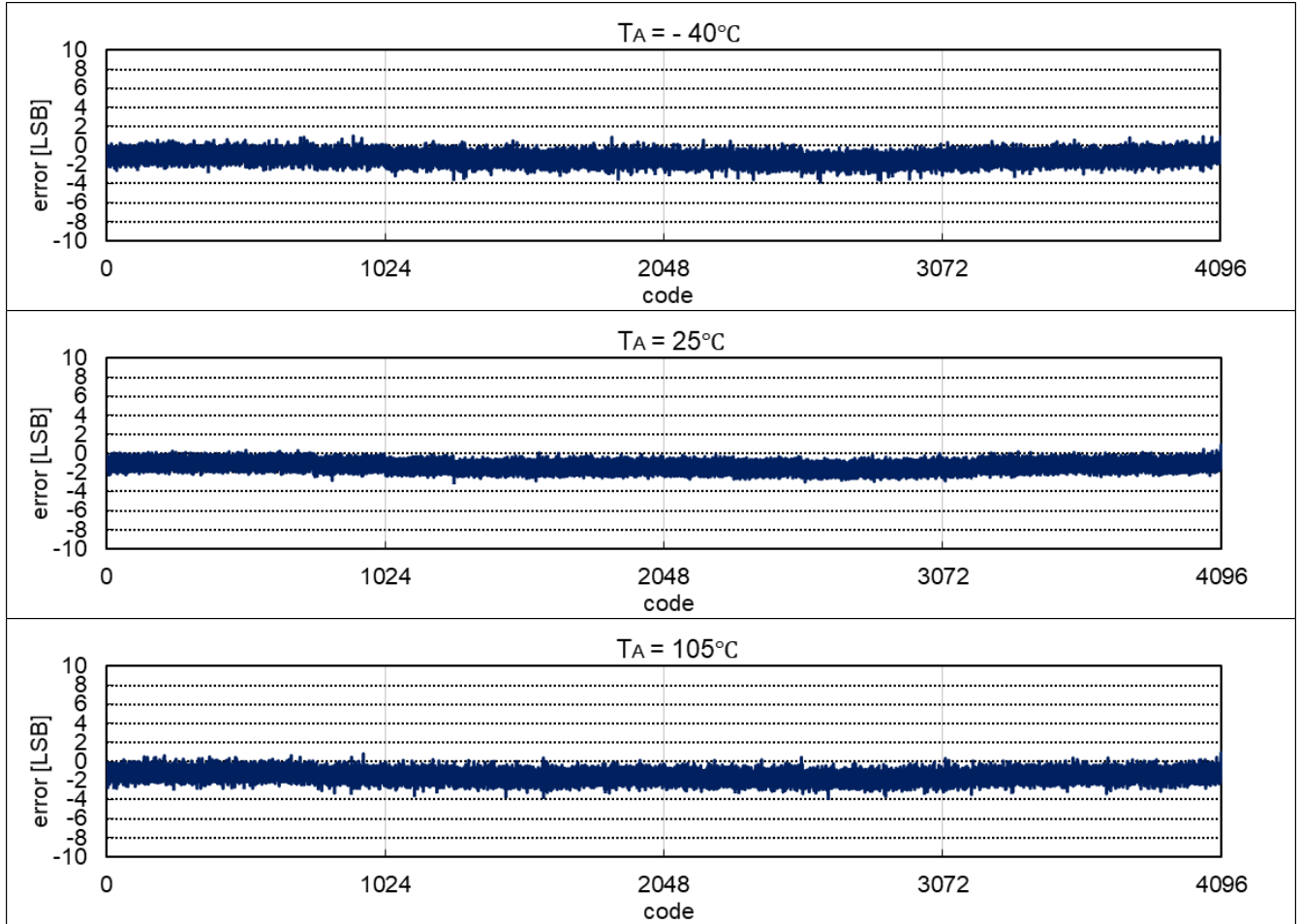


The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

$V_{DD} = 3.0\text{ V}$   
 $T_A = -40^\circ\text{C}, +25^\circ\text{C}, +105^\circ\text{C}$   
CPU : LS mode, RUN  
 $f_{CLK} = 8\text{ MHz}$  (High-speed OCO)

reference voltage (+) =  $AV_{REFP} = 3.0\text{ V}$ , reference voltage (-) =  $AV_{REFM} = 0\text{ V}$   
 $f_{AD} = 8\text{ MHz}$   
conversion time =  $8.25\text{ }\mu\text{s}$   
mode : Normal mode 1

Prepared on August 27th, 2025

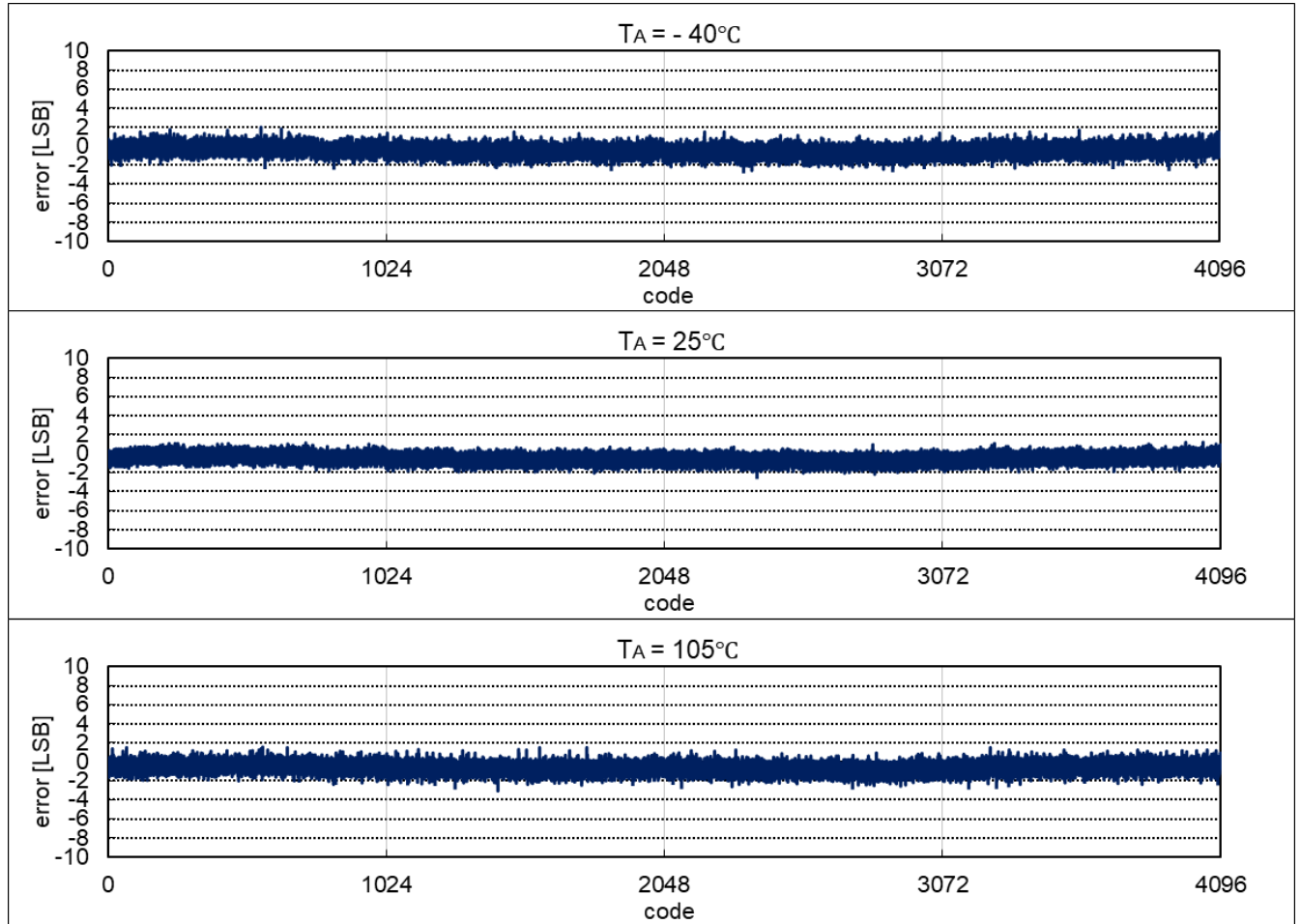


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$V_{DD} = 3.0\text{ V}$   
 $T_A = -40^\circ\text{C}, +25^\circ\text{C}, +105^\circ\text{C}$   
CPU : LS mode, HALT  
 $f_{CLK} = 8\text{ MHz}$  (High-speed OCO)

reference voltage (+) =  $AV_{REFP} = 3.0\text{ V}$ , reference voltage (-) =  $AV_{REFM} = 0\text{ V}$   
 $f_{AD} = 8\text{ MHz}$   
conversion time =  $8.25\text{ }\mu\text{s}$   
mode : Normal mode 1

Prepared on August 27th, 2025



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.