To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

ROM number	

QzROM PROGRAMMING CONFIRMATION FORM
SINGLE-CHIP 8-BIT MICROCOMPUTER
M38039G4H-XXXKP/HP
RENESAS TECHNOLOGY

Receipt	Date:	
	Section head signature	Supervisor signature

Note : Please fill in all items marked ∗.

∗	Customer	Company name		Issuance signature	Supervisor
		Telephone number	( )		
		Date issued	Date:		

∗1. Confirmation

Specify the name of the product being ordered.
The submitted floppy disk must be 3.5-inch 2HD type and DOS/V format if this order is performed by a floppy disk.
And the number of the mask files must be 1 in one floppy disk.

Microcomputer name: ☐ M38039G4H-XXXKP ☐ M38039G4H-XXXHP

File code | | | | | | | | | (hexadecimal notation)

Mask file name | | | | | | | | | .MSK (equal or less than eight characters)

Note: Write data to only ROM data area (addresses $C080_{16}$ to $FFDA_{16}$, $FFDC_{16}$ to $FFFD_{16}$).
ROM option data area: Addresses 10_{16}

Notes (RENESAS ---> Customer)

Note 1 : ROM data confirmation request

QzROM programming will be processed based on the mask file generated by the mask file generating utility. Only in the case when ROM data programmed in the actual mass produced product differs from that of above mentioned mask file, Renesas takes the responsibility. There is no Engineering Sample, thus please confirm the ROM data at the receipt of the Initial product delivery.
Should you find any problem, please return immediately. Two weeks without technical error feedback towards Renesas will automatically be regarded as acceptance of products.

Note 2 : ROM option ("Mask option" written in the mask file converter MM)

Either of the following data should be set to the ROM option data address (10_{16}) of the mask file you have ordered. When you don't protect the ROM data, a third party can read out it.

When the ROM data of protect area1 ($C080_{16}$ to $EFFF_{16}$) is protected	**FE_{16}**	Address 10_{16}
When the ROM data of all area ($C080_{16}$ to $FFFD_{16}$) is protected	**00_{16}**	Address 10_{16}
When the ROM data is not protected	**FF_{16}**	Address 10_{16}

If you set except the above data or nothing at the ROM option data address (10_{16}), We can't generate the ROM data. Then we request to submit the data again.
When Renesas ships QzROM write products, we write the data in the ROM option address (10_{16}) to the actual ROM code protect address ($FFDB_{16}$).
Therefore, set FF_{16} to address $FFDB_{16}$ in the ROM data regardless of the presence or absence of a protect.
When data other than FF_{16} is set, we may ask that the ROM data be submitted again.

Note 3 : Mark specification

You can appoint the mark by the mark specification form. Without submitting the mark specification form, your mark will be a standard mark. Please fill out the 64P6U MARK SPECIFICATION FORM for the M38039G4H-XXXKP, the 64P6Q MARK SPECIFICATION FORM for the M38039G4H-XXXHP, and attach it when you submit the QzROM PROGRAMMING CONFIRMATION FORM. We can't deal with special font marking (customer's trademark etc.) in QzROM microcomputer.

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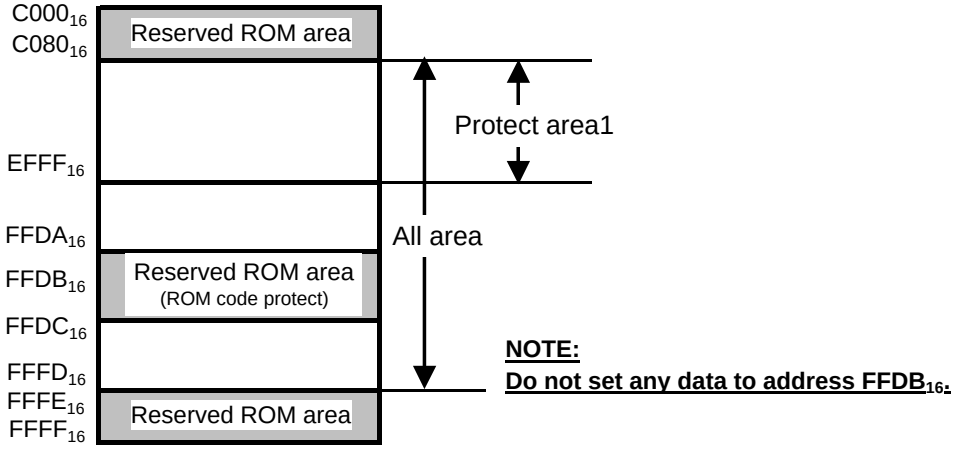
ROM-Protection-Area



∗2. Usage conditions

For our reference of new products, please reply to the following questions about the sage of the products you ordered.

(1) Which operation source main clock do you use?

☐ Ceramic resonator ☐ Quartz-crystal oscillation

☐ External clock input ☐ Other ( )

At what frequency? $f(X_{IN})$= MHz

(2) Which operation P41/X_{CIN} and P40/X_{COUT} do you use?

☐ P40, P41 Port funtion ☐ X_{CIN}-X_{COUT} clock function ☐ Not use

(3) What is the voltage of power supply (V_{CC}) you use?

Typ.= V Min.= V Max.= V

(4) What is the ambient temperature you use?

Typ.= ℃ Min.= ℃ Max.= ℃

(5) Which main clock division ratio mode do you use? (Except program start timing)

☐ High-speed mode ($f(\phi)=f(X_{IN})/2$)

☐ Middle-speed mode ($f(\phi)=f(X_{IN})/8$)

☐ Slow-speed mode ($f(\phi)=f(X_{CIN})/2$)

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(6) Which function do you use?

- ☐ Timer1 ☐ Timer2 ☐ TimerX ☐ TimerY ☐ TimerZ
- ☐ A/D converter ☐ D/A converter ☐ Watchdog Timer ☐ PWM ☐ Serial I/O2
- ☐ Serial I/O1 (☐ Clock synchronous Serial I/O mode ☐ Asynchronous Serial I/O(UART) mode)
- ☐ Serial I/O3 (☐ Clock synchronous Serial I/O mode ☐ Asynchronous Serial I/O(UART) mode)
- ☐ Pull-up ☐ LED direct drive port

Thank you cooperation

∗3. Comments