

Product Alert Notice (PAN)

Subject: Datasheet Specification Change for Standard Microcircuit Drawing (SMD) Part Number 5962R0922501*, 5962R0922502* and Intersil Part Numbers ISL70001S*

Publication Date: 2/26/2016

Effective Date: 2/26/2016

Revision Description:

Initial Release

Description of Change:

Added 2 paragraphs (reference Appendix A) to the 1st page of the datasheet to highlight the requirement of the input voltage ramp to be equal to or greater than 10V/ms when the ENABLE input is tied high to PVIN.

Reason for Change:

The change is required to highlight to customers that when the ENABLE input is tied high to PVIN we recommend that the Input voltage ramp rate be equal to or greater than 10V/ms. This is to prevent unwanted voltage from prematurely appearing on the output. The updated datasheet is available on Intersil's site at:

<http://www.intersil.com/content/dam/intersil/documents/isl7/isl70001seh-srh.pdf>

Impact on fit, form, function, quality & reliability:

The change will have no impact on the form, fit, function, quality, reliability and environmental compliance of the devices.

Product Identification:

There have been no changes to the die/silicon or product itself. There will be no change in the external marking, the changes are corrections to the datasheet only.

Qualification status: Not applicable, documentation correction only

Sample availability: 2/26/2016

Device material declaration: Available upon request

Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: PCN-US@INTERSIL.COM	Europe: PCN-EU@INTERSIL.COM	Japan: PCN-JP@INTERSIL.COM	Asia Pac: PCN-APAC@INTERSIL.COM

Appendix A: Product List

ISL70001SEHFE/PROTO	ISL70001SEHVXS2745	ISL70001SRHFR5427	ISL70001SRHVXS2745
ISL70001SEHVF	ISL70001SEHVXS2746	ISL70001SRHQF	ISL70001SRHVXS2746
ISL70001SEHVFE	ISL70001SEHVXS2747	ISL70001SRHVF	ISL70001SRHVXS2747
ISL70001SEHVX	ISL70001SRHF/PROTO	ISL70001SRHVX	ISL70001SRHX/SAMPLE
5962R0922502VXC	5962R0922502V9A	5962R0922501VXC	5962R0922501V9A
5962R0922502VYC	5962R0922501QXC		

Appendix B: Paragraphs added to the datasheet.

In applications where the **ENABLE** input is tied high to **PVIN** we recommend that the input voltage ramp rate be equal to or greater than 10V/ms. This is to prevent unwanted voltage from prematurely appearing on the output.

For a **PVIN** voltage that has a slower ramp rate or is stepped up we recommend use of the [ISL70001ASEH](#). Ensuring that the **ENABLE** input is held low until the chosen **VINPOR** is satisfied will prevent this 'false start'.