Integrated Device Technology, Inc.
2975 Stender Way, Santa Clara, CA - 95054

PRODUCT/PROCESS CHANGE NOTICE (PCN)

PCN #: **L0304-01** DATE: 5/19/03

Product Affected: Please see attachment for the product list

Date Effective: August 18, 2003

MEANS OF DISTINGUISHING CHANGED DEVICES:

- ☐ Product Mark
- ☐ Back Mark
- ■ Date Code — Prefix (Stepping) change. Please see attachment for details.
- ☐ Other

Contact: Bimla Paul
Title: Quality Assurance Manager
Phone #: (408)-654-6419
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Attachment:: ■ Yes ☐ No

Samples: Available upon request.

DESCRIPTION AND PURPOSE OF CHANGE:

- ☐ Die Technology
- ☐ Wafer Fabrication Process
- ☐ Assembly Process
- ☐ Equipment
- ☐ Material
- ☐ Testing
- ■ Manufacturing Site
- ☐ Data Sheet
- ☐ Other

Selected Logic products will be transferred to IDT's wafer fab facility in Hillsboro, Oregon (Fab 4). There is no change in die technology.

Please see attachment for the product list and qualification data.

RELIABILITY/QUALIFICATION SUMMARY:

Please see attachment for qualification data.

CUSTOMER ACKNOWLEDGMENT OF RECEIPT:

IDT records indicate that you require written notification of this change. Please use the acknowledgement below or E-Mail to grant approval or request additional information. If IDT does not receive acknowledgement within 30 days of this notice it will be assumed that this change is acceptable.
IDT reserves the right to ship either version manufactured after the process change effective date until the inventory on the earlier version has been depleted.

Customer: _____ ☐ ***Approval for shipments prior to effective date.***

Name/Date: _____ E-Mail Address: _____

Title: _____ Phone# /Fax# : _____

CUSTOMER COMMENTS: _____

IDT ACKNOWLEDGMENT OF RECEIPT:

RECD. BY: DATE:

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ATTACHMENT - PCN #: L0304-01

PCN Type: Fab Site Change

Data Sheet Change: None

Detail of Change: Transfer existing qualified products to Hillsboro, Oregon Wafer Fab Facility (Fab 4).

The following devices are affected by this change. All packages and speed grades are affected.

Part Number	Old Stepping	New Stepping (Fab 4)
IDT74LVC4245A	XF	XF4
IDT74LVCC3245A	XF	XF4
IDT74LVCC4245A	XF	XF4
IDTQS32XVH245	Z	Z4

	Conversion Schedule:	Sample Availability:	Production Shipment:
XF4 Stepping	August 18, 2003	Available	August 18, 2003
Z4 Stepping	Available	Available	August 18, 2003

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Qualification Plans: Following reliability tests are being performed per process and device families. The qualification results are as follow:

	Required Sample/ # Fails	CMOS 8 Process Family	Lot # 2 XF4 Device	Z4 Device Family
Operating Life Test: Dynamic 1000 hrs @ 125°C or equivalent	116/0	116/0	116/0 (500 Hrs) ECD June 03	116/0
Temperature Cycling 500 Cycles -65°C to +150°C	45/0	45/0	N/A	N/A
Highly Accelerated Stress Test 100 hours	45/0	45/0	N/A	N/A
ESD Human Body Model	3/0	2000V (3/0)	2000V (3/0)	3500V (3/0)
ESD Charged Device Model	3/0	1000V (3/0)	1000V (3/0)	1000V (3/0)
Latch up: (Tested to 1.5X Vcc)	10/0	10/0	10/0	10/0

Characterization Data:
Characterization data is available upon request.