

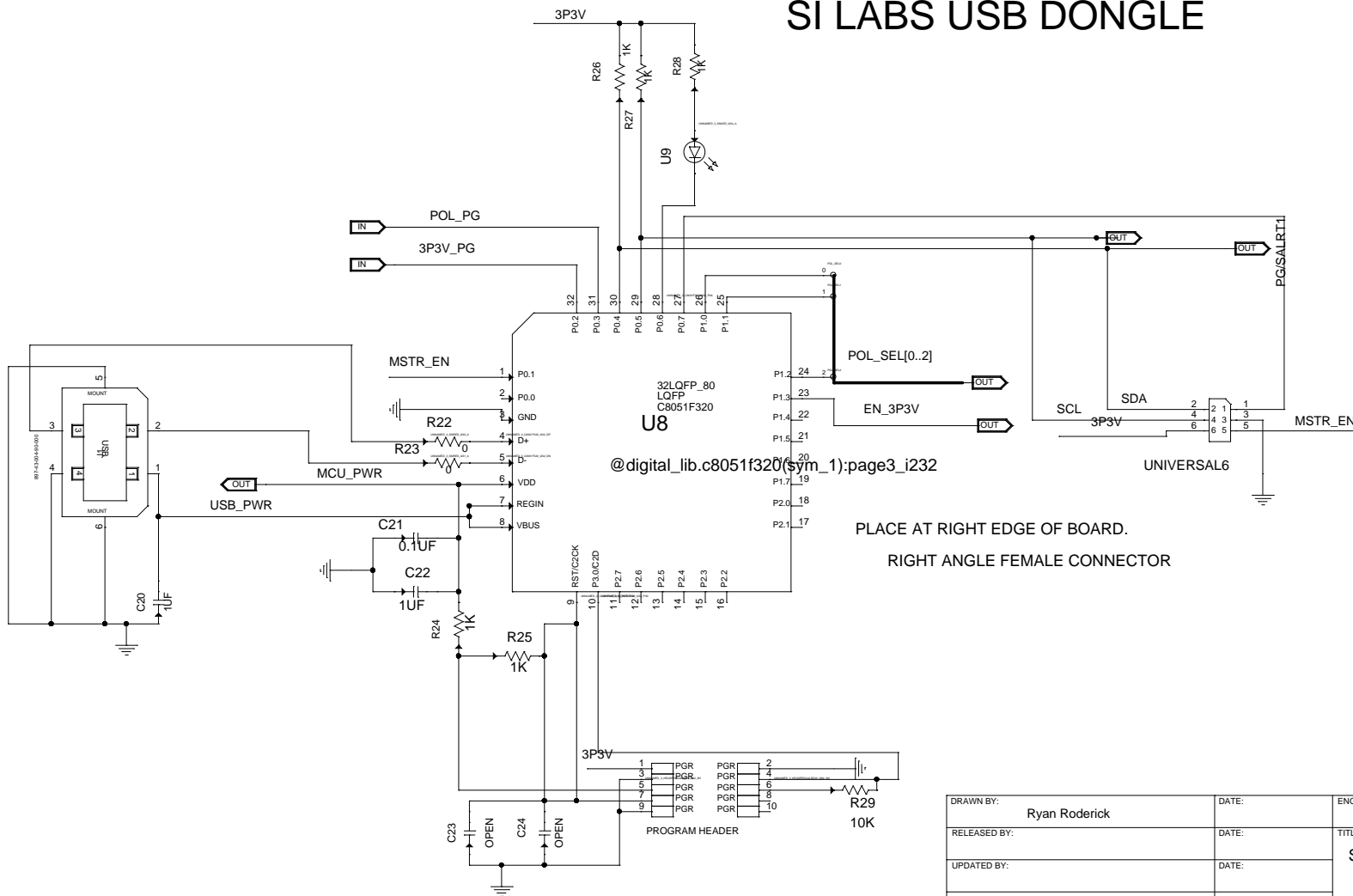
SI LABS GENERIC DONGLE BOARD (POWER)

BY

RYAN RODERICK

DRAWN BY: Ryan Roderick	DATE:	ENGINEER: Ryan Roderick	DATE:	
RELEASED BY:	DATE:	TITLE: Si Labs USB Dongle		
UPDATED BY:	DATE:			
\$CDS_IMAGE\intersil_color_sm.jpg\1194\282 Thu Apr 10 17:57:42 2014	TESTER	MASK#	HRDWR ID	REV. A
	FILENAME:			SHEET 1 OF 3

SI LABS USB DONGLE



PLACE AT RIGHT EDGE OF BOARD.
RIGHT ANGLE FEMALE CONNECTOR

DRAWN BY:	Ryan Roderick	DATE:	ENGINEER:	Ryan Roderick	DATE:
RELEASED BY:		DATE:	TITLE: Si Labs USB Dongle UCONTROLLER		
UPDATED BY:		DATE:			
\$CDS_IMAGE[intersil_color_sm.jpg 1194 282 Thu Apr 10 22:20:51 2014		TESTER	MASK# HRDWR ID REV: A		
		FILENAME:			

