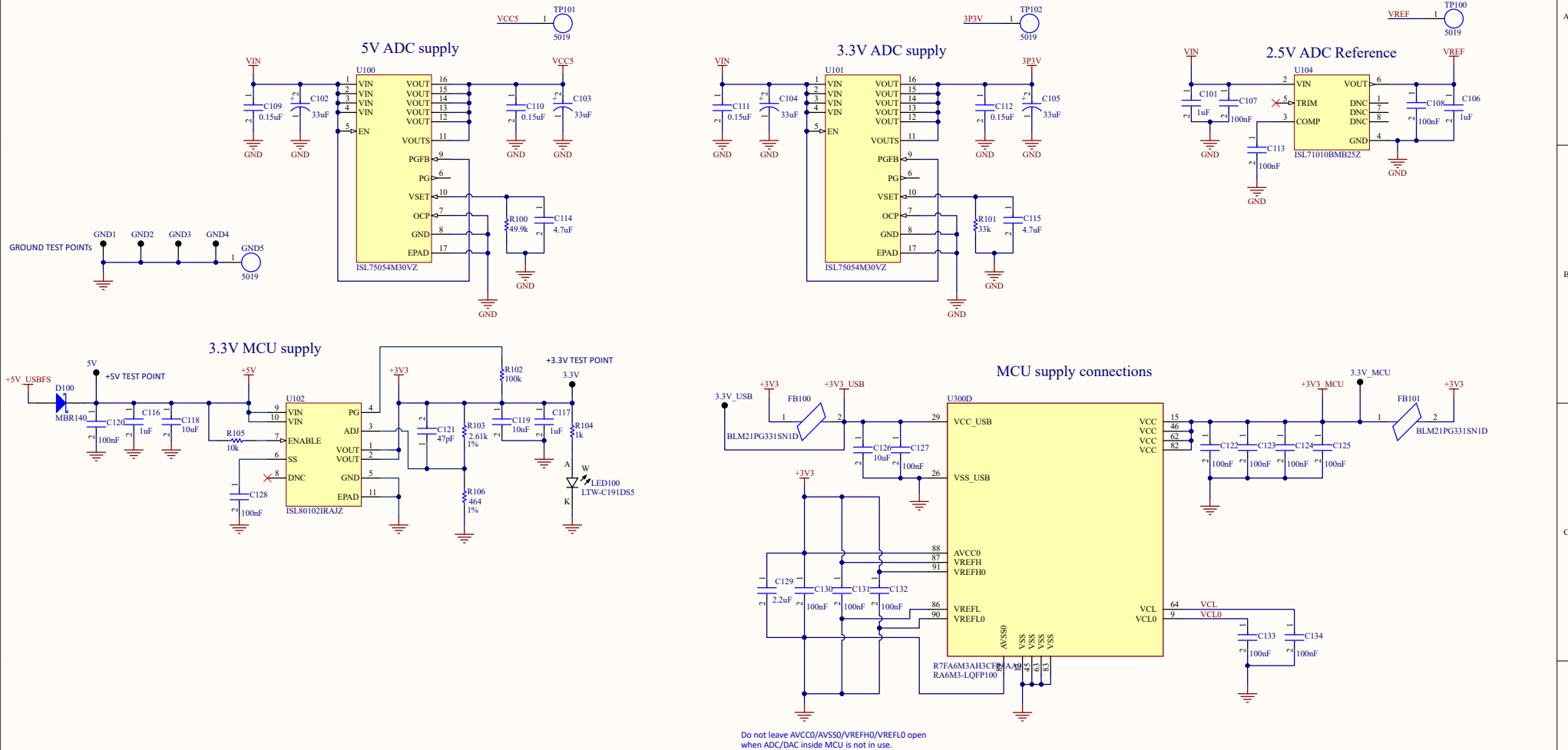


# Power Supply and Reference Circuitry



Do not leave AVCC0/AVSS0/VREFH0/VREFL0 open when ADC/DAC inside MCU is not in use.

ISL71148VMREFEV2Z

Version: 1.0  
Release Date: 2023-01-10  
File: Sheet\_S0700

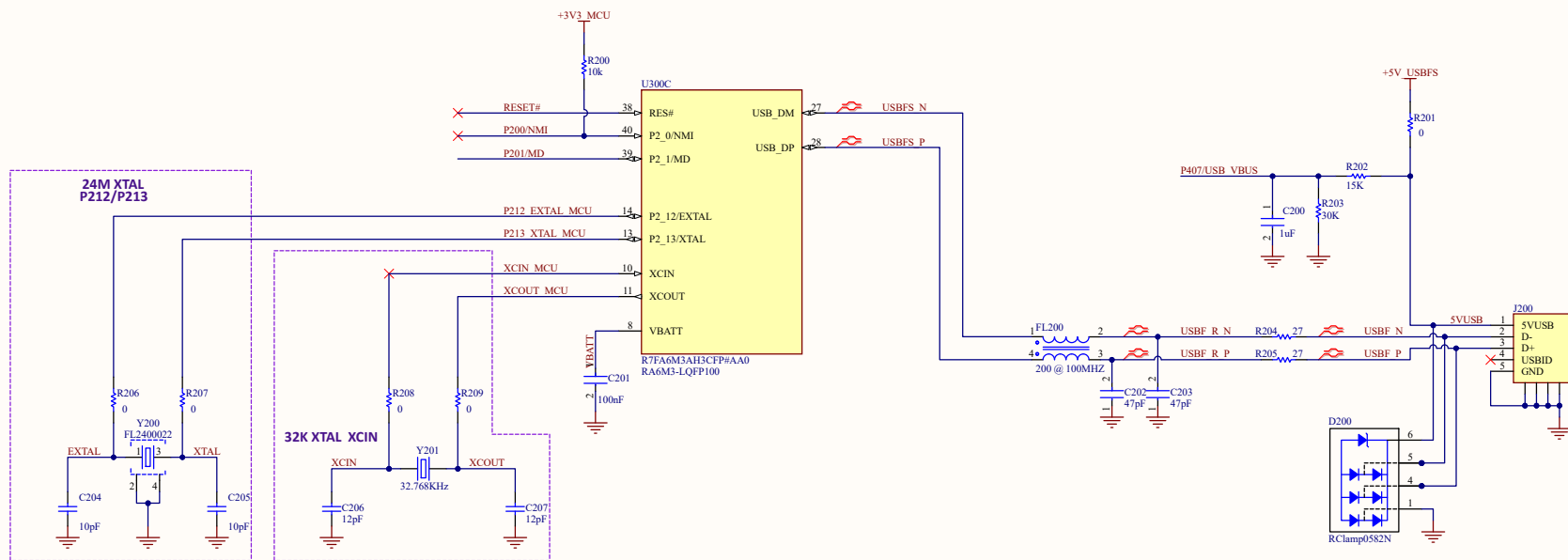
Document Revision: 1.0  
Sheet 1 of 6

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60 Last Updated: 2023-01-10

# USB FS Interface and Clocks



ISL71148VMREFEV2Z

Version:   
 Release Date:   
 File: sheet3\_30fdoc

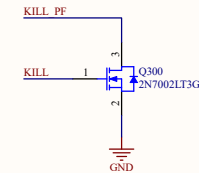
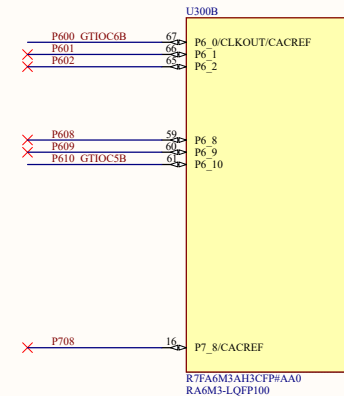
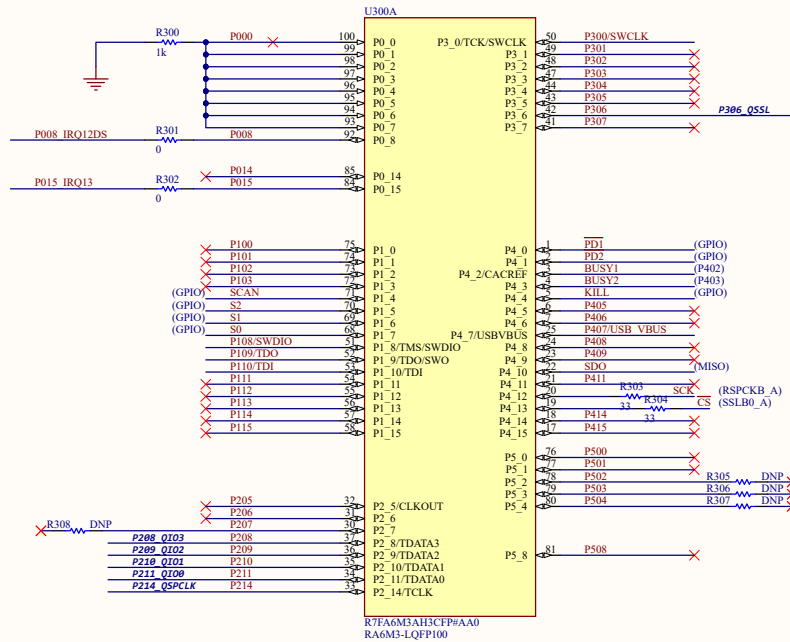
Document Revision:   
 Sheet 2 of 6

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60 Last Updated: 2019/03/01

# MCU I/O Ports



ISL71148VMREFEV2Z

Version: Document Revision:  
Release Date: Sheet 3 of 6  
File: sheet3.pdf

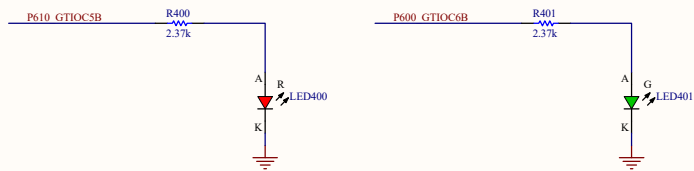
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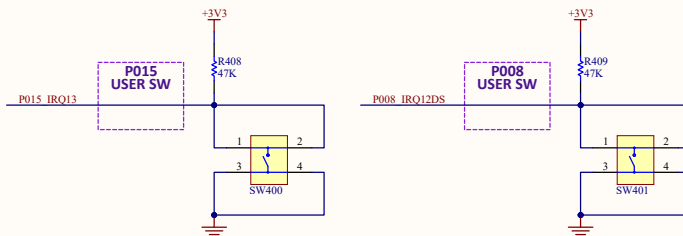
60 Last Updated: 2019/05/01

# Buttons, LEDs, QSPI Flash, JTAG Circuits

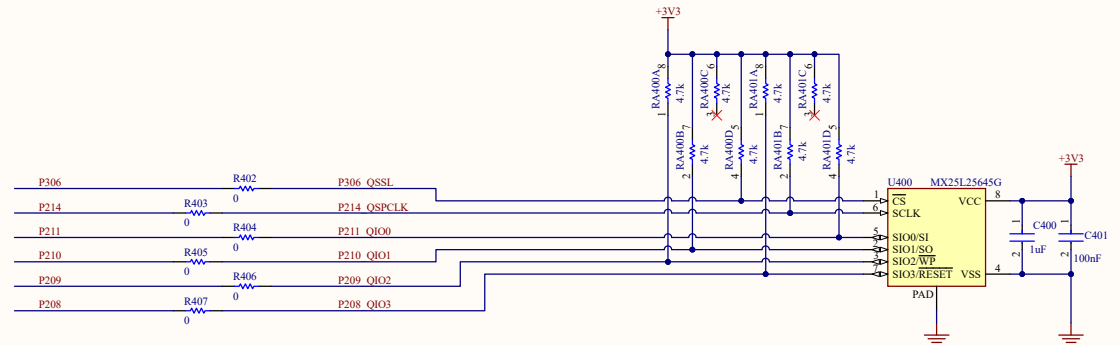
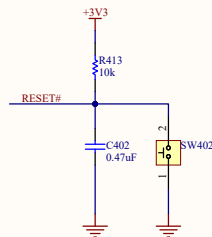
## USER LEDS



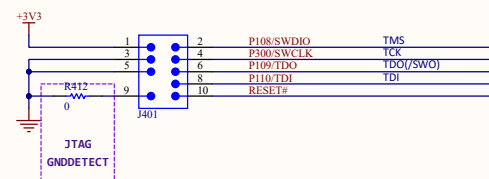
## USER PUSH-BUTTONS



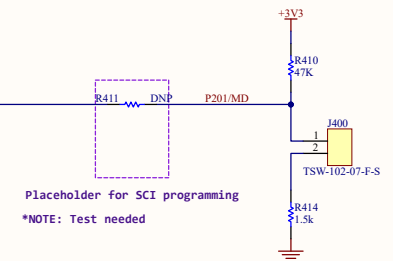
## RESET PUSH-BUTTON



## JTAG CONNECTORS



## MCU BOOT MODE



ISL71148VMREFEV2Z

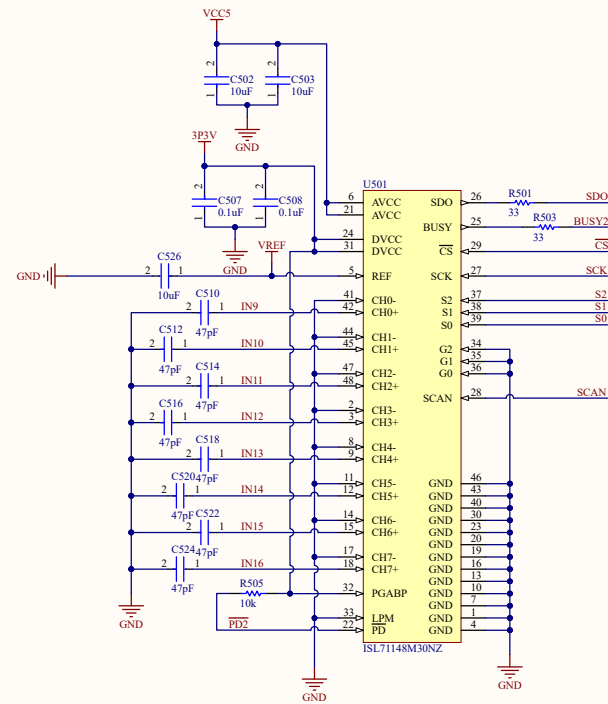
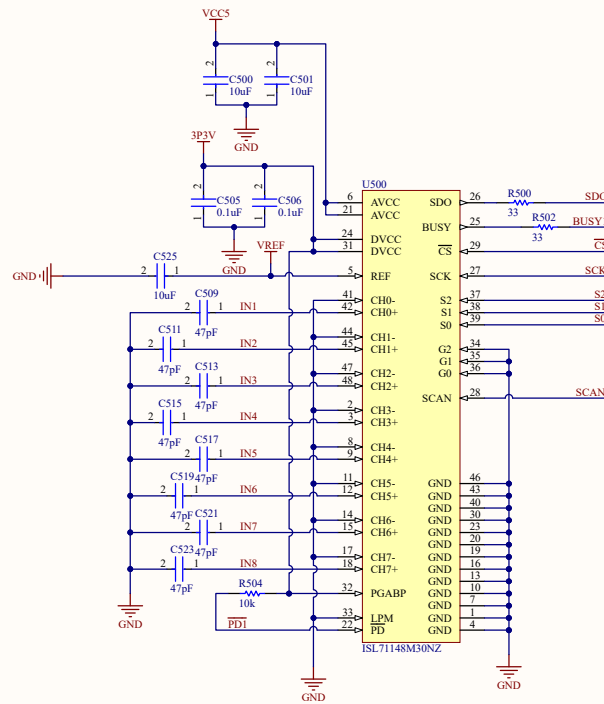
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Release Date: 2014-01-01  
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60 Last Updated: 2014-01-01

# ADC Circuit



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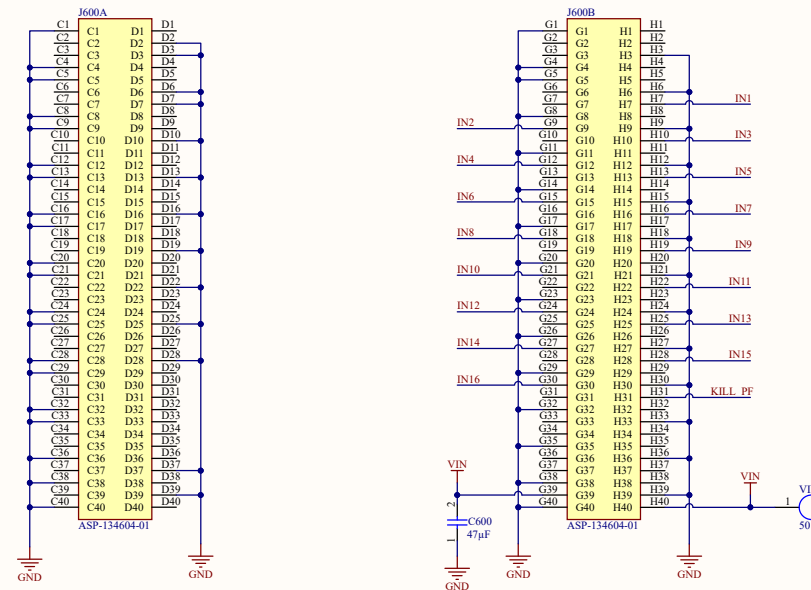
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Release Date: \_\_\_\_\_ Sheet 5 of 6  
File: sheets\_507doc

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# FMC Connector



\*FMC Connector Mounted to Bottom Side of Board