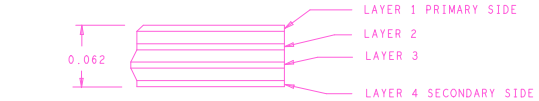
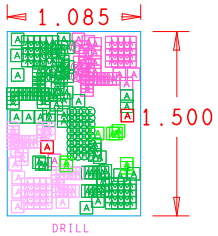
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



.005 BETWEEN TOP AND ADJACENT LAYER2
AND BETWEEN BOTTOM AND ADJACENT LAYER3.

DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
Ⓟ	11.0	PLATED	47
Ⓐ	12.0	PLATED	218



DRILL

NOTES:

1. THIS BOARD IS RoHS COMPLIANT.
2. PRINTED WIRING BOARD DESIGN AND ACCEPTANCE CRITERIA SHALL BE IAW WITH THE REQUIREMENTS OF IPC-D-275 AND IPC-A-600.
3. MATERIAL: FR4 (RoHS COMPLIANT), 2 OZ COPPER..005" SPACING BETWEEN LAYER1 AND LAYER2 AND BETWEEN LAYER3 AND LAYER4.
4. APPLY SOLDER MASK, BOTH SIDES OVER BARE COPPER IAW IPC-SM-840. CLASS 2 (LPI) (BLUE MASK).
5. ALL PATTERNS ARE VIEWED FROM THE PRIMARY SIDE LOOKING THROUGH THE BOARD.
6. UNLESS OTHERWISE SPECIFIED ALL HOLE DIAMETERS ARE AFTER PLATING.
7. APPLY SILKSCREEN USING WHITE NON-CONDUCTIVE EPOXY BASED INK.
8. PWB MUST BE 100% ELECTRICALLY TESTED FOR SHORTS AND CONTINUITY. USE NETLIST PROVIDED ISL73007SEHEV2ZA_IPC356.IPC IAW IPC-D-356.
9. MARK DATE CODE AND MANUFACTURES IDENIFICATION ON SOLDER SIDE PER IPC-6011 AND IPC-6012.
10. TOLERANCE ON ALL DRILL HOLES SHALL BE IAW IPC-D-2221 & 2222 UNLESS OTHERWISE SPECIFIED.
11. BOTTOM LAYER HAS SHAPE WHICH SHORTS GROUND TO S1 GROUND BY DESIGN WHICH IS PLACED UNDER COPONENT U1.
12. INTERSIL LOGO USED ON THIS DESIGN PLEASE DO NOT CHANGE TO RENESAS.
13. ALL 11 MIL VIA'S ARE TO BE THERMAL EPOXY FILLED AND CAPPED.
14. GND_S1 AND GND ARE INTENTIONALLY SHORTED ON LAYER 1.

DASH NO	NEXT ASSY
	APPLICATION

PART TO BE FREE OF BURRS AND SHARP EDGES
COMMERCIAL TOLERANCES APPLY TO STOCK SIZE
MANUFACTURING TOLERANCES PER 900003
WORKMANSHIP PER 900065
SCREW THREADS PER ANSI Y14.6
ABBREVIATIONS PER MIL-STD-12
LOGIC SYMBOLS PER IEEE STD 91
DIMENSIONING AND TOLERANCING PER ANSI Y14.5
ELECTRICAL AND ELECTRONIC DIAGRAM PER ANSI Y14.15
GRAPHIC SYMBOLS FOR ELECTRICAL AND ELECTRONIC DIAGRAM PER IEEE STD 315
ELECTRICAL AND ELECTRONIC REFERENCE DESIGNATIONS PER IEEE STD 200
WELDING SYMBOLS PER ANSI/AWS A2.4
INTERPRET DRAWING IAW DOD-STD-100

UNLESS OTHERWISE SPECIFIED DIMENSION ARE IN INCHES AND INCLUDE APPLIED FINISH TOLERANCES
2 PLACE DIM. ± .---
3 PLACE DIM. ANGLES ± .---

CONTRACT NO.		
DWN BY 10/20/21 Tim Klemann	CHK BY	
ENGR	ELEC ENGR	
PROJ ENGR	MFG ENGR	
RELIABILITY	CONFIGURATION	
APPROVAL- DFTG	QA	
APPROVAL	SYS ENGR	

PRINTED WIRING BOARD, EVAL-
ISL73007SEHEV2Z

SIZE	DWG NO.	REV
D	ISL73007SEHEV2Z	A
SCALE 2/1		SHEET 1 OF 1