
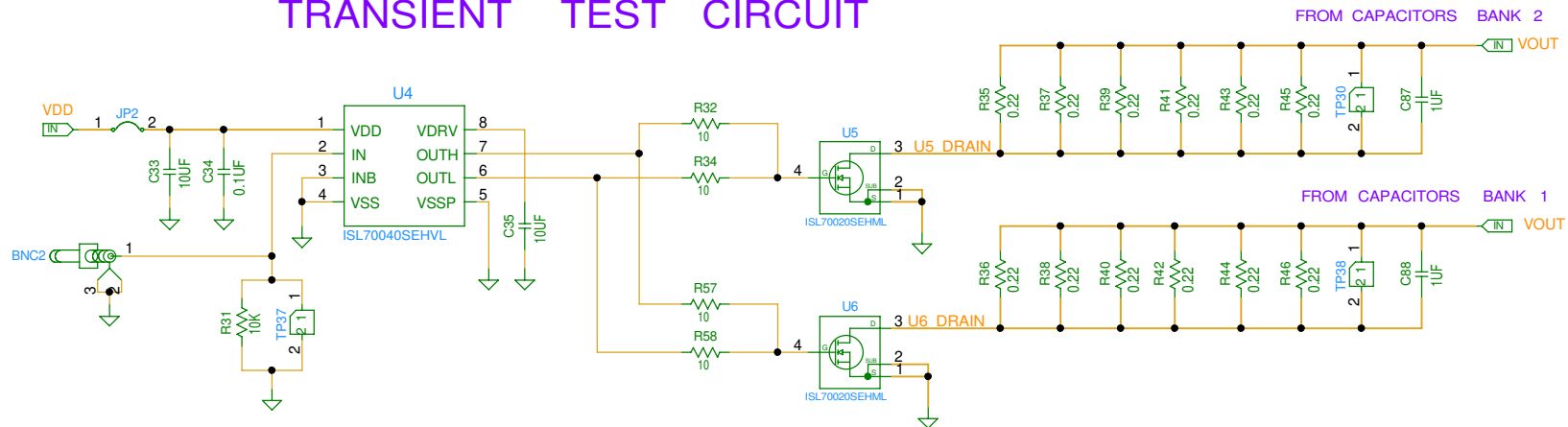
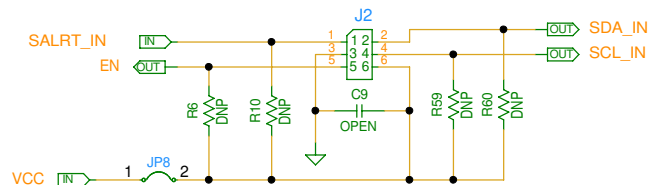



| | | | | | | | |
|---|--|-------------------------|--|---|--|----------|--|
| DRAWN BY: TIM KLEMANN | | DATE: 01/14/2025 | | ENGINEER: JUAN GARCIA | | DATE: | |
| RELEASED BY: | | DATE: | | ISL73849SLH EVALUATION BOARD SCHEMATIC | | | |
| UPDATED BY: | | DATE: | | | | | |
|  | | TESTER | | | | | |
| | | | | MASK# | | HRDWR ID | |
| FILENAME: ~\ISL73849SLH\ISL73849SLHEV1ZB | | | | SHEET 1 OF 3 | | | |

TRANSIENT TEST CIRCUIT



PMBUS INTERFACE



| | | | |
|---|---------------------|--|----------|
| DRAWN BY: TIM KLEMMANN | DATE: 01/14/2025 | ENGINEER: JUAN GARCIA | DATE: |
| RELEASED BY: | DATE: | TITLE: ISL73849SLH EVALUATION BOARD SCHEMATIC | |
| UPDATED BY: | DATE: | TESTER | |
|  | | MASK# | HRDWR ID |
| | | REV. B | |
| FILENAME: ~\ISL73849SLH\ISL73849SLHEV1ZB | | SHEET 3 OF 3 | |