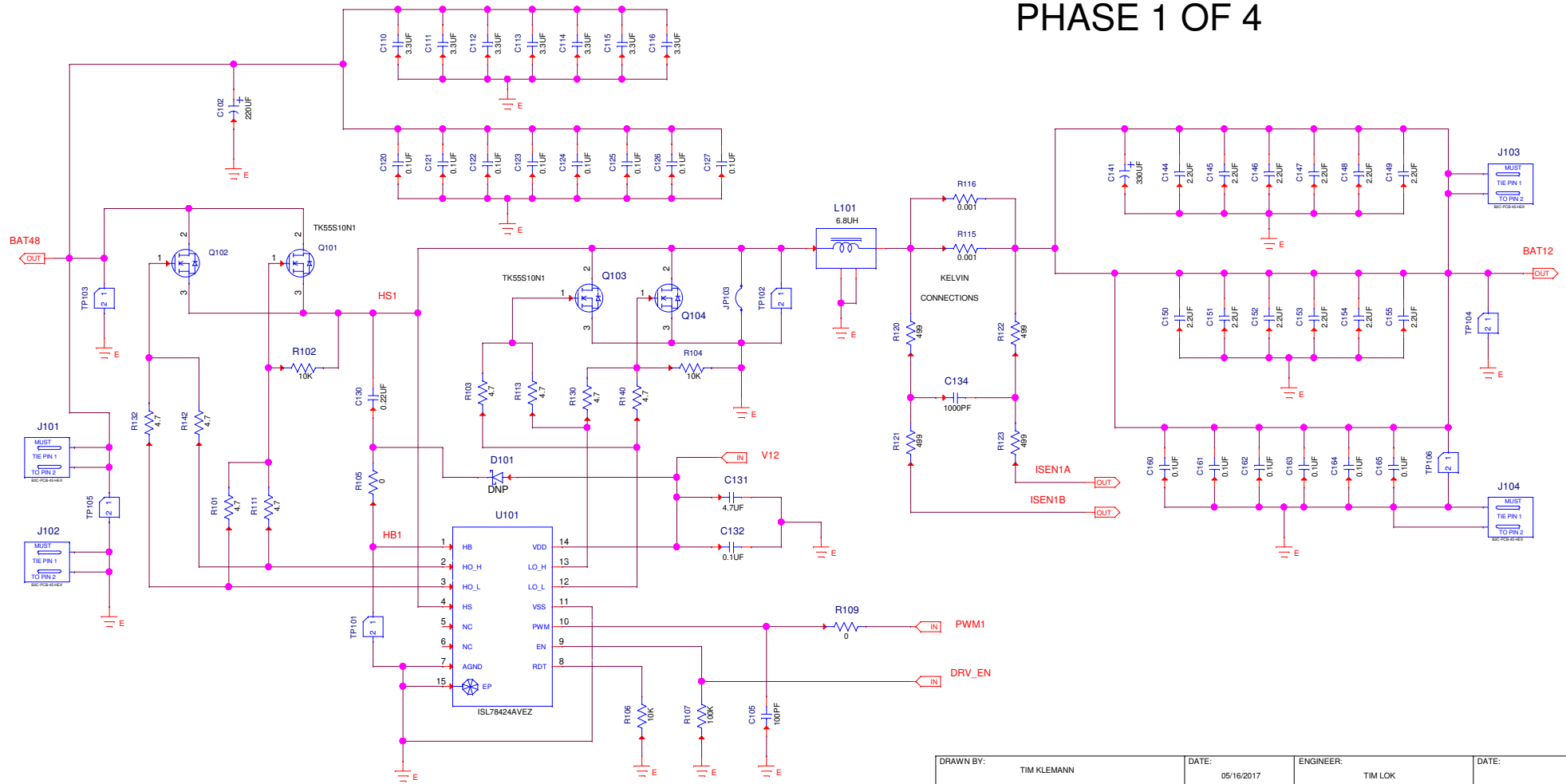
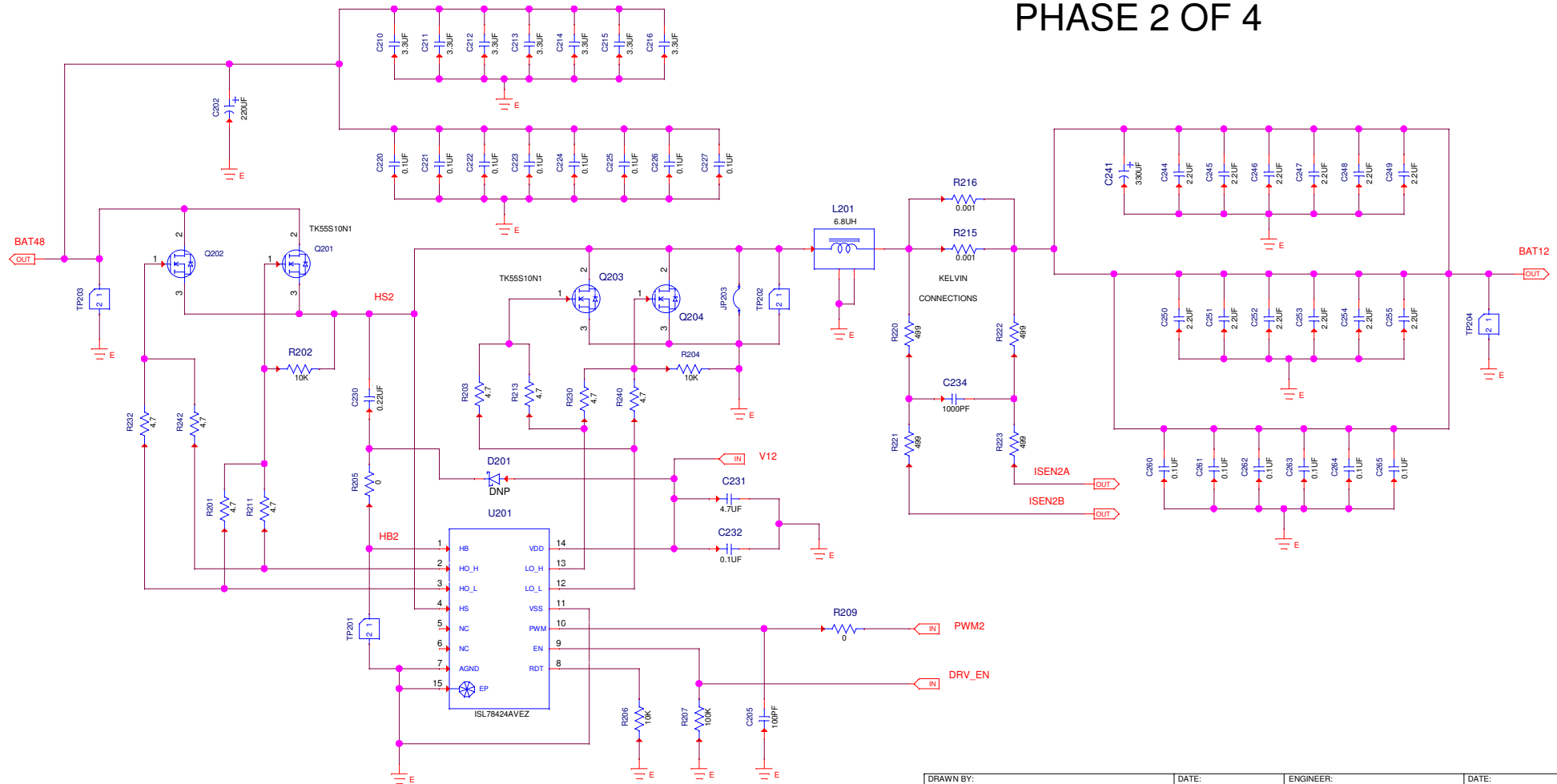


PHASE 1 OF 4



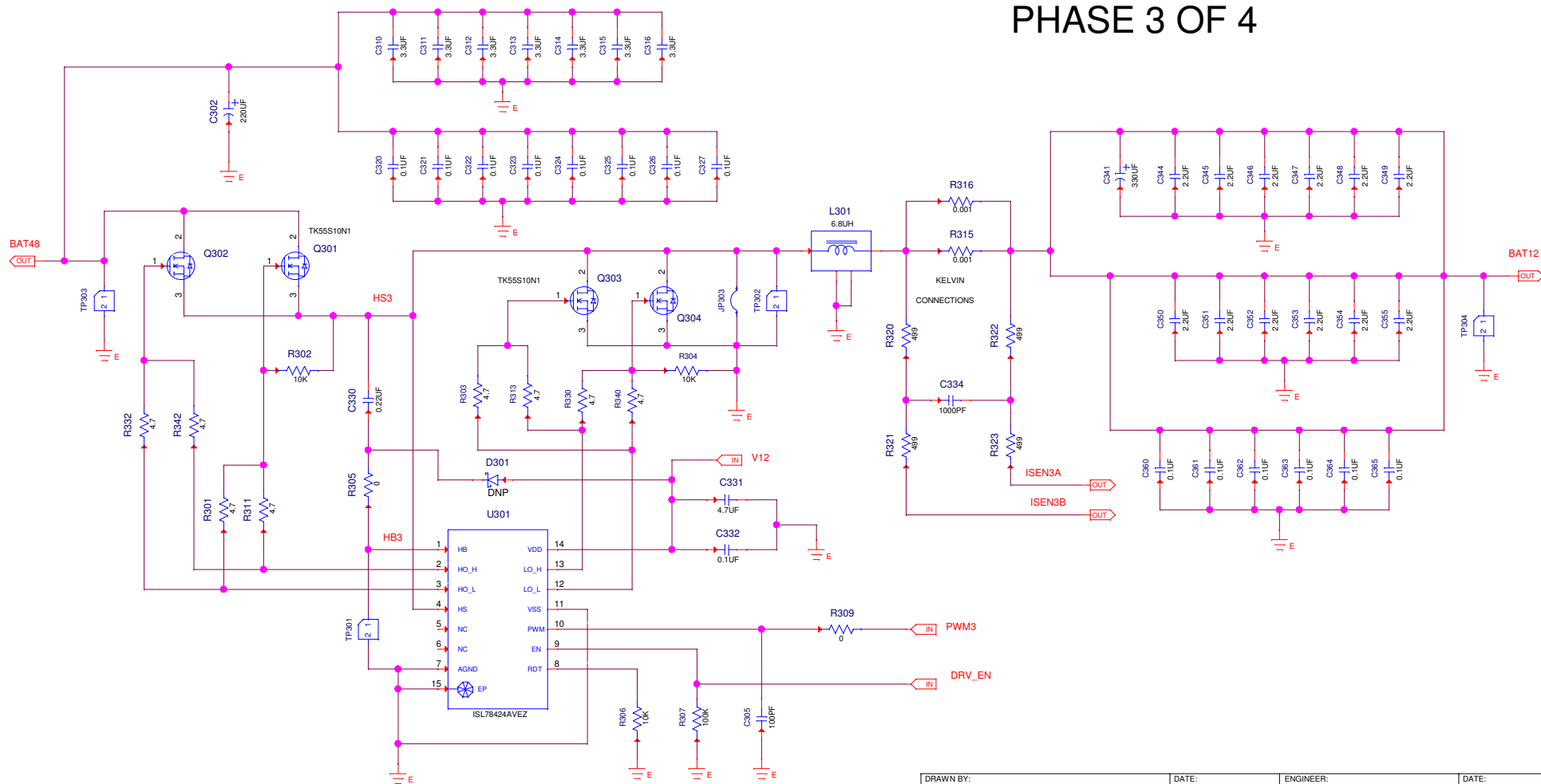
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UPDATED BY:		DATE:		TESTER			
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				SHEET	2	OF	8

PHASE 2 OF 4



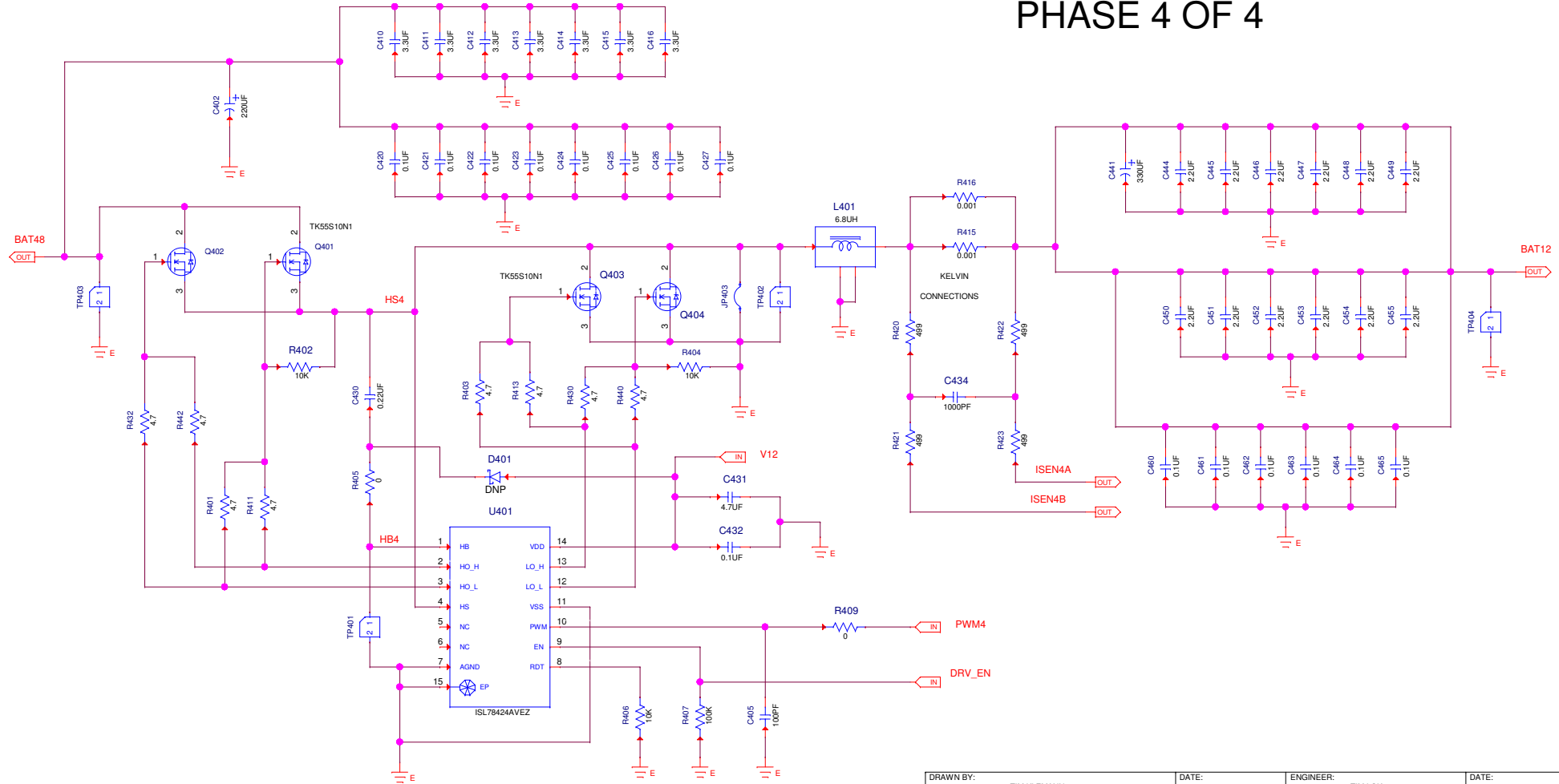
DRAWN BY:	TIM KLEMMANN	DATE:	05/16/2017	ENGINEER:	TIM LOK	DATE:	
RELEASED BY:		DATE:		TITLE:	ISL78224 - 4 PHASE EVALUATION BOARD SCHEMATIC		
UPDATED BY:		DATE:		TESTER:			
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FILENAME:	~/ISL78224/ISL78224EVAL2A					SHEET	3 OF 8

PHASE 3 OF 4

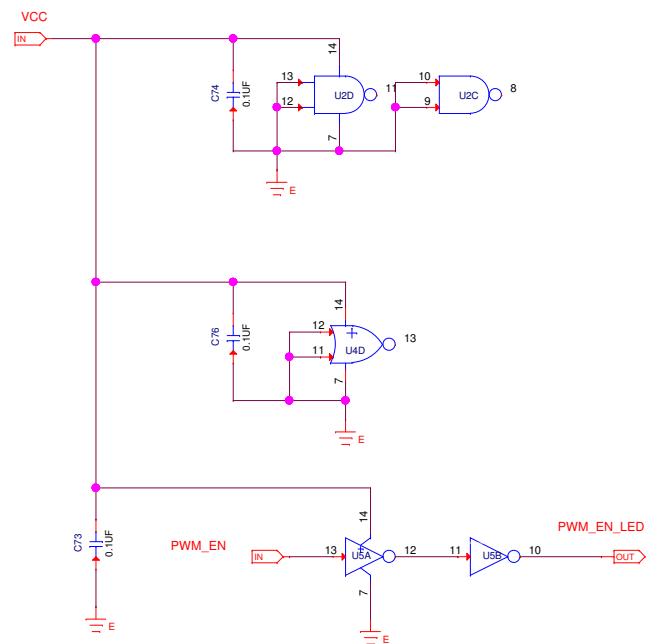


DRAWN BY: TIM KLEMMANN		DATE: 05/16/2017		ENGINEER: TIM LOK		DATE:			
RELEASED BY:		DATE:		ISL78224 - 4 PHASE EVALUATION BOARD SCHEMATIC					
UPDATED BY:		DATE:							
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FILENAME: -\ISL78224\ISL78224EVAL2ZA						SHEET 4		OF 8	

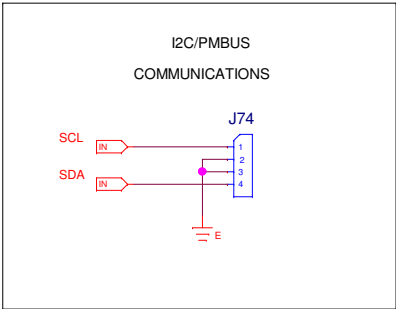
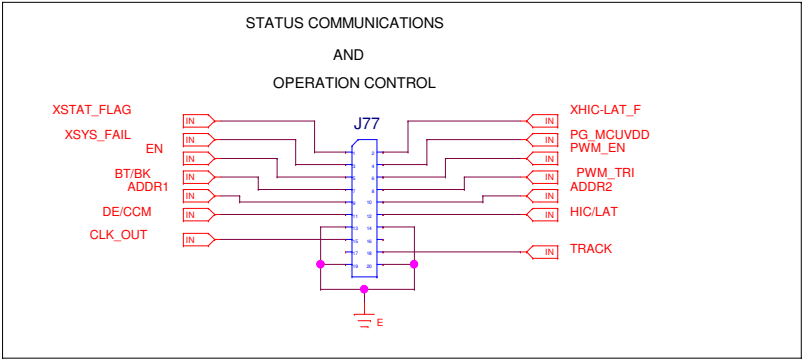
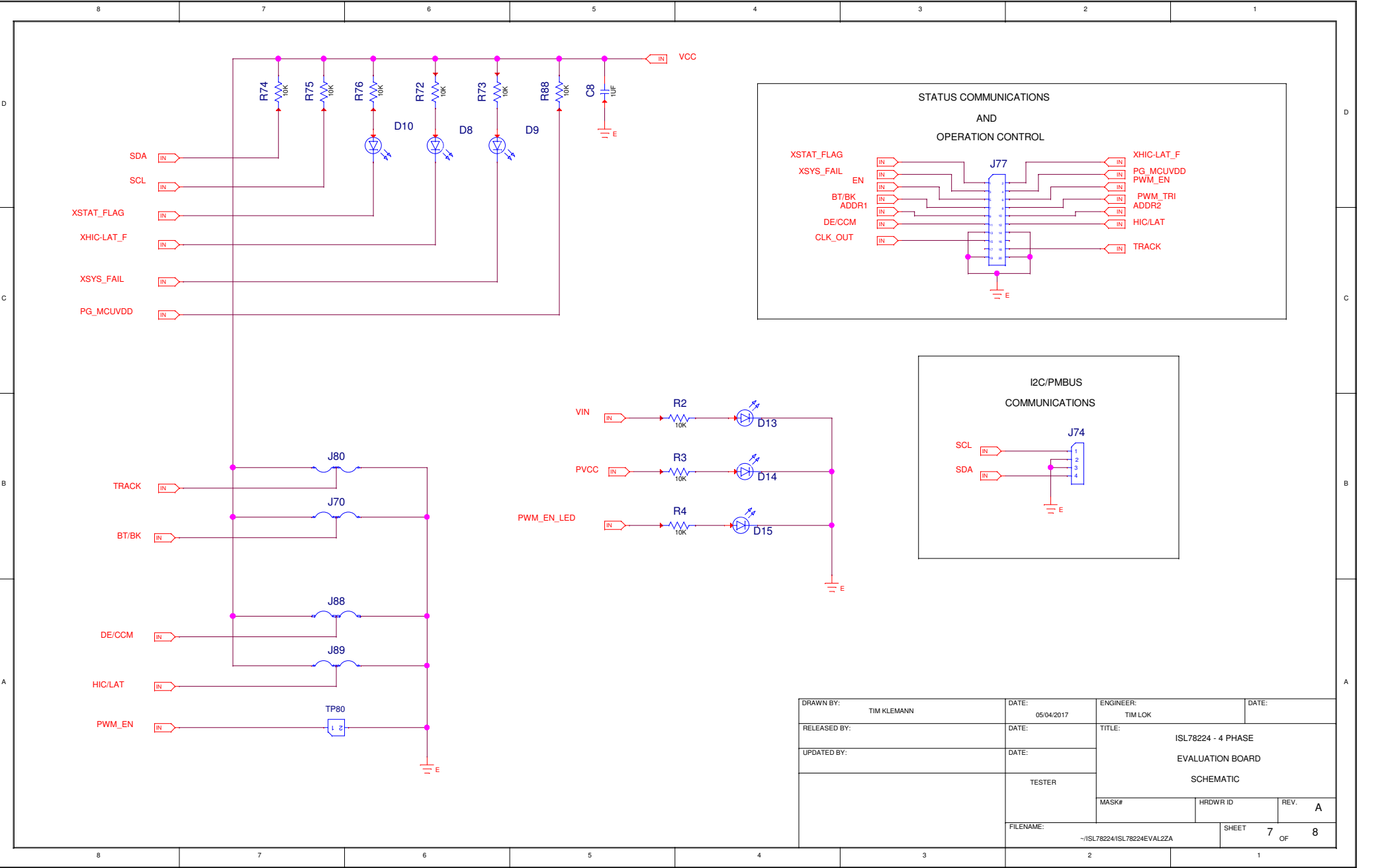
PHASE 4 OF 4



DRAWN BY:	TIM KLEMMANN	DATE:	05/16/2017	ENGINEER:	TIM LOK	DATE:	
RELEASED BY:		DATE:		TITLE:	ISL78224 - 4 PHASE EVALUATION BOARD SCHEMATIC		
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FILENAME:	~/ISL78224/ISL78224EVAL2ZA					SHEET	5 OF 8



DRAWN BY:	TIM KLEMMANN	DATE:	04/26/2017	ENGINEER:	TIM LOK	DATE:	
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DRAWN BY: TIM KLEMMANN	DATE: 05/04/2017	ENGINEER: TIM LOK	DATE:
RELEASED BY:	DATE:	ISL78224 - 4 PHASE EVALUATION BOARD SCHEMATIC	
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		SHEET 7 OF 8	

