

SURFACE OF VIAS IN PADS FOR WLCSP MUST BE FLAT AFTER PLATING.  
CUPPER FILLED OR PLATE SHUT & PLATE OVER.

ALL THE VIAS INSIDE THIS OUTLINE MUST BE COPPER FILLED

ALL THE VIAS IN PADS OUTSIDE THIS OUTLINE CAN BE FILLED WITH NON CONDUCTIVE EPOXY.

NOTES: UNLESS OTHERWISE SPECIFIED.

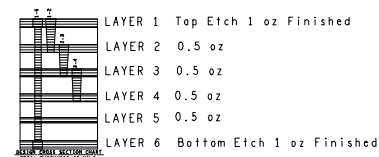
- BOARD IS VIEWED FROM LAYER 1 (PRIMARY SIDE). TOTAL CONDUCTOR LAYERS ARE 6.
- THIS DRAWING ALSO EXISTS ON ELECTRONIC MEDIA.
- MANUFACTURE/FABRICATE TO MEET EU RoHS DIRECTIVE. ALL MATERIALS AND SOLDERMASK TO BE COMPLIANT TO EU RoHS DIRECTIVE 20002/95/EC  
LAMINATE AND RESIN MATERIAL PARAMETERS  
Tg > 170 C  
Td > 294 C
- FINISH:  
ELECTROLISS NICKEL/GOLD(ENIG)PLATING IS TO BE USED. 3-8 uin GOLD OVER 100uin NICKEL.
- SOLDERMASK SHALL BE LPI (RED) OVER BARE COPPER. 0.0005"-0.002" THICK, AND CONFORM TO THE LATEST REVISION OF IPC-SM-840.
- CONDUCTOR WIDTH AND SPACING:  
THE NOMINAL CONDUCTOR WIDTH AND SPACING ON FINISHED BOARD IS:  
WIDTH 0.006"  
SPACING 0.004"
- SILKSCREEN SHALL BE WHITE NONCONDUCTIVE EPOXY INK, AND SHALL NOT APPEAR ON ANY PADS OR MOUNTING HOLES.
- PRIOR TO BOARD FABRICATION, COMPARE GERBER DATA TO SUPPLIED IPC-D-356A NET LIST. REPORT ALL DISCREPANCIES.

DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
*	6.0	PLATED	32
⊙	8.0	PLATED	117
⊙	8.0	PLATED	1193
⊙	10.0	PLATED	5
*	23.0	PLATED	19
*	23.0	PLATED	1
⊙	40.0	PLATED	18
⊙	40.0	PLATED	12
⊙	40.0	PLATED	12
△	40.0	PLATED	15
△	41.0	PLATED	2
⊙	255.0	PLATED	6
⊙	128.0	NON-PLATED	4

DRILL CHART: TOP to L2			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
*	4.0	PLATED	50
*	6.0	PLATED	200

DRILL CHART: L2 to L3			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
*	4.0	PLATED	44

DRILL CHART: L3 to L4			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
*	6.0	PLATED	33



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Released By:	Date Released:	ISL91302B22-EVZ	
Updated By: AJIT PAL	Date Updated: 04/12/18	Fabrication Drawing	
RENASAS	MAXX6 N/A	DATE 15 N/A	REV. A
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