


8	7	6	5	4	3	2	1
D							D
C							C
B							B
A							A
8	7	6	5	4	3	2	1

ISOLATION DONGLE BOARD

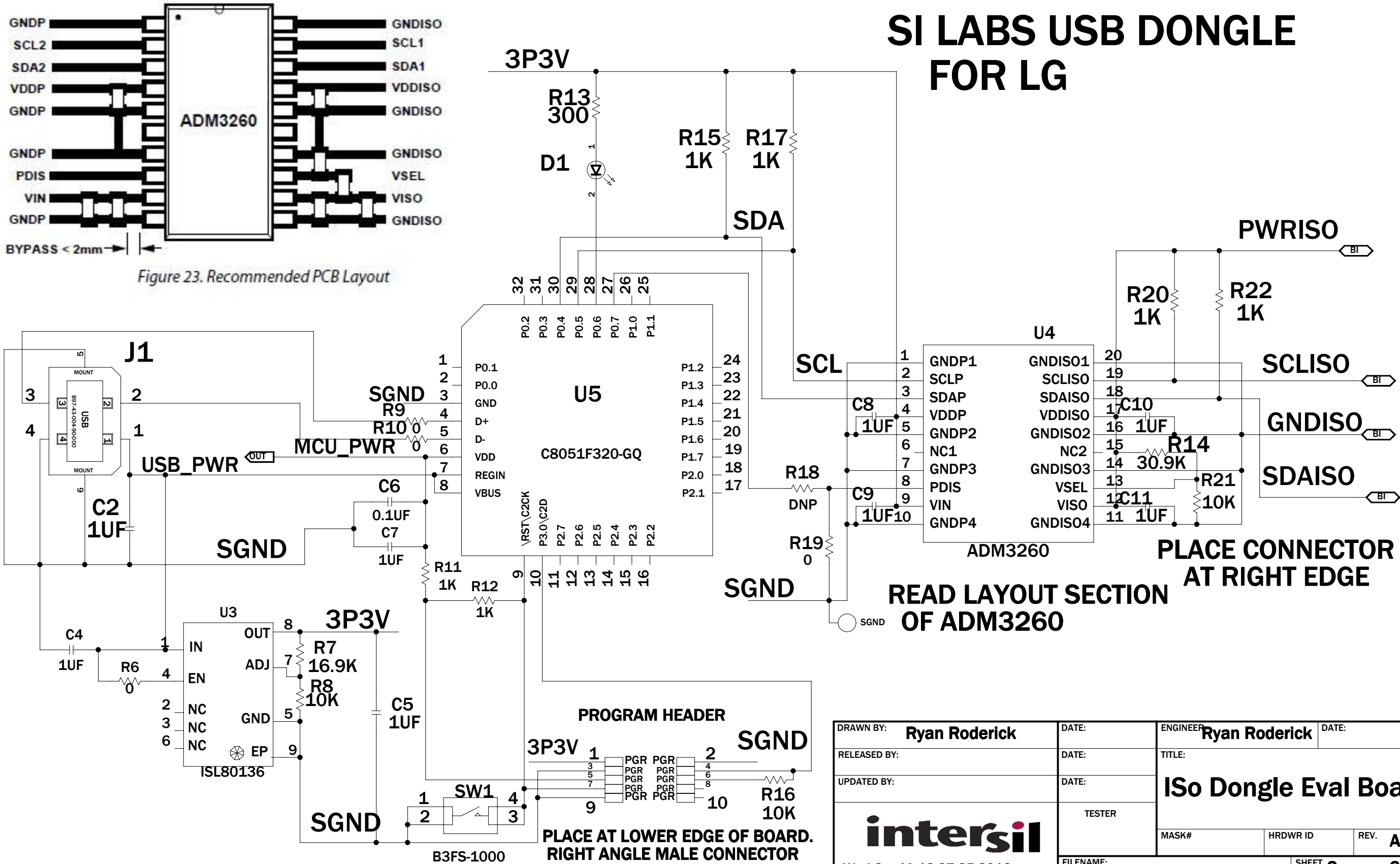
BY

RYAN RODERICK

DRAWN BY: Ryan Roderick	DATE:	ENGINEER: Ryan Roderick	DATE:	
RELEASED BY:	DATE:	TITLE: ISO Dongle Eval Board		
UPDATED BY:	DATE:			
 Thu Sep 01 14:38:25 2016	TESTER	MASK#	HRDWR ID	REV. A
	FILENAME:			SHEET 1 OF 3

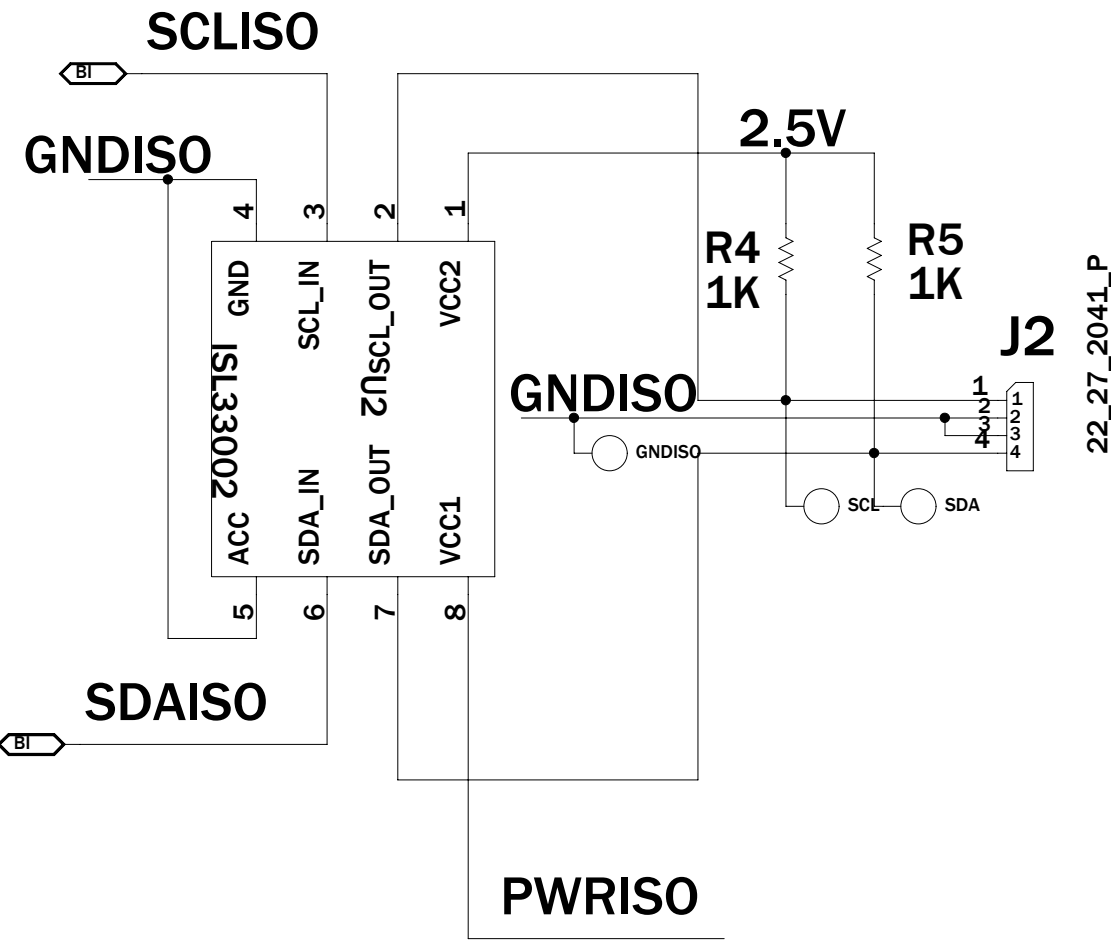
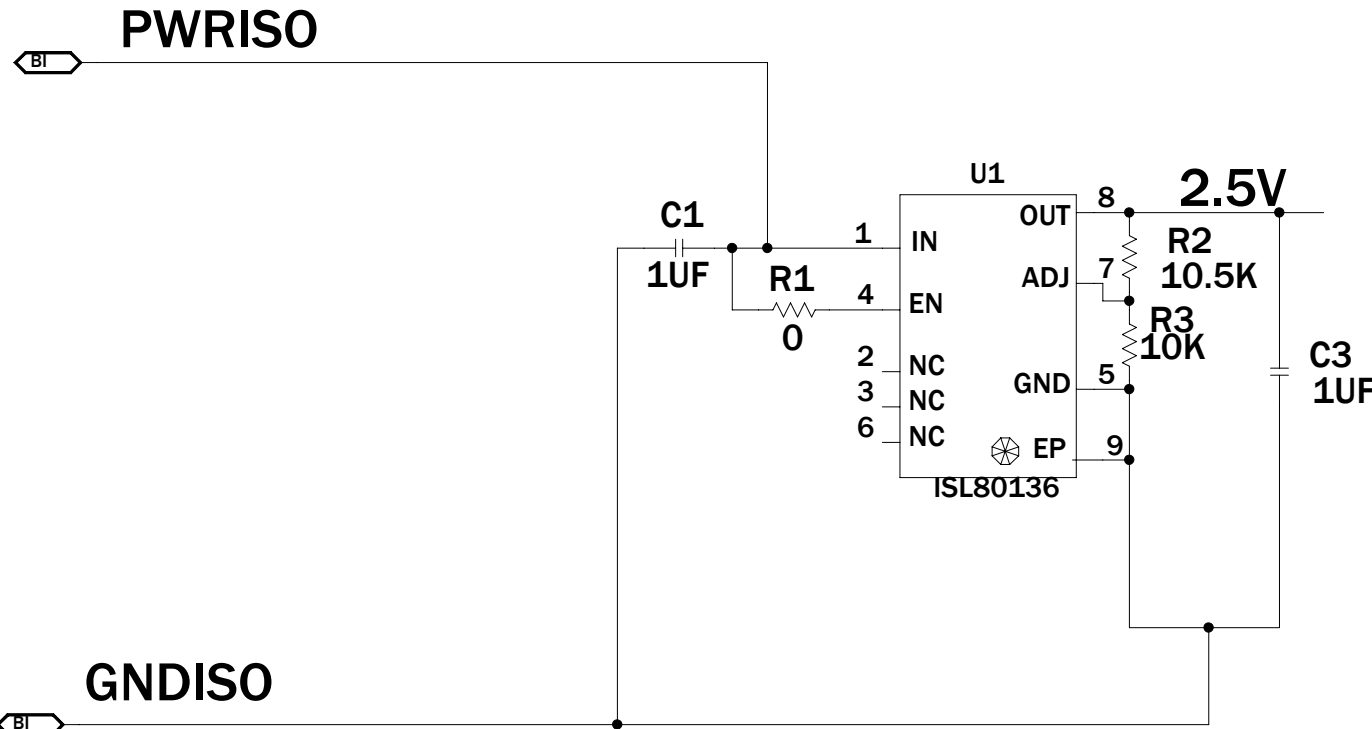
SI LABS USB DONGLE FOR LG


Figure 23. Recommended PCB Layout



DRAWN BY: Ryan Roderick	DATE:	ENGINEER: Ryan Roderick	DATE:
RELEASED BY:	DATE:	TITLE:	
UPDATED BY:	DATE:	TESTER:	
interSil		ISo Dongle Eval Board	
Wed Oct 19 13:37:35 2016		MASK#	HRDWR ID
		REV. A	
FILENAME:		SHEET 2 OF 3	

SI LABS USB DONGLE



DRAWN BY: Ryan Roderick	DATE:	ENGINEER: Ryan Roderick	DATE:
RELEASED BY:	DATE:	TITLE:	
UPDATED BY:	DATE:	ISo Dongle Eval Board	
 Wed Oct 19 13:37:36 2016	TESTER	MASK#	HRDWR ID
	FILENAME:	SHEET 3	OF 3
		REV. A	