

NOTES: UNLESS OTHERWISE SPECIFIED.

1. FABRICATE PER IPC-6012A CLASS 2.

2. MATERIAL:

DIELECTRIC: FR4 WITH MIN TG 180 DEGREE C[IPC-4101C/126]

COPPER: AS PER STACKUP.

U.L. RATING: 94V-0 MINIMUM

3. SURFACE FINISH: ENIG

4. SOLDER MASK MATERIAL SHALL MEET ALL THE REQUIREMENTS OF IPC-SM-840C AND SHALL BE RED IN COLOR AND APPLIED OVER BARE COPPER.

5. SILK SCREEN LEGEND TO BE APPLIED PER LAYER STACKUP USING WHITE NON-CONDUCTIVE EPOXY INK.

6. 100% CONTINUITY TESTING USING DATABASE NETLIST SHALL BE PERFORMED. VENDOR TO IDENTIFY TEST PASSED IN SECONDARY SIDE.

7. MARK DATE CODE AND MANUFACTURES IDENTIFICATION ON SOLDER SIDE PER IPC-6011 AND IPC-6012.

8. BOW AND TWIST SHALL NOT EXCEED 0.7% OF LONGEST SIDE .

9. DIELECTRICS AND LINEWIDTHS MAY BE ADJUSTED TO MEET THE IMPEDANCE REQUIREMENTS.

10. MINIMUM CONDUCTOR WIDTH: 5 MILS
MINIMUM SPACING: 5.5 MILS



SENSITIVE ELECTRONIC DEVICES-CLASS 1

DIFFERENTIAL IMPEDANCE DETAILS

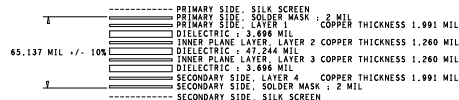
LAYER NO.	CONDUCTOR WIDTH	SPACING	IMPEDANCE	TOLERANCE
1	5 MILS	5.648 MILS	90E	+/-10%
4	5 MILS	5.648 MILS	90E	+/-10%

SINGLE ENDED IMPEDANCE DETAILS

LAYER NO.	CONDUCTOR WIDTH	IMPEDANCE	TOLERANCE
1	5.99 MILS	50E	+/-10%
4	5.99 MILS	50E	+/-10%



LAYER STACK-UP



DRILL CHART: TOP to BOTTOM

ALL UNITS ARE IN MILS

FIGURE	SIZE	TOLERANCE	PLATED	QTY
A	10.0	+3.0/-3.0	PLATED	42
*	12.0	+3.0/-3.0	PLATED	98
*	40.0	+3.0/-3.0	PLATED	4
*	41.0	+3.0/-3.0	PLATED	6
*	35.0	+3.0/-3.0	NON-PLATED	2
F	163.0	+3.0/-3.0	NON-PLATED	4