
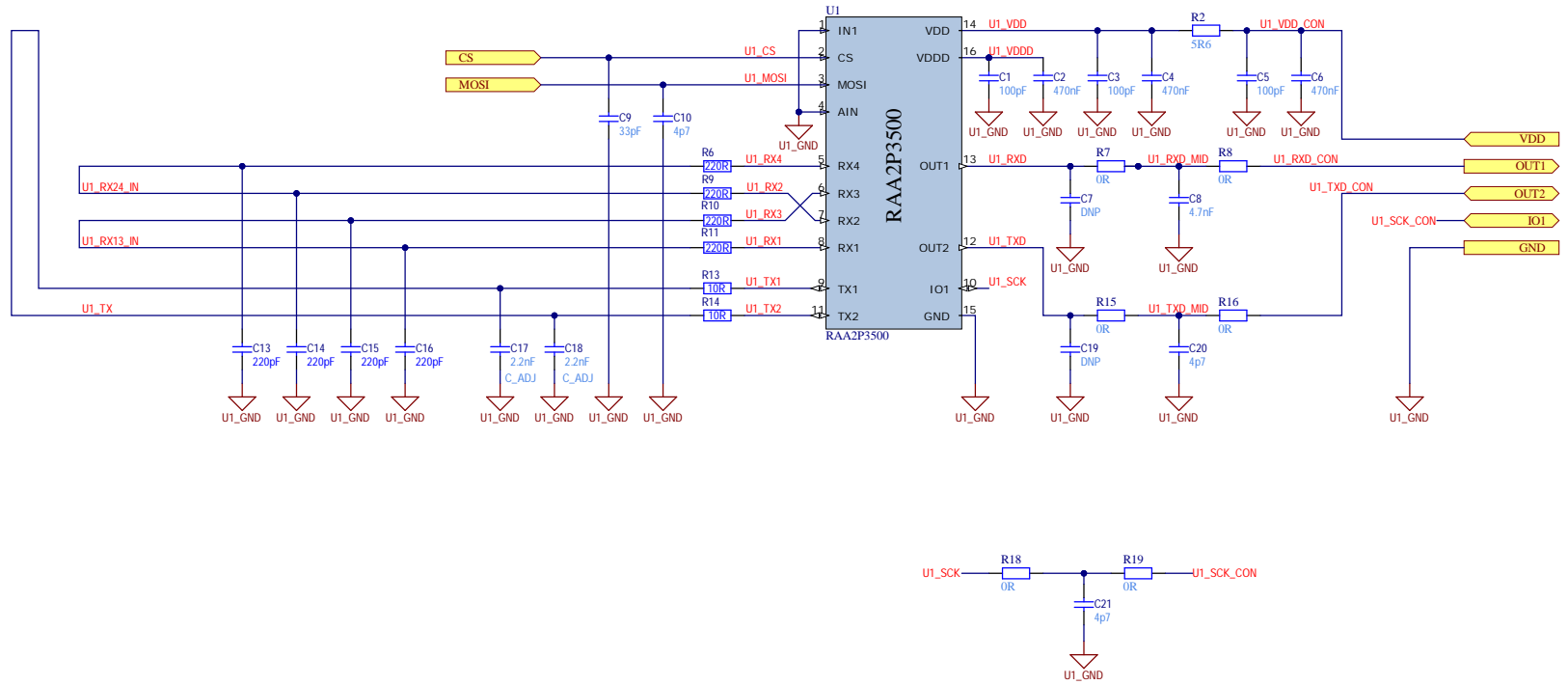


Project name				RAA2P3500R0400.PjPcb		Version	1.0				
Sheet title				RAA2P3500R0400		Size	A3				
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L = 2 μH



$$f_{TX} = \frac{1}{2\pi\sqrt{L_{TX}C_{TX}}} \rightarrow C_{TX} = \frac{1}{(2\pi f_{TX})^2 L_{TX}}$$

C_ADJ = To be adjusted according to coil inductivity. Calculate C_TX with the given formula.
C_ADJ = 2 x C_TX
It's recommended to use COG or NP0 ceramic capacitors
Use dielectric strength Vr >= 50V capacitors for (C9 - C19 and C30 - C40)

L = 2 μH

Ftx = 3.5 MHz

CTX = 1.033 nF

C_ADJ = 2.067 nF

○ Fiducial
○ Fiducial
Silicone feet
FT1 FT2 FT3 FT4
Mechanical
ZCL1ZSH1ZKN1
ESD Bag
ZB1

Project name	RAA2P3500R0400.PrjPcb	Version	1.0	RENESAS
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Date	10.9.2025	System Application Engineering Team	Drawn by	S.HOEFLER
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