

1	2	3	4	5	6	7	8	9	
						REV	COMMENT	DATE	DRAWN BY
						1.0	Initial Release	21/04/2021	SDD/KSS
						3.2	Changed values of R308 and R322	12/05/2022	SDD/KSS
						3.3	Aligned with BoM v0750	22/09/2022	SDD/KSS
						3.4	Replaced VSC8541XMV-02 with -05	08/09/2023	SDD/KSS

RENESAS

RZ

RSK RZ/N2L

Table of Contents:

Page 1: Title Page

Page 2: RZN2L Microprocessor [POWER]

Page 3: RZN2L Microprocessor [IO]

Page 4: RZN2L Microprocessor [IO] B

Page 5: PSU

Page 6: User Interface

Page 7: USB

Page 8: PMOD, Grove and QWIIC Connections

Page 9: mikroBUS Connection

Page 10: J-Link On Board with S124

Page 11: SDRAM and NOR Flash

Page 12: SPI Flash Memory

Page 13: Pin Headers

Page 14: Ethernet P0

Page 15: Ethernet P1

Page 16: Ethernet P2

Page 17: Application Headers

Page 18: CAN and RS485

Page 19: USB to Serial

Page 20: Option Settings

Page 21: Option Settings B

1

TP4

Fiducial_0.5mm round

1

TP5

Fiducial_0.5mm round

Rubber Foot

MP1

Rubber Foot

MP2

Rubber Foot

MP3

Rubber Foot

MP4

RENESAS

Electronics Europe GmbH

Title

RSK+RZ/N2L

Title Page

Size

A3

Document Number

D017892_04

Date:


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
PAGE 1 OF 21

ISSUE 3.4


Table of Contents:

Page 1: Title Page
Page 2: RZN2L Microprocessor [POWER]
Page 3: RZN2L Microprocessor [IO]
Page 4: RZN2L Microprocessor [IO] B
Page 5: PSU
Page 6: User Interface
Page 7: USB
Page 8: PMOD, Grove and QWIIC Connections
Page 9: mikroBUS Connection
Page 10: J-Link On Board with S124
Page 11: SDRAM and NOR Flash
Page 12: SPI Flash Memory
Page 13: Pin Headers
Page 14: Ethernet P0
Page 15: Ethernet P1
Page 16: Ethernet P2
Page 17: Application Headers
Page 18: CAN and RS485
Page 19: USB to Serial
Page 20: Option Settings
Page 21: Option Settings B

1  TP4
Fiducial_0.5mm round

1  TP5
Fiducial_0.5mm round

Rubber Foot MP1
Rubber Foot MP2
Rubber Foot MP3
Rubber Foot MP4

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Title
RSK+RZ/N2L
Title Page

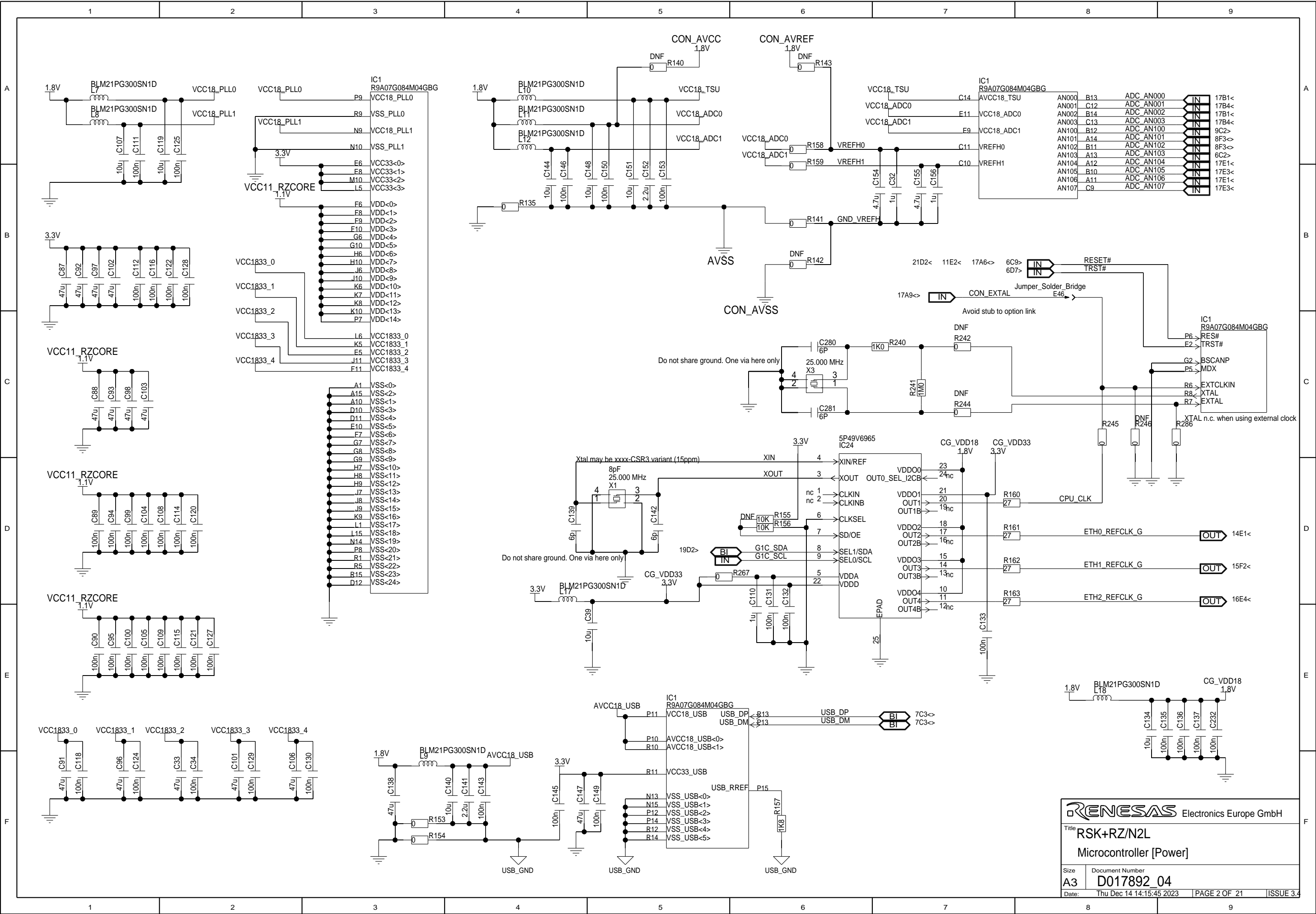
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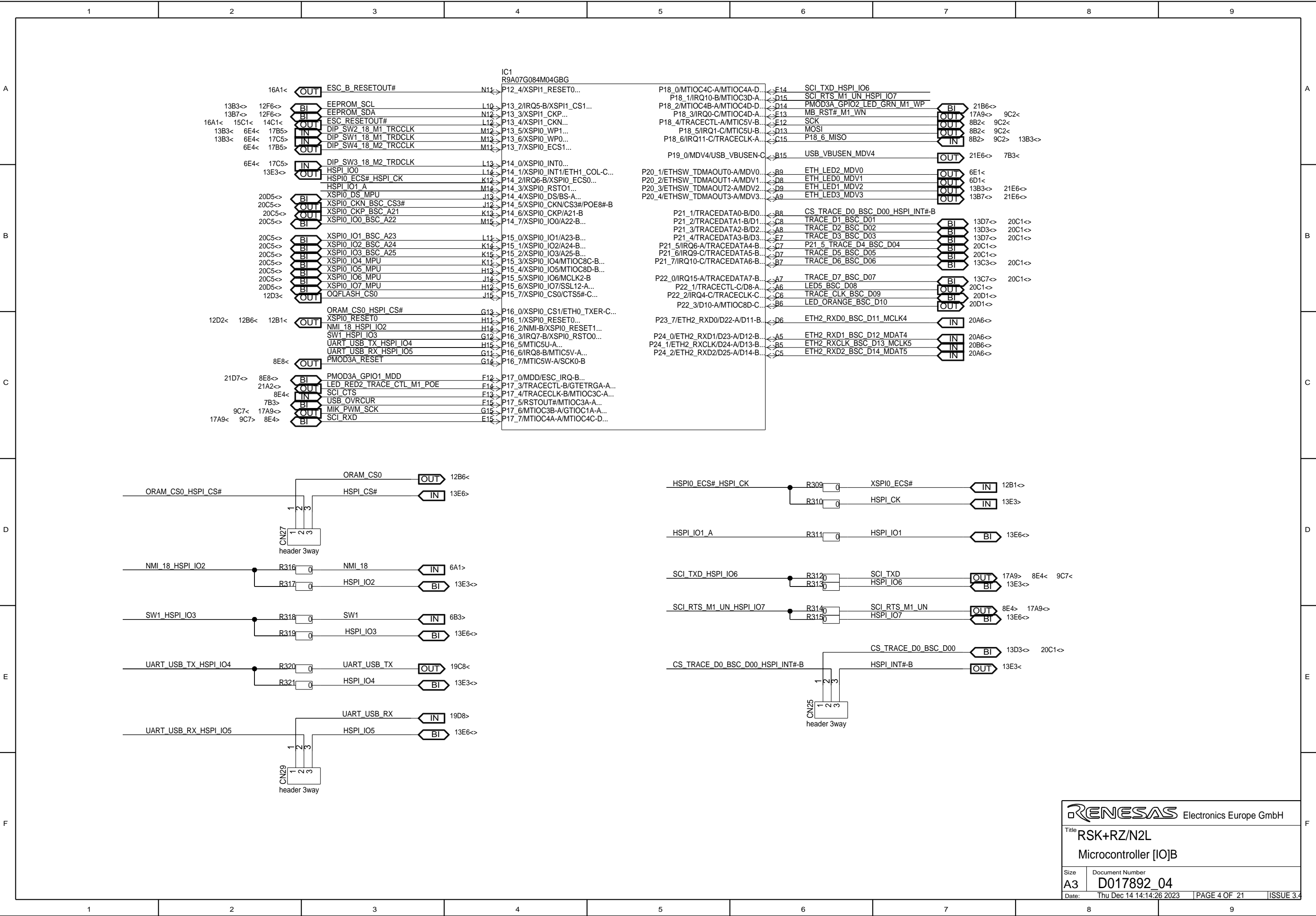
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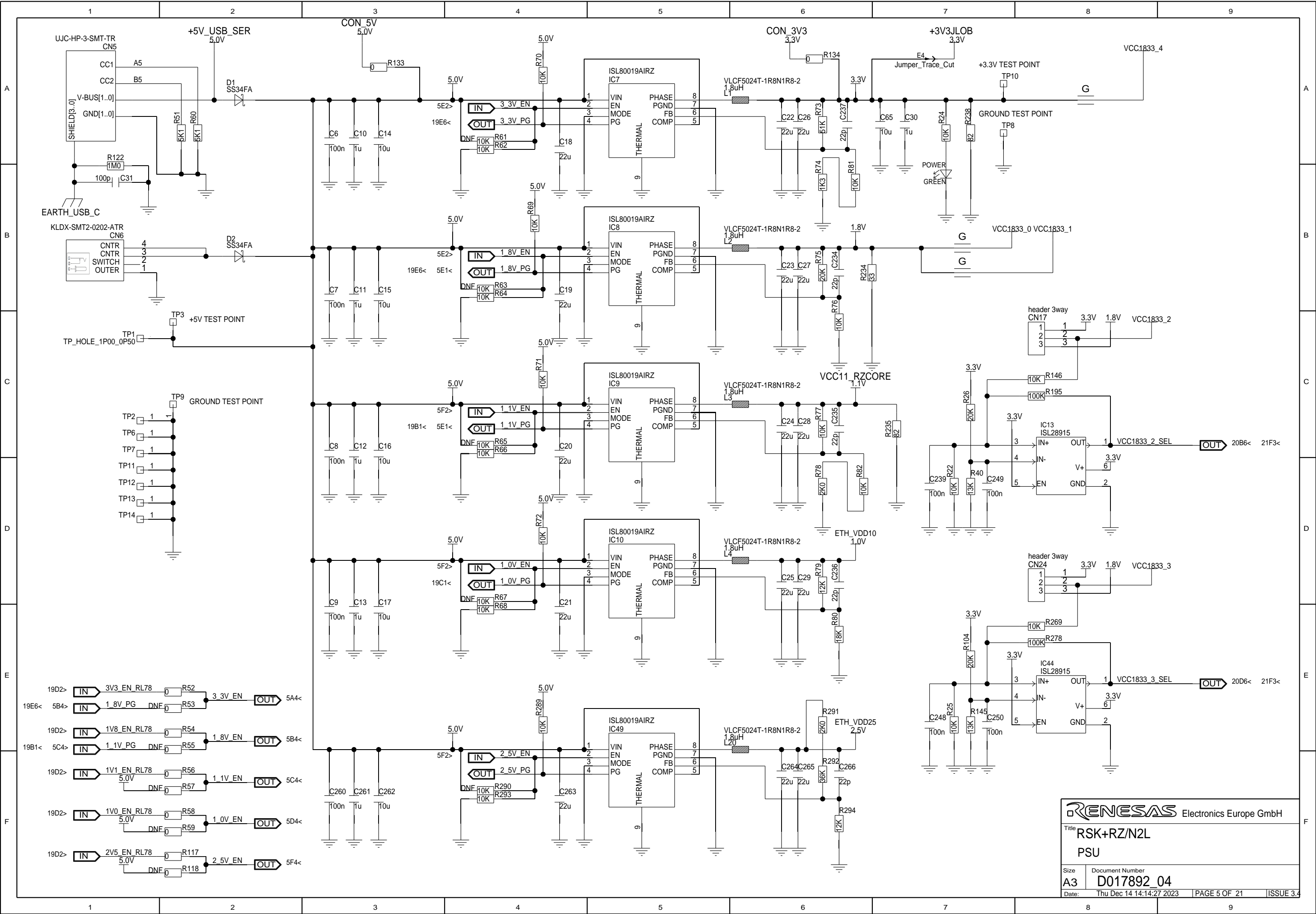
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PAGE 1 OF 21

ISSUE 3.4

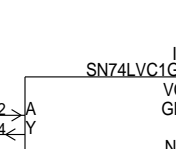
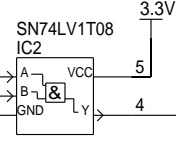
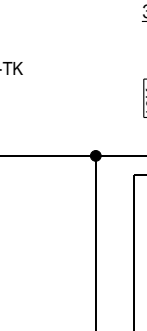
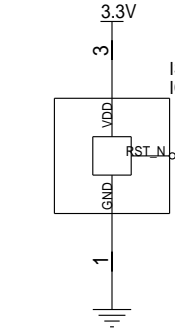
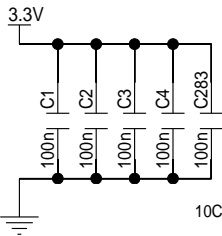
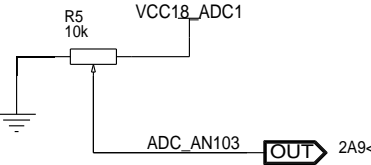
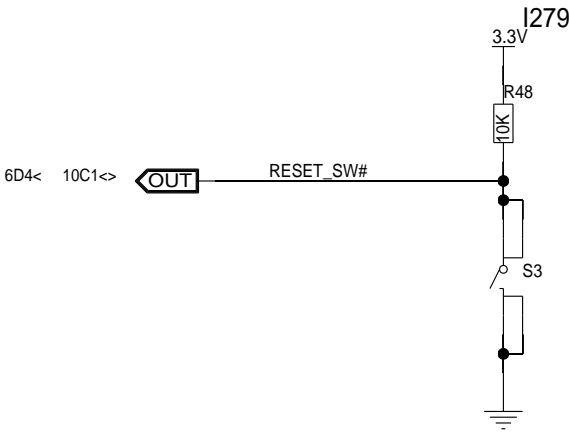
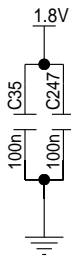
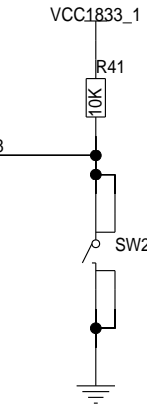
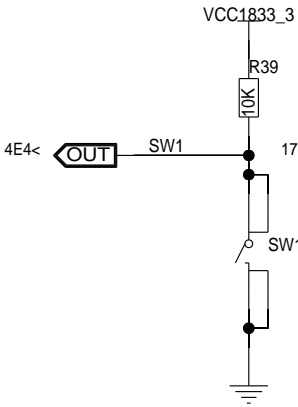
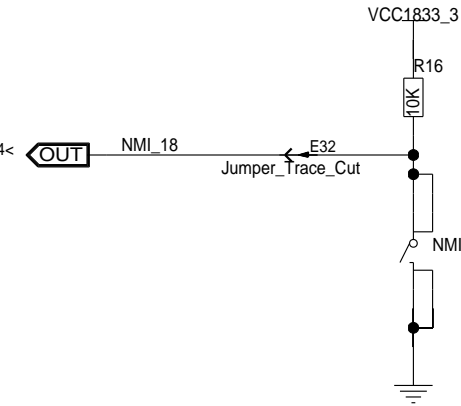






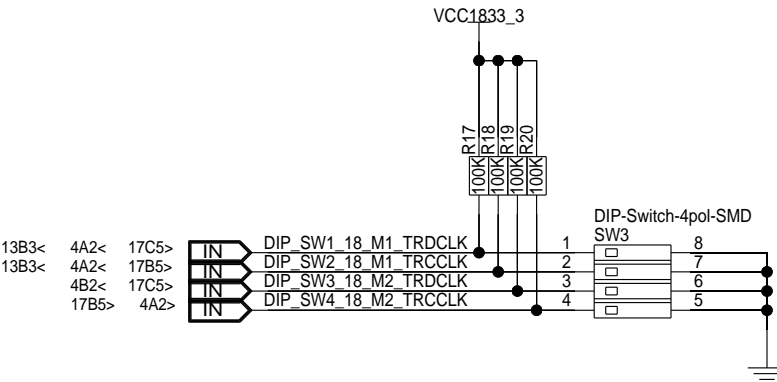
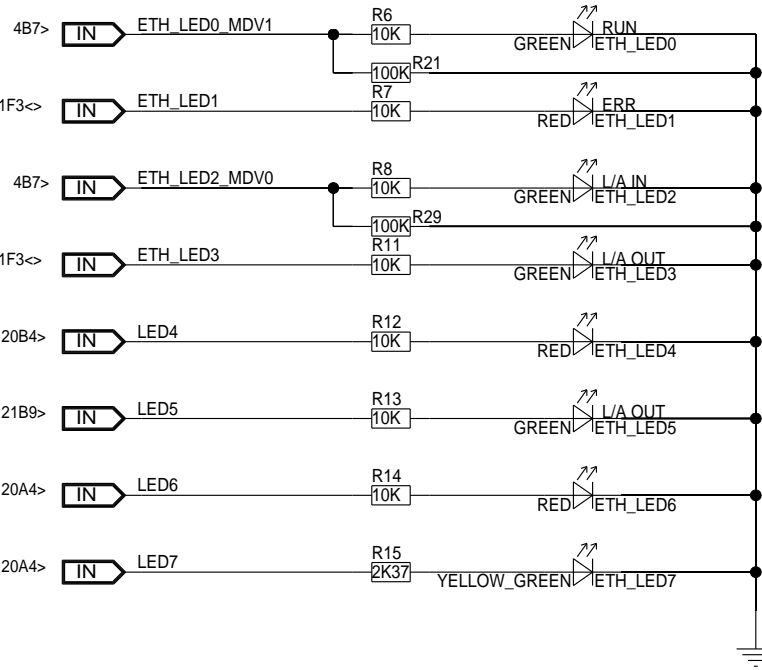
USER PUSH-BUTTONS

RESET PUSH-BUTTON

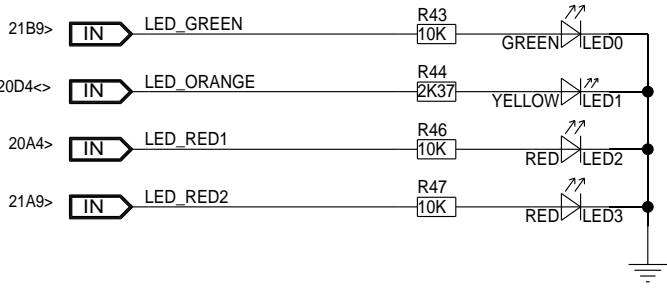


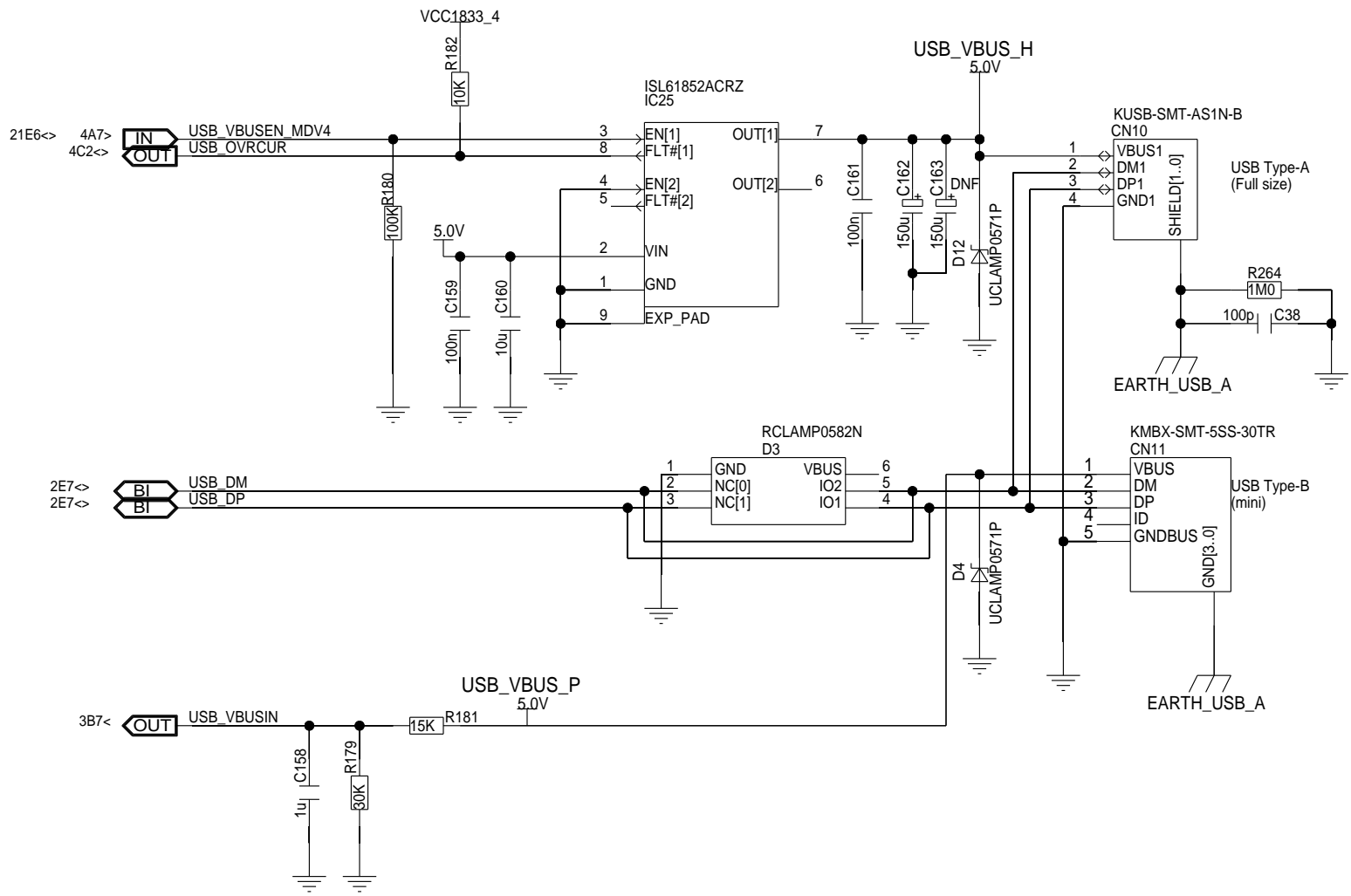
ETHER-CAT LEDS

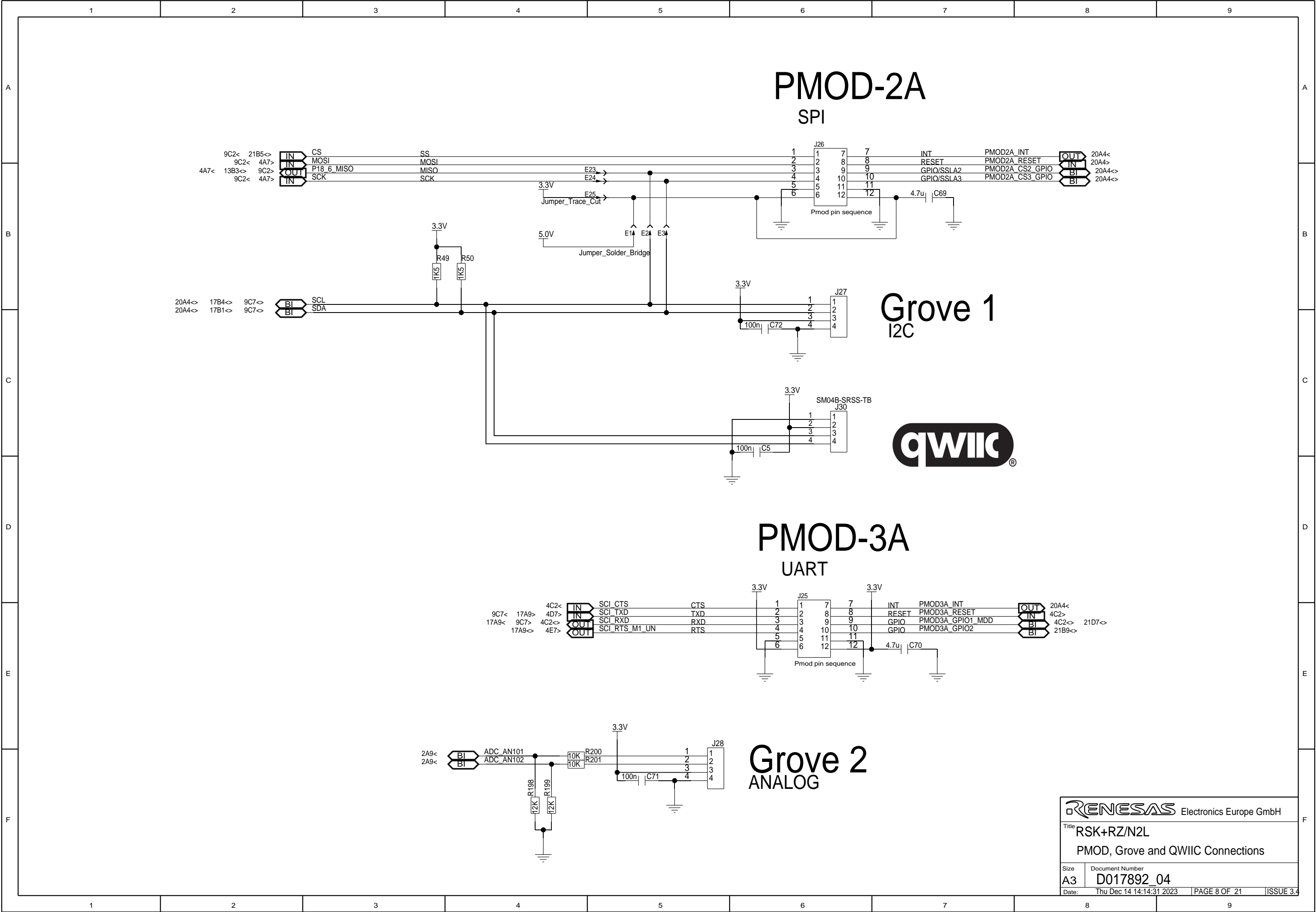
MDV Signals are sampled at reset
Low level (pulled down by resistors)
causes interfaces to be set at 1.8V

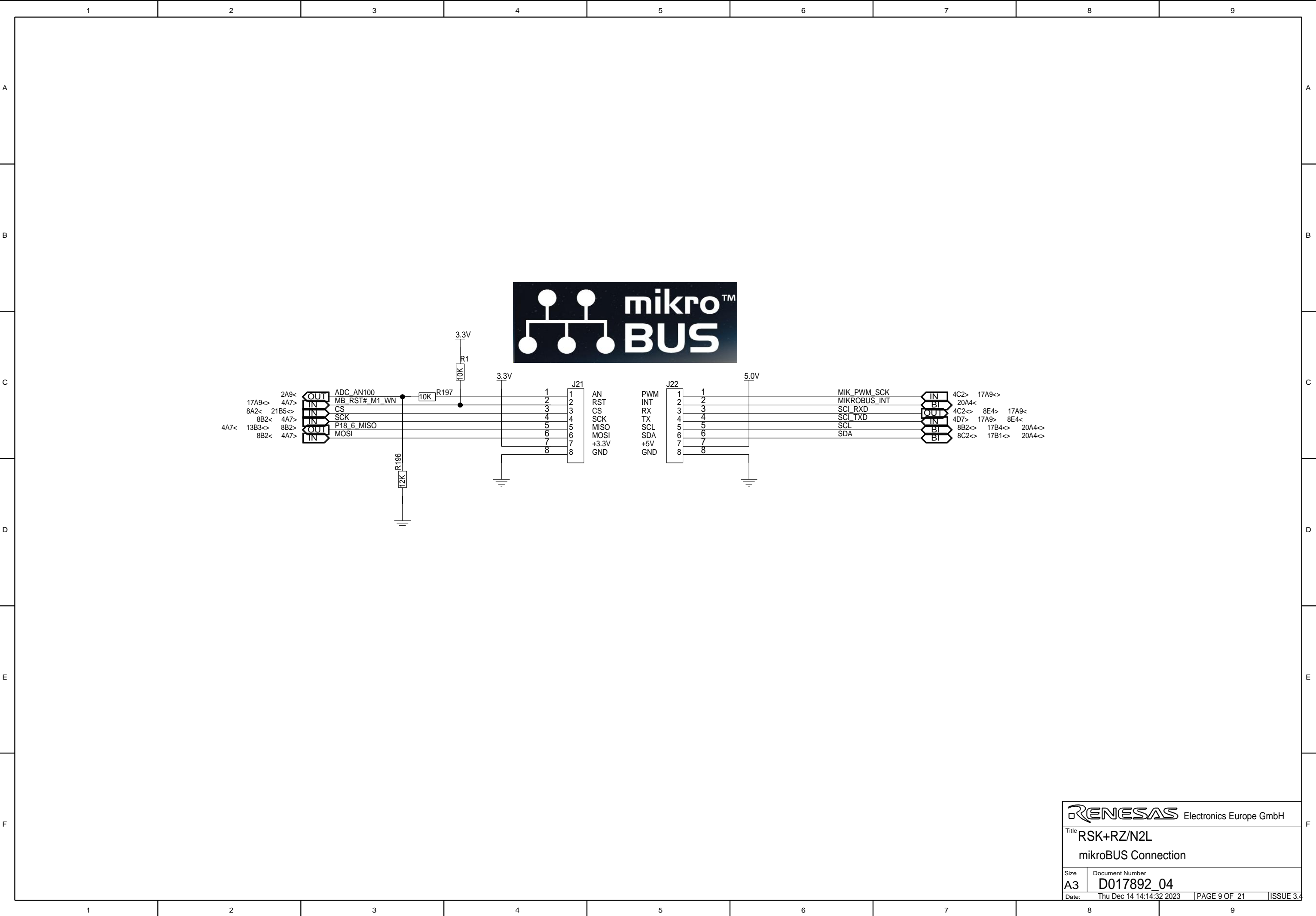


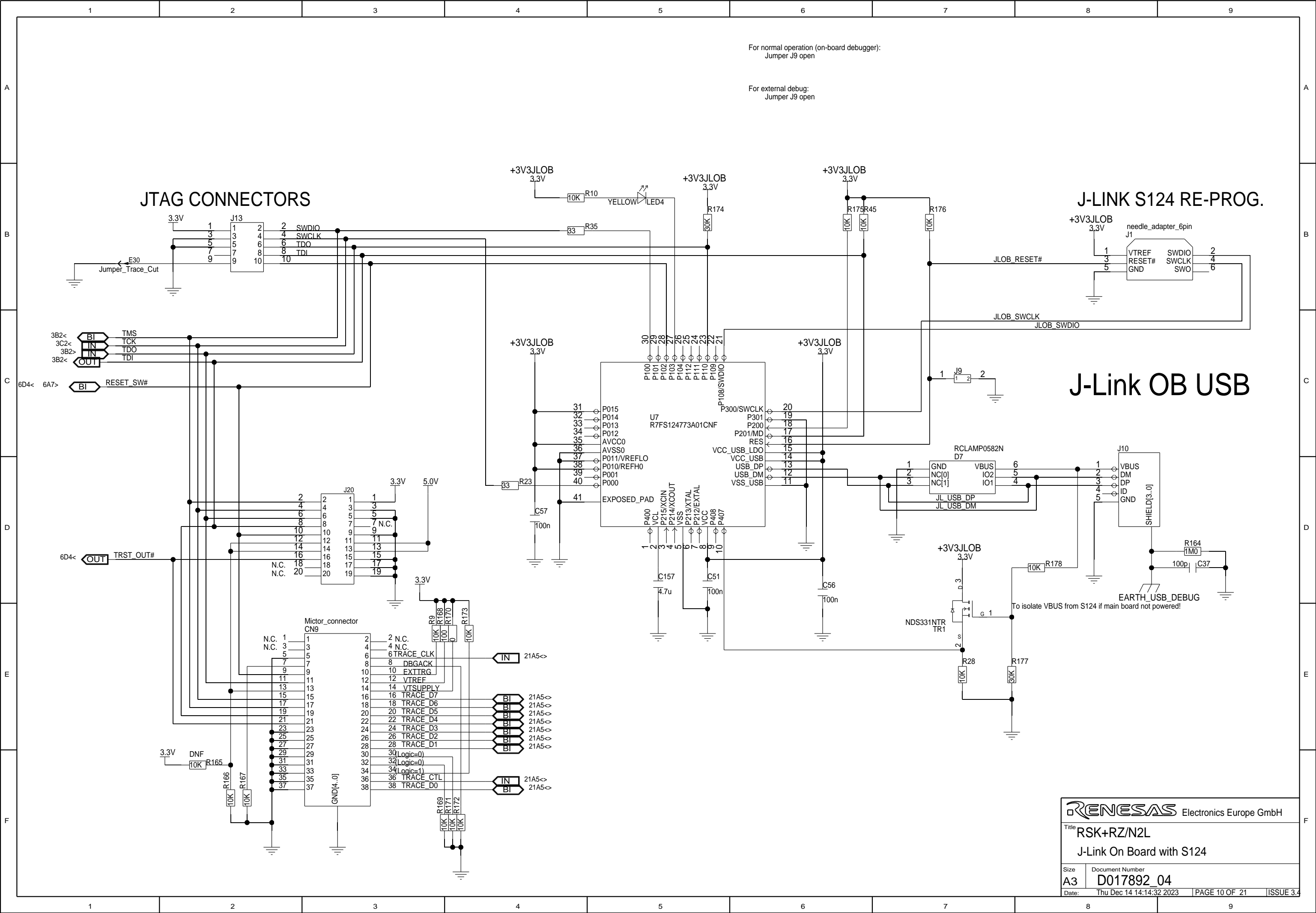
RSK USER LEDS



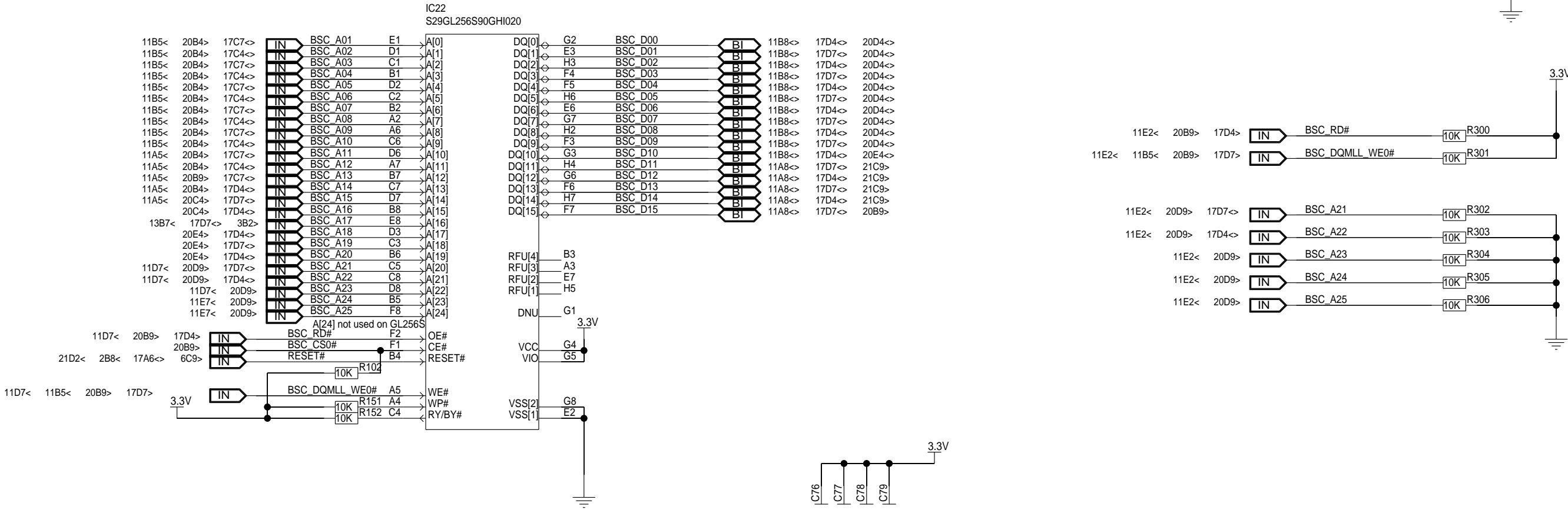
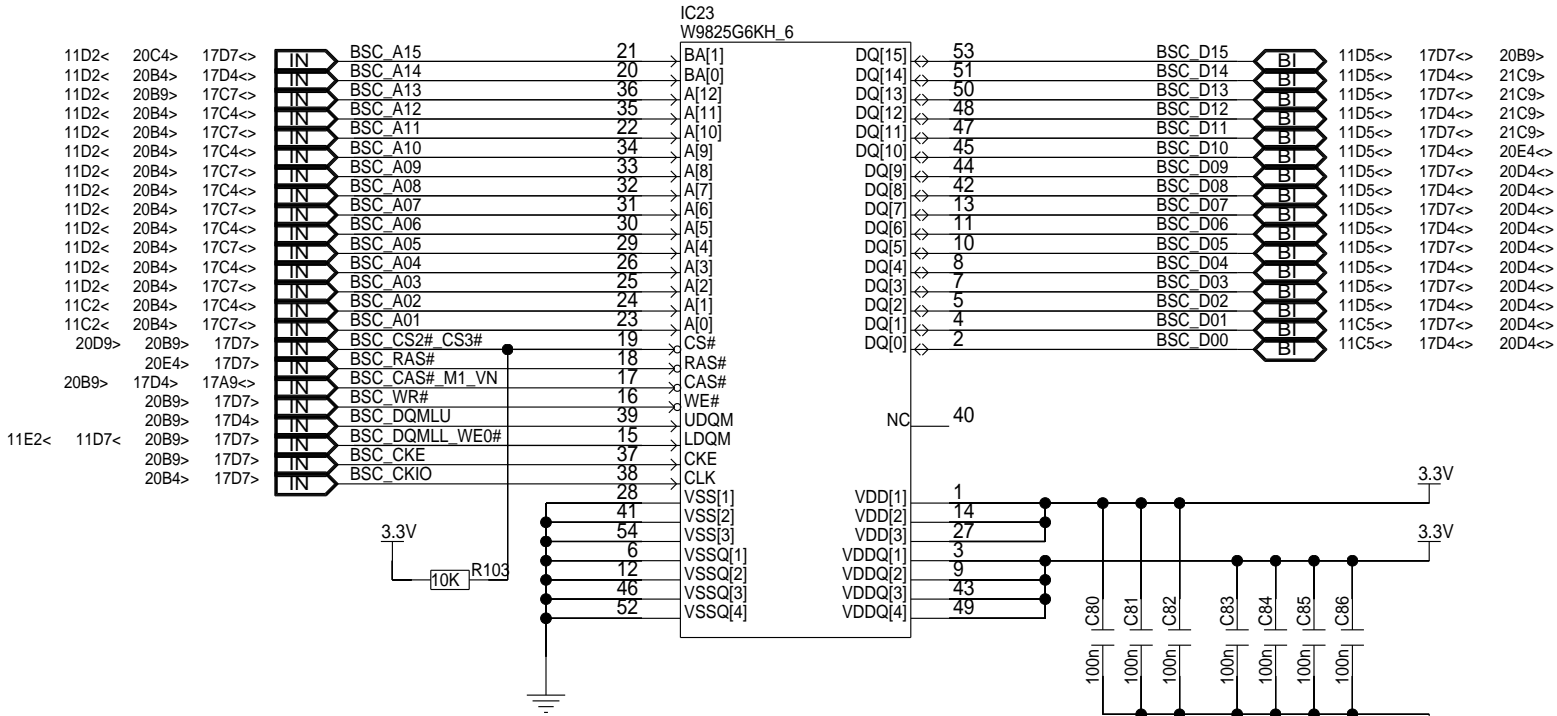








SDRAM: 32MB
NOR Flash: 32MB



Octal SPI Flash: 64MB Octal SPI HyperRAM: 8MB
Quad SPI Flash: 64MB

A

B

C

D

E

F

A

B

C

D

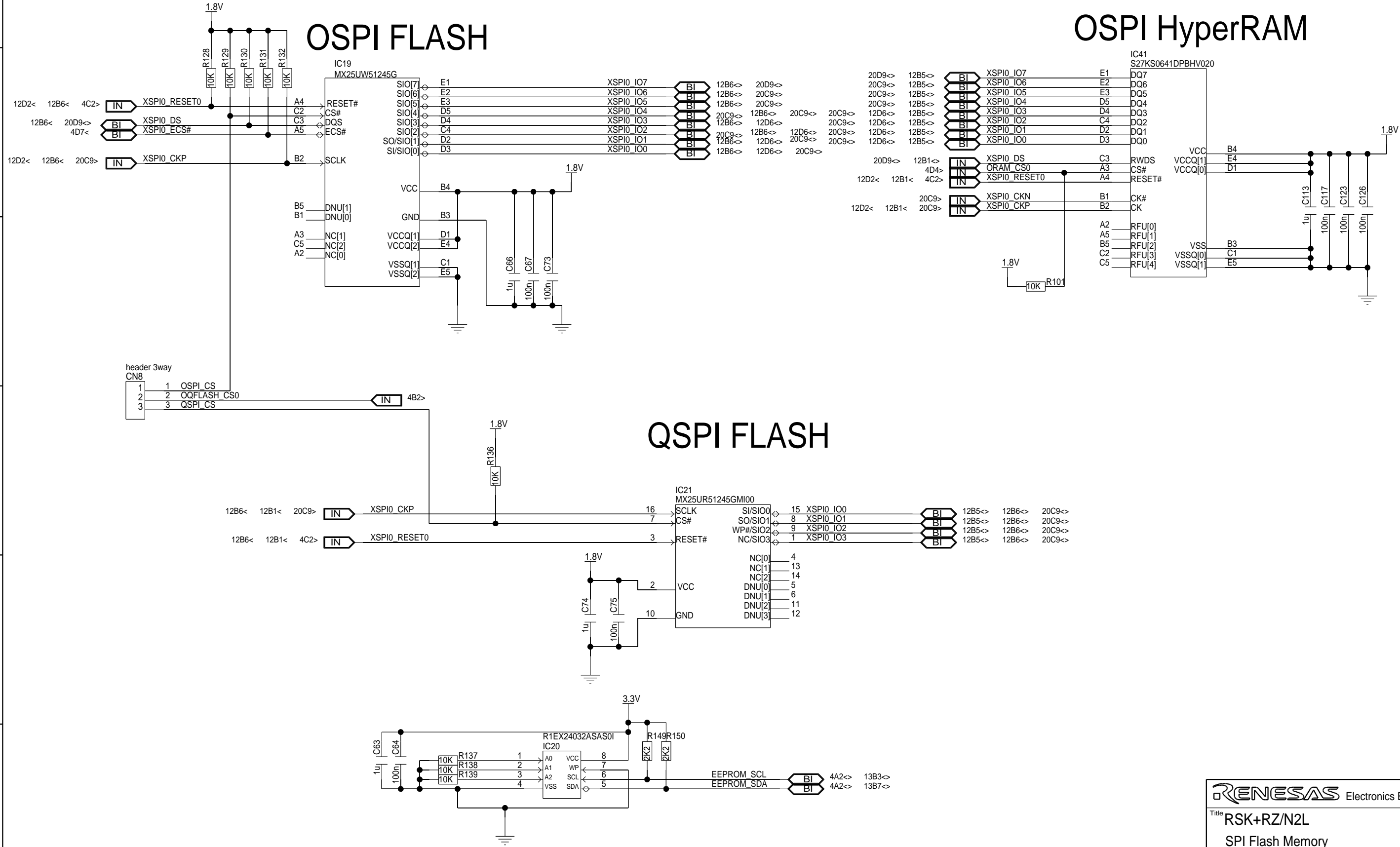
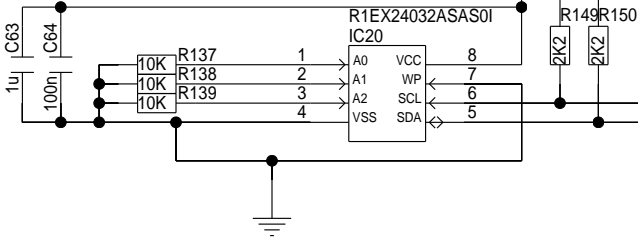
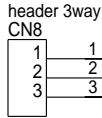
E

F

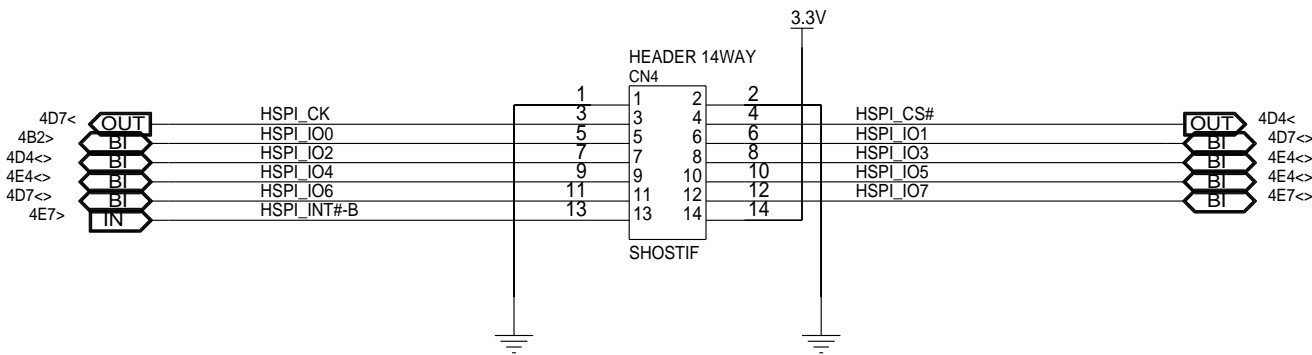
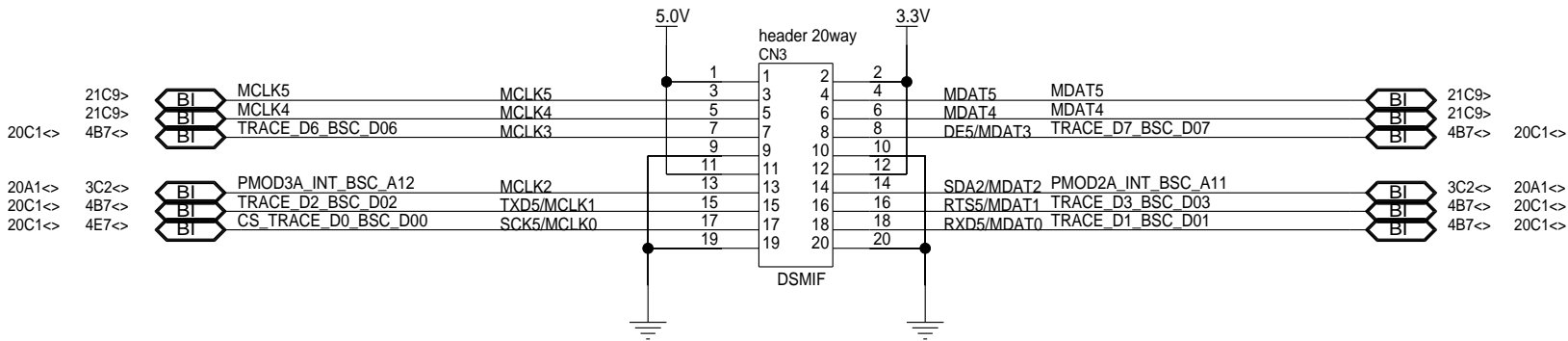
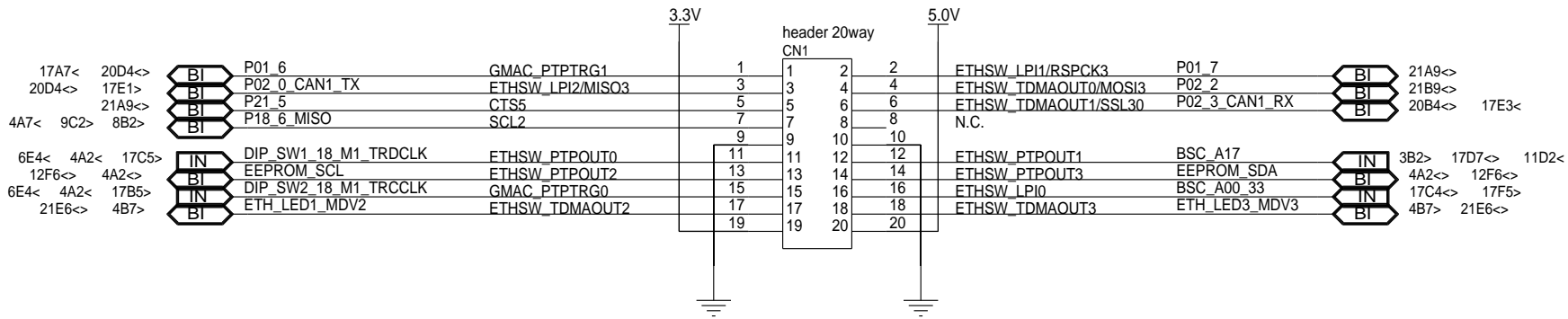
OSPI FLASH

OSPI HyperRAM

QSPI FLASH

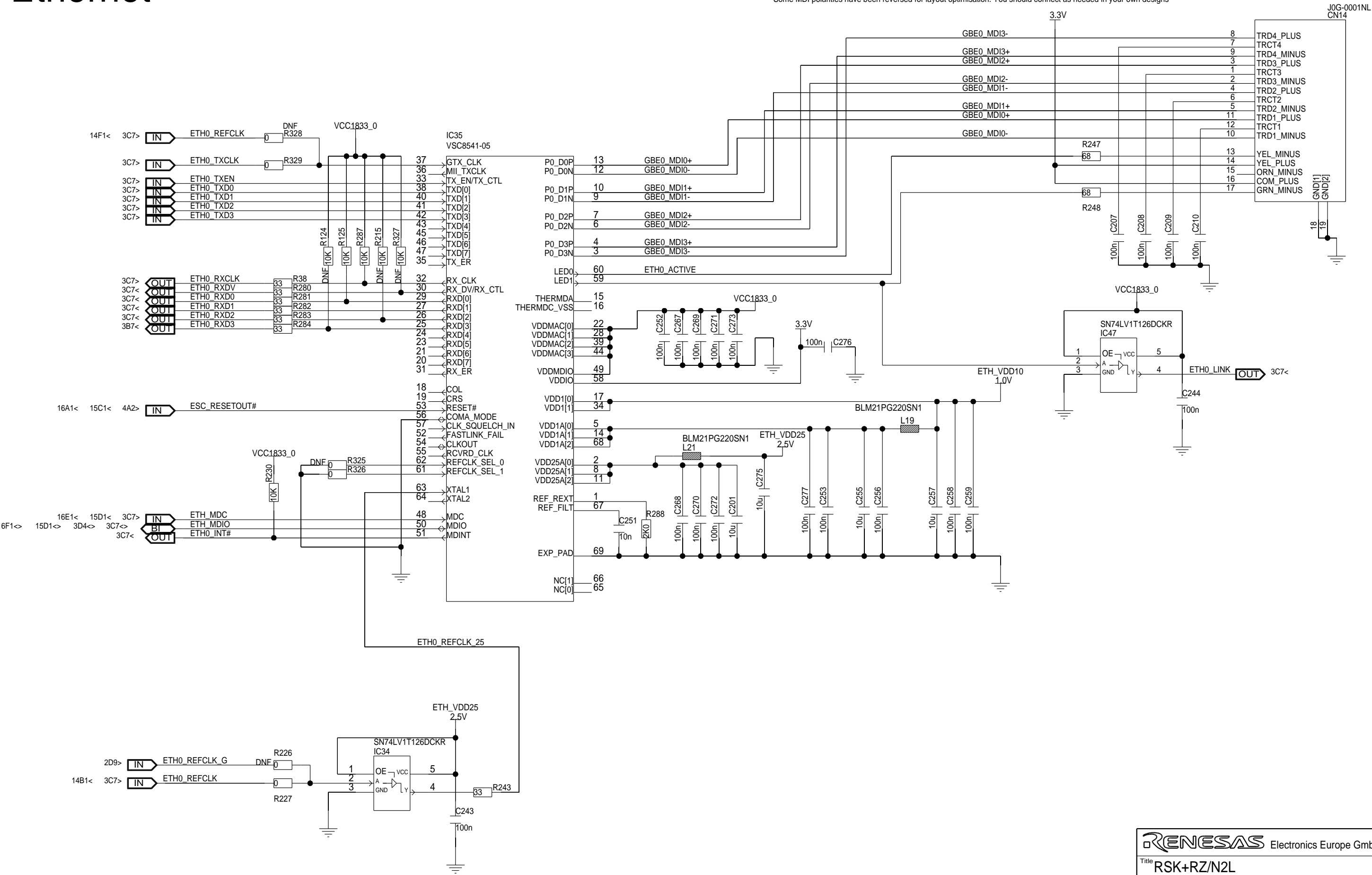


PIN HEADERS



Ethernet

Some MDI polarities have been reversed for layout optimisation. You should connect as needed in your own designs



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Title RSK+RZ/N2L

Ethernet P0

Size

Document Number

A3

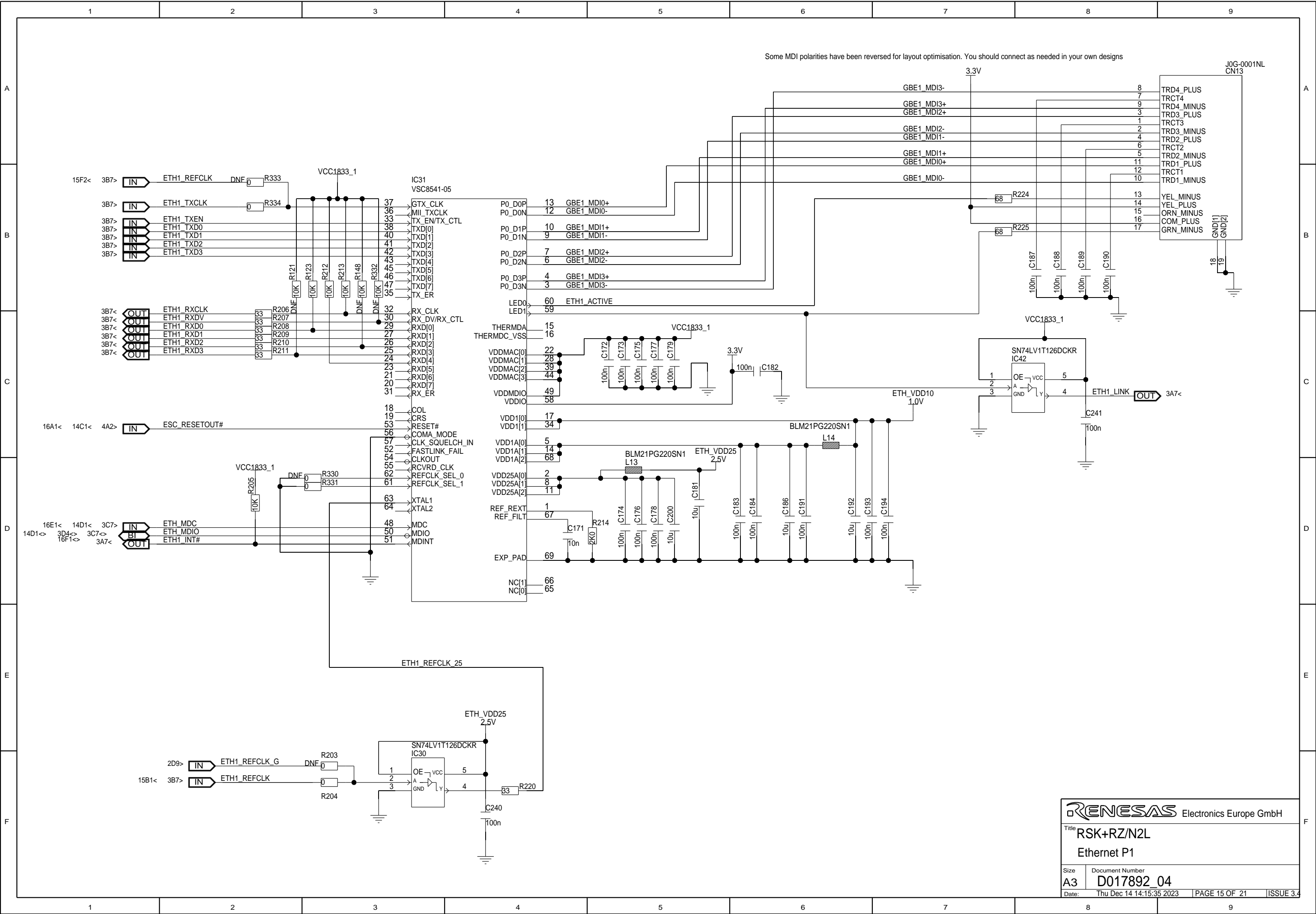
D017892_04

Date:

Thu Dec 14 14:14:35 2023

PAGE 14 OF 21

ISSUE 3.4



Application Headers

A

B

C

D

E

F

A

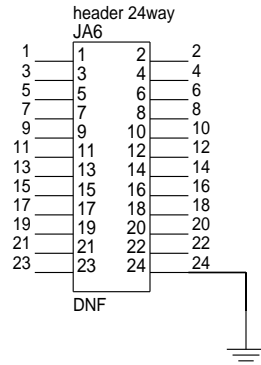
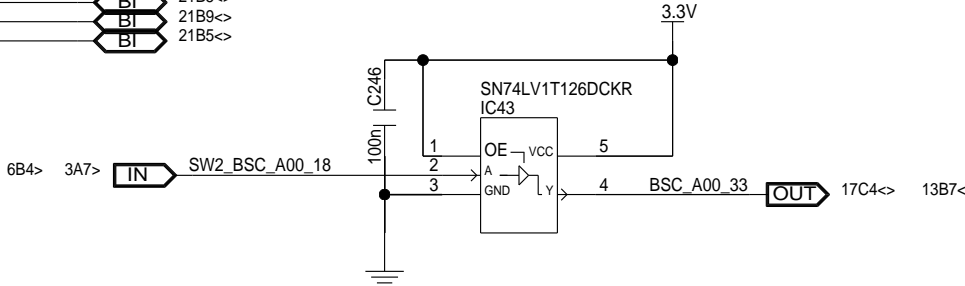
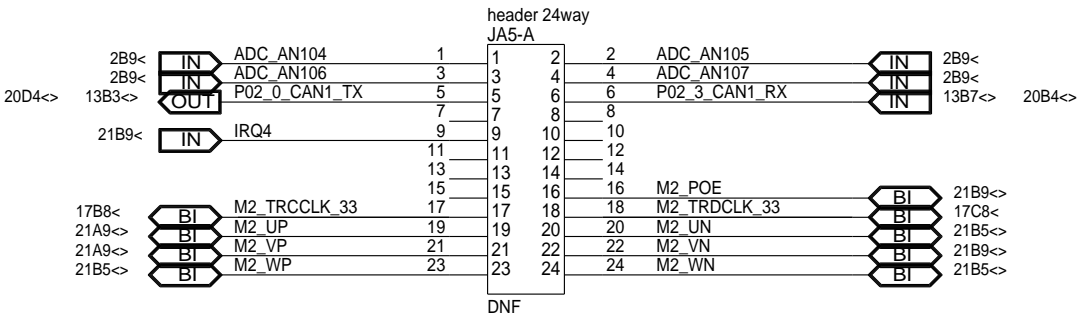
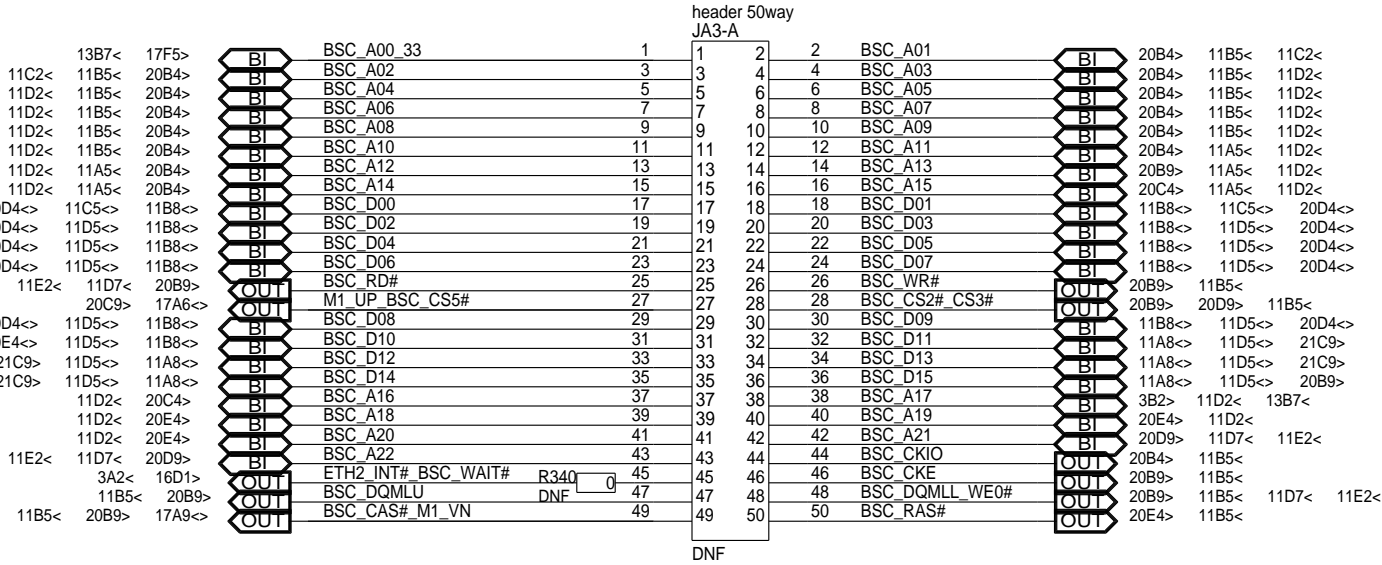
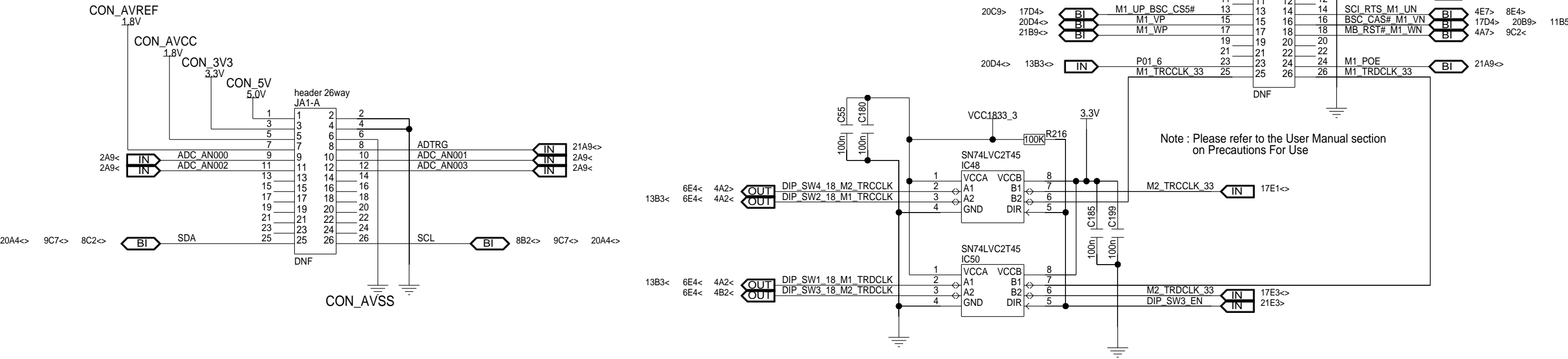
B

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D

E

F



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Title RSK+RZ/N2L

Application Headers

Size

Document Number


A3

D017892_04

Date:

Thu Dec 14 14:14:38 2023 PAGE 17 OF 21 ISSUE 3.4

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Title RSK+RZ/N2L CAN and RS485			
Size	Document Number		
A3	D017892_04		
Date:	Thu Dec 14 14:14:38 2023	PAGE 18 OF 21	ISSUE 3.4

RL78G1C USB to Serial

