

| | | | | | | | | | |
|---|---|---|---|---|---|-----|---------------------------------|------------|----------|
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | |
| | | | | | | REV | COMMENT | DATE | DRAWN BY |
| | | | | | | 1.0 | Initial Release | . | SDD/KSS |
| | | | | | | 5.0 | MP Board | 17/01/2022 | SDD/KSS |
| | | | | | | 5.1 | Changed values for R33 and R294 | 11/05/2022 | SDD/KSS |
| | | | | | | 5.2 | Synchronised with v0520 BoM | 23/09/2022 | SDD/KSS |

RENESAS

RZ

RSK+ RZ/T2M

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1

TP4

Fiducial_0.5mm round

1

TP5

Fiducial_0.5mm round

Rubber Foot

MP1

Rubber Foot

MP2

Rubber Foot

MP3

Rubber Foot

MP4

RENESAS

Electronics Europe GmbH

Title

RSK+RZ/T2M

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A3

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ISSUE 5.3



RSK+ RZ/T2M

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1 TP4
Fiducial_0.5mm round

1 TP5
Fiducial_0.5mm round



Electronics Europe GmbH

Title
RSK+RZ/T2M
Title Page

Size
A3

Document Number
D017882_04

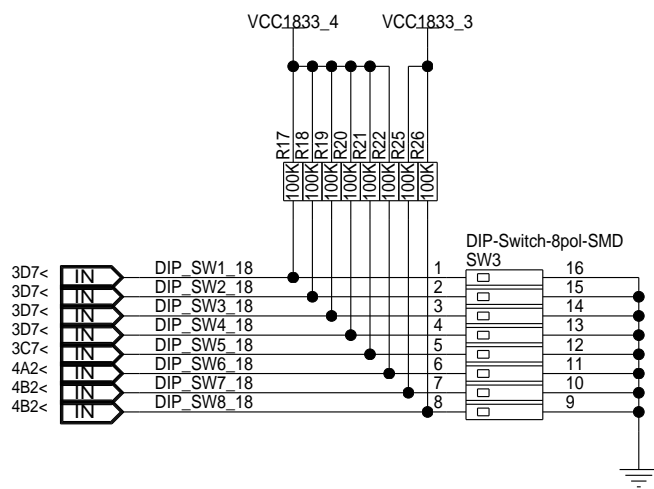
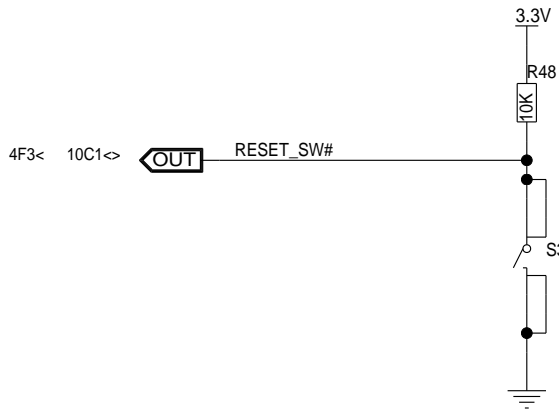
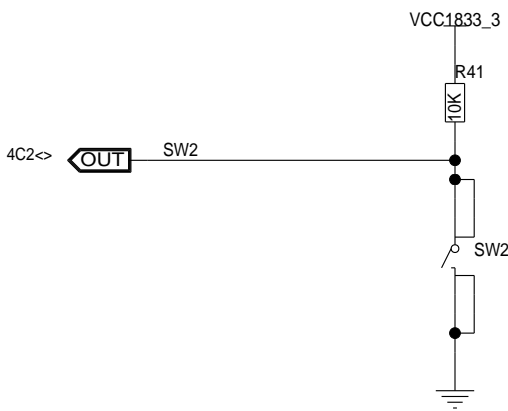
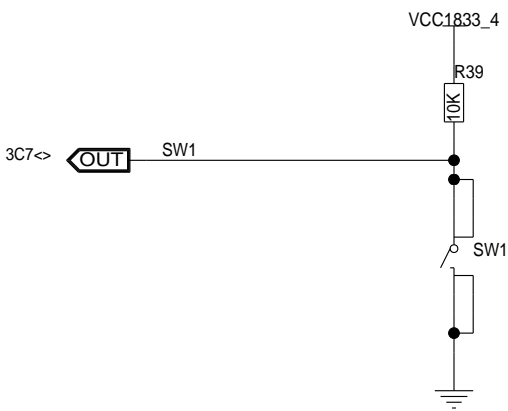
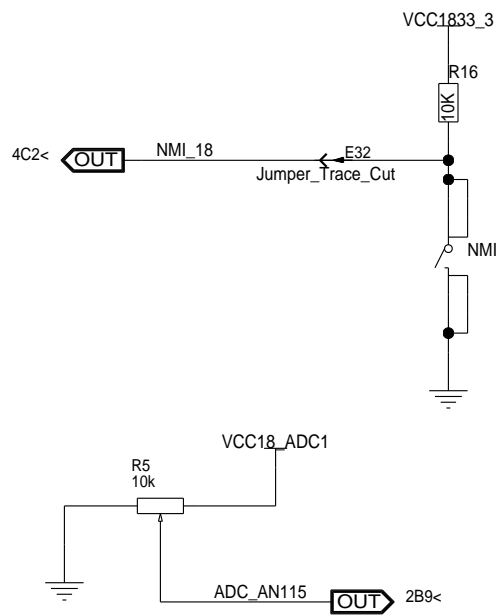
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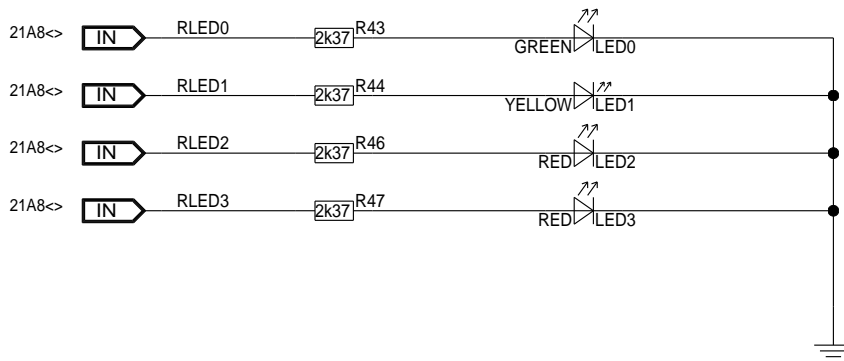
ISSUE 5.2

USER PUSH-BUTTONS

RESET PUSH-BUTTON

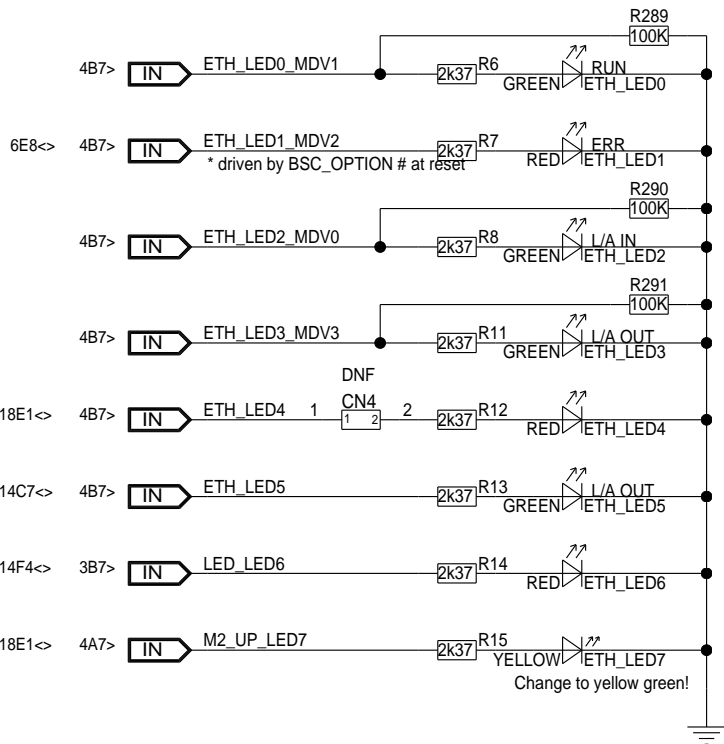


RSK USER LEDS

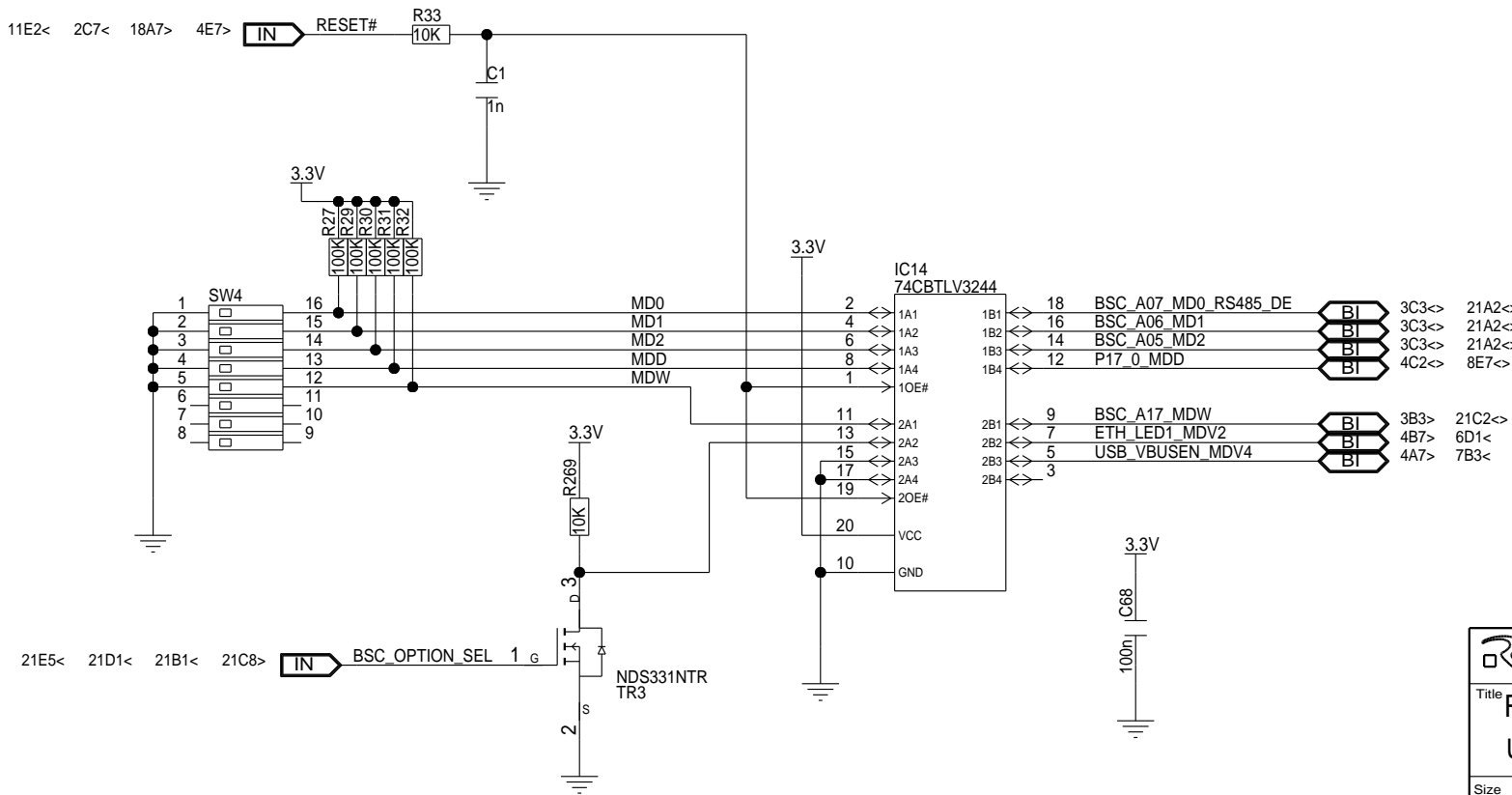


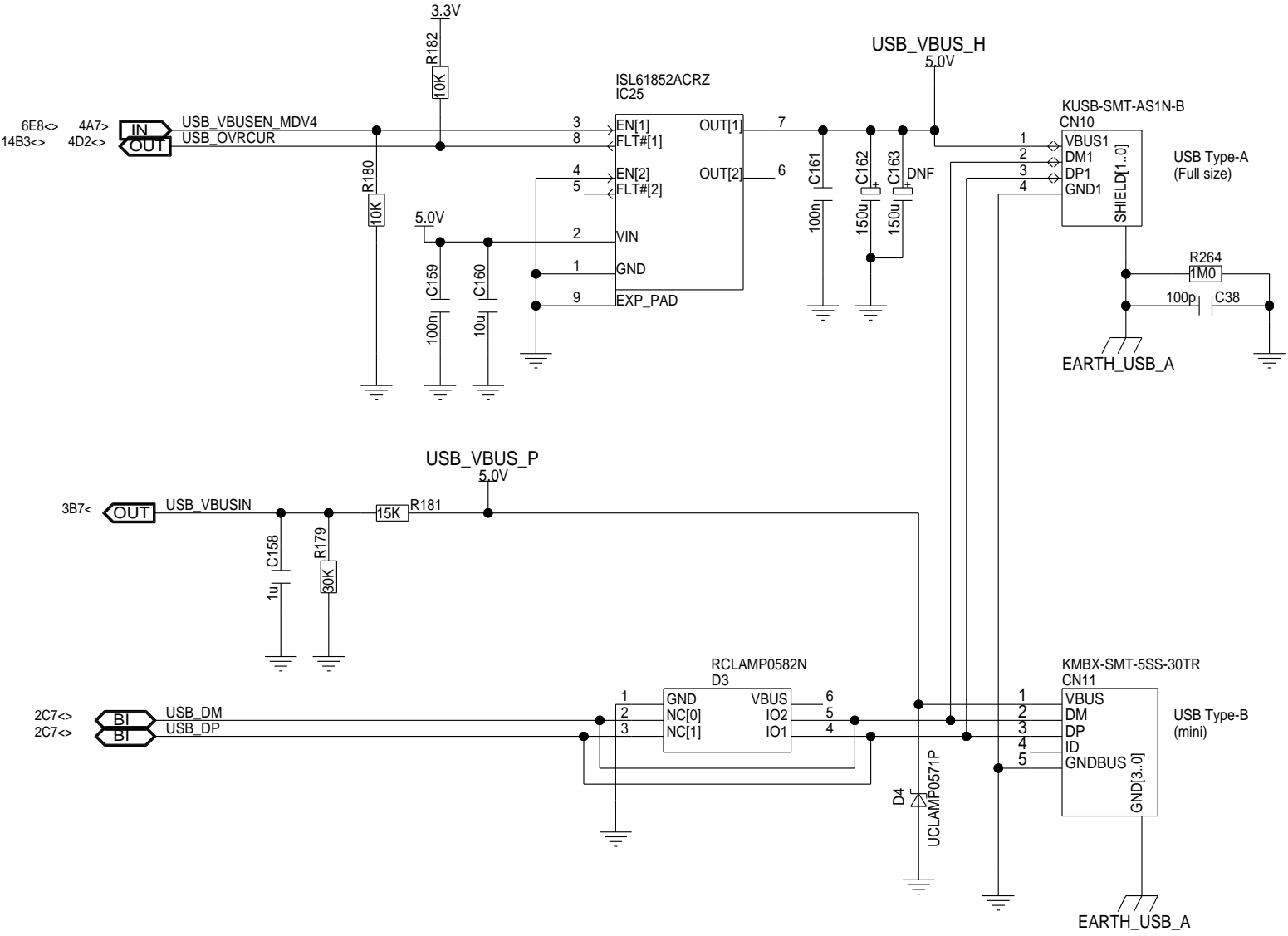
ETHER-CAT LEDS

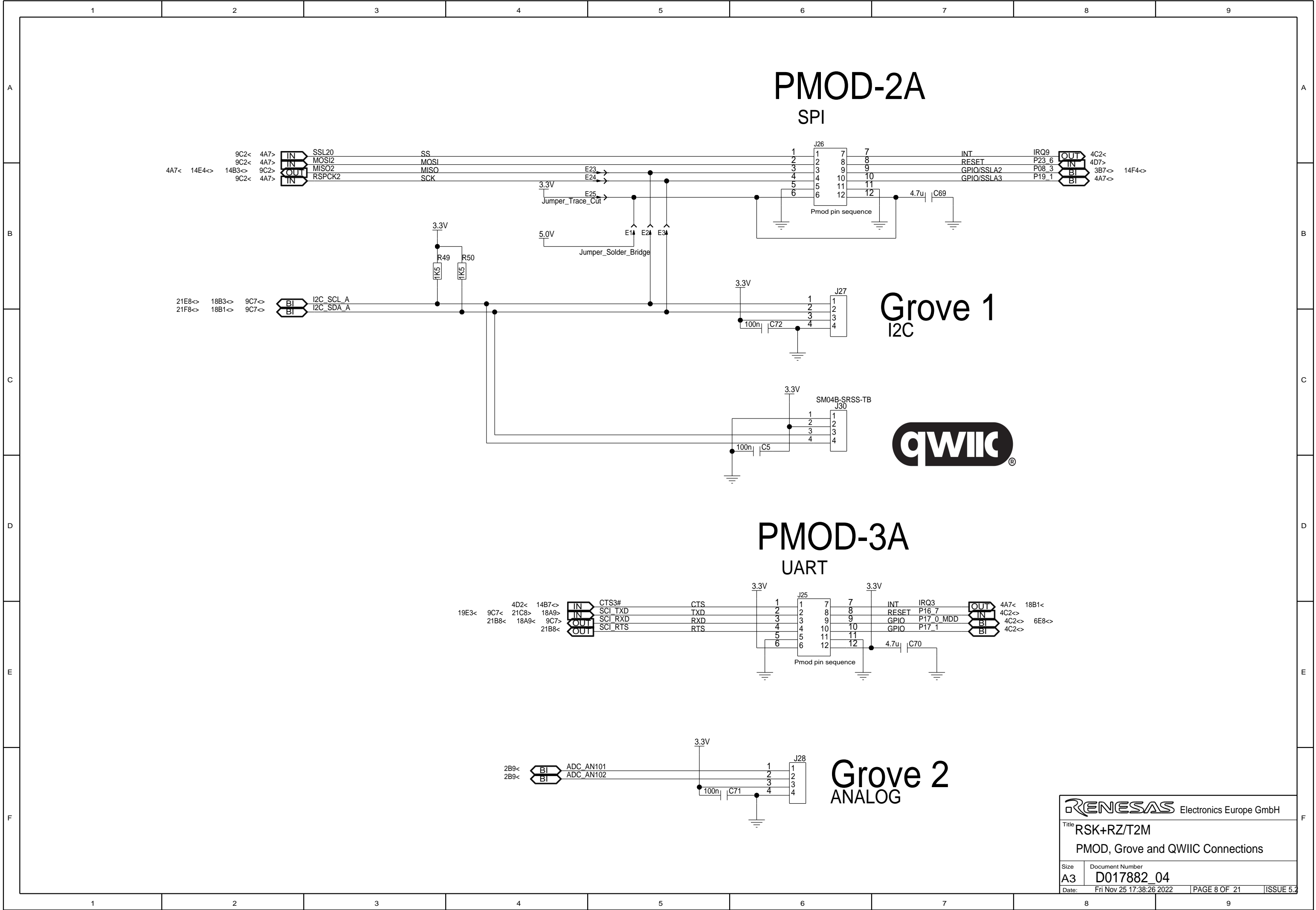
MDV Signals are sampled at reset.
Low level (pulled down by resistors)*
causes interfaces to be set at 1.8V

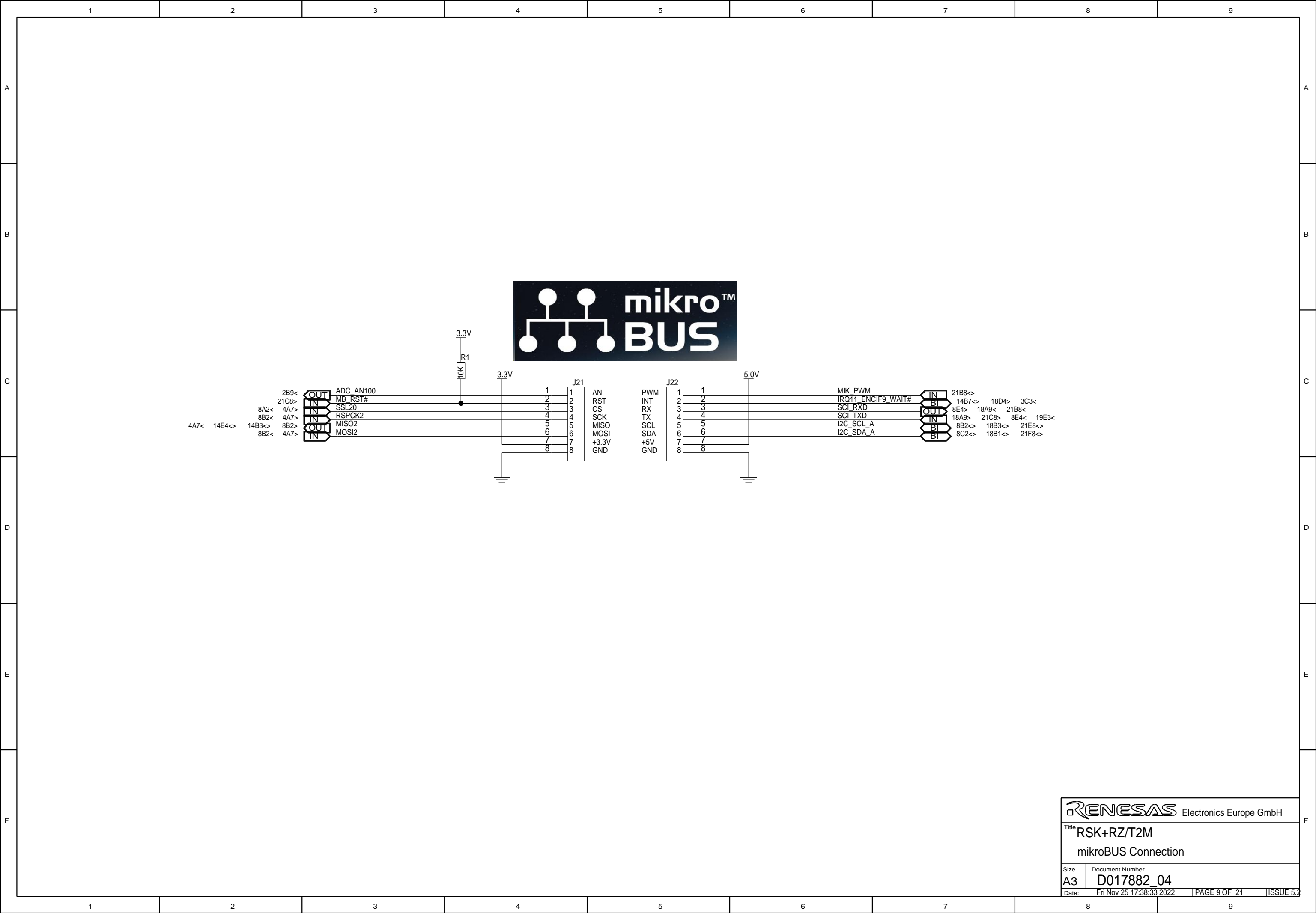


MODE SETTINGS









For external debug
Jumper J9 pins 1-2

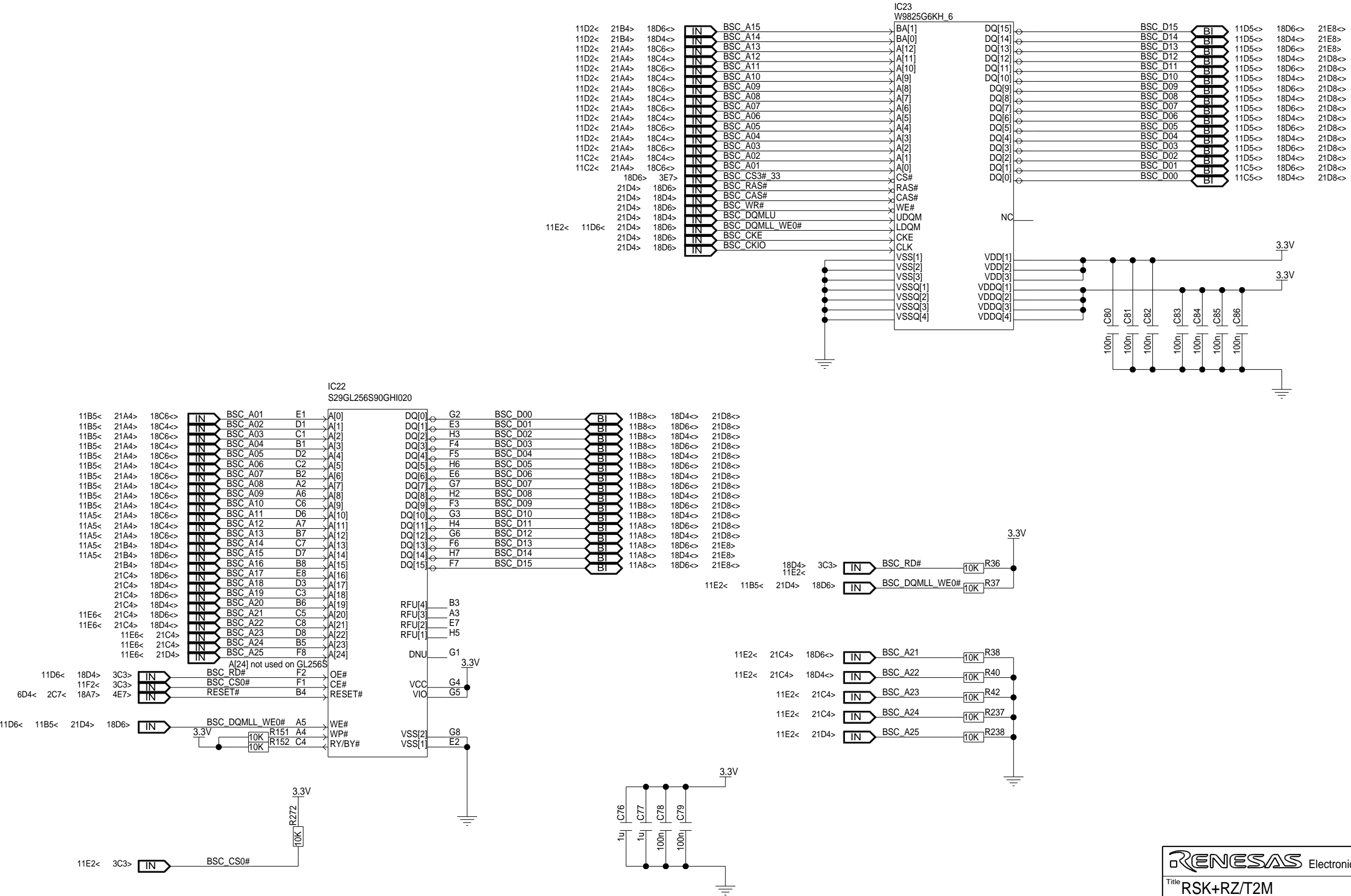
JTAG CONNECTORS

J-LINK S124 RE-PROG.

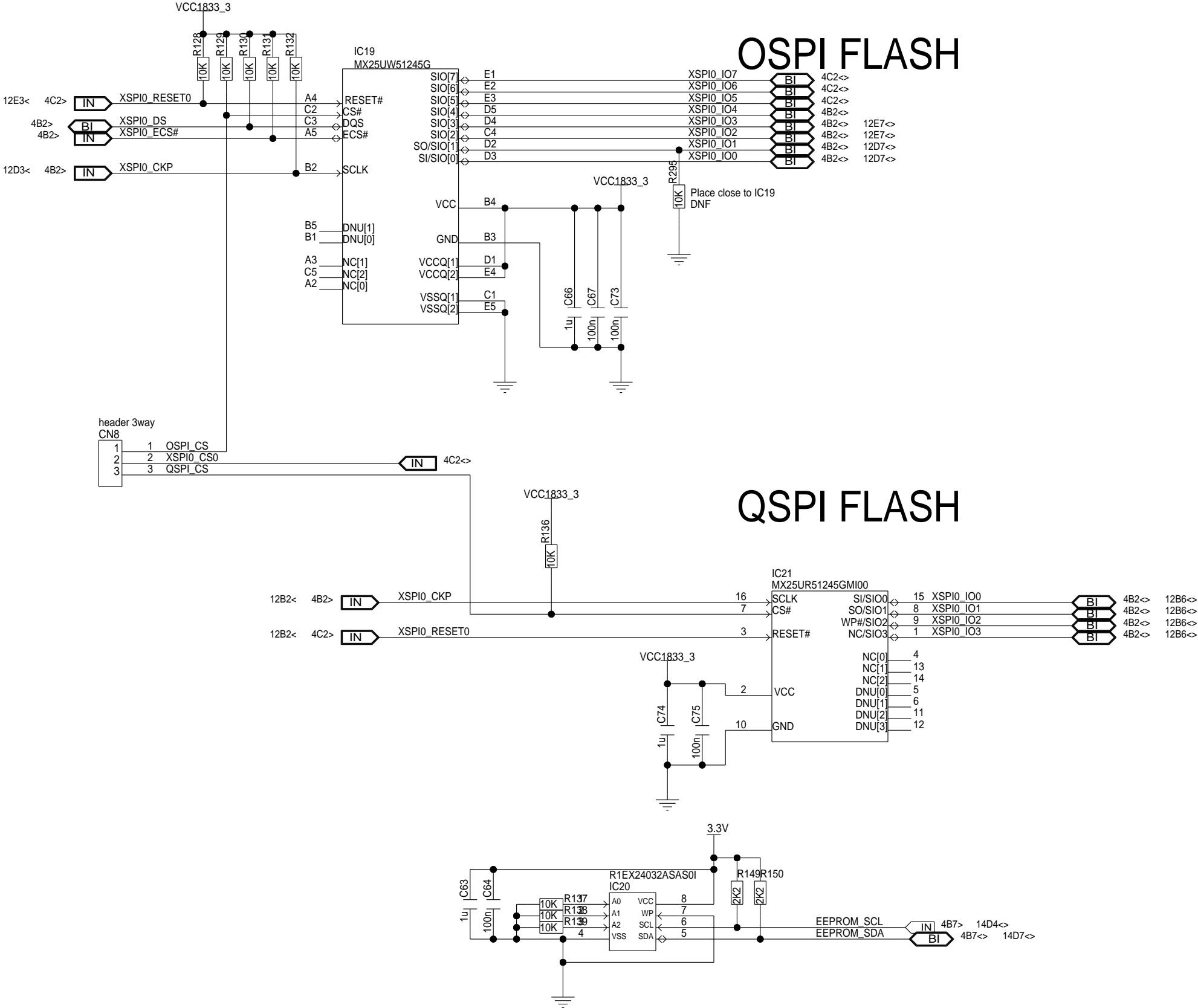
J-Link OB USB

SDRAM: 32MB

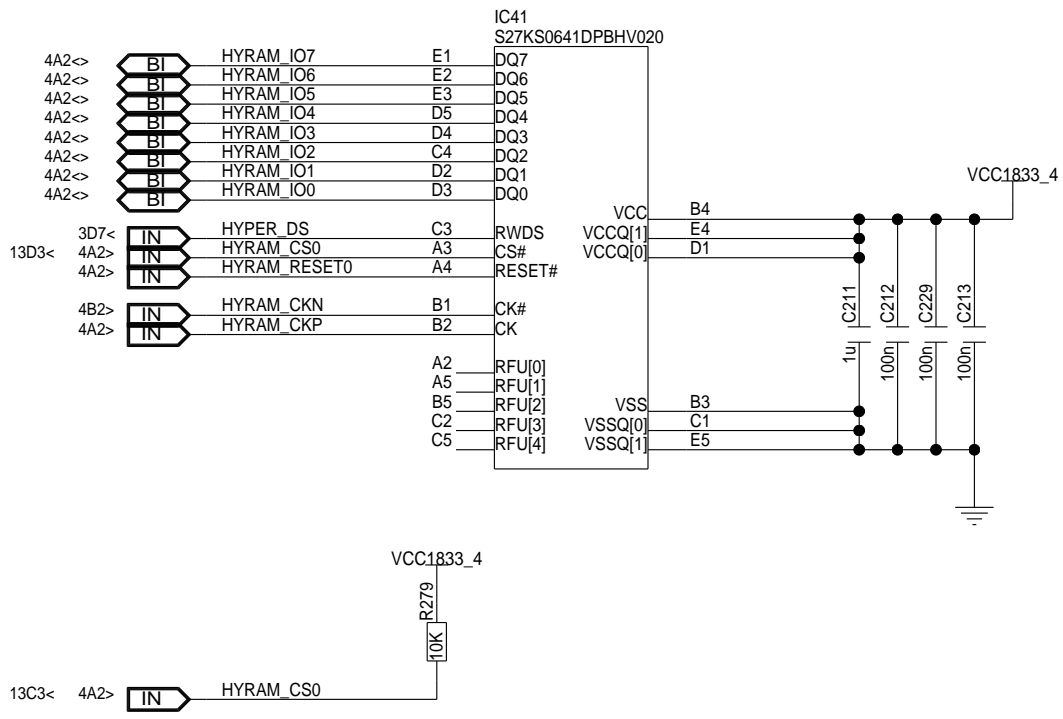
NOR Flash: 32MB



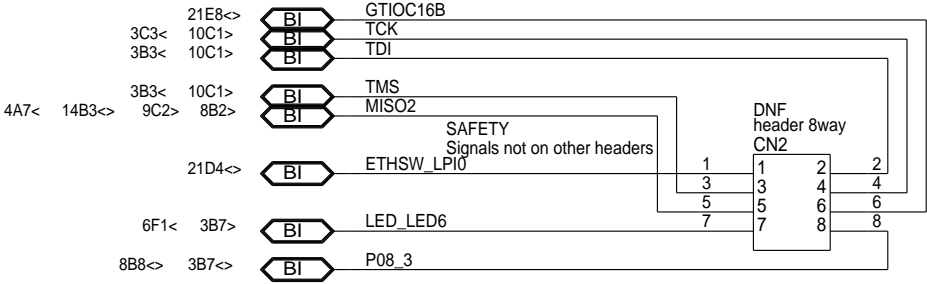
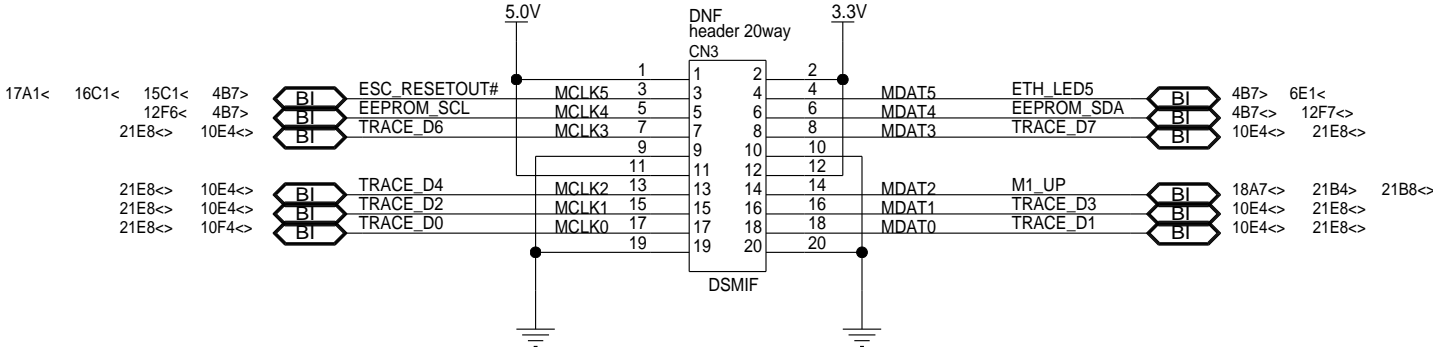
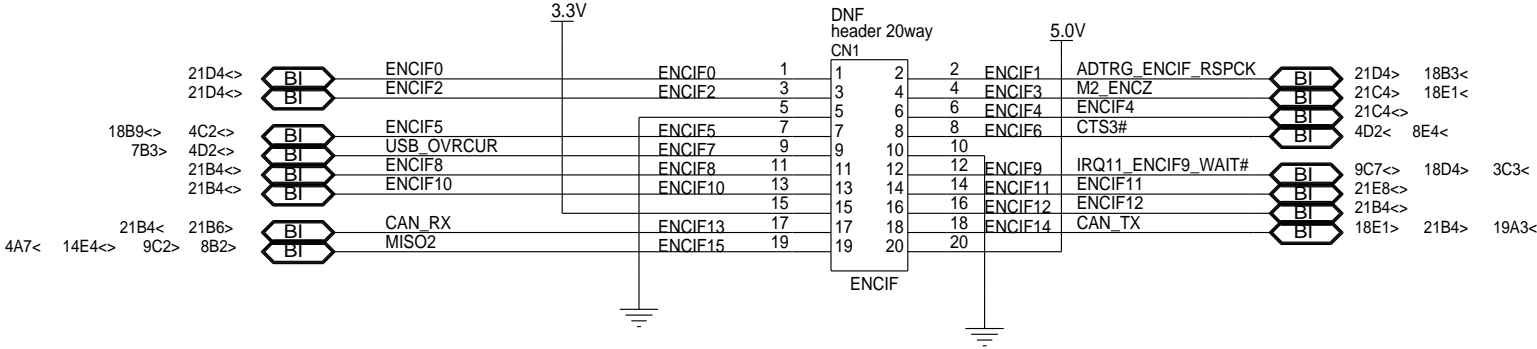
Octal SPI Flash: 64MB
Quad SPI Flash: 64MB



Hyper RAM: 8MB

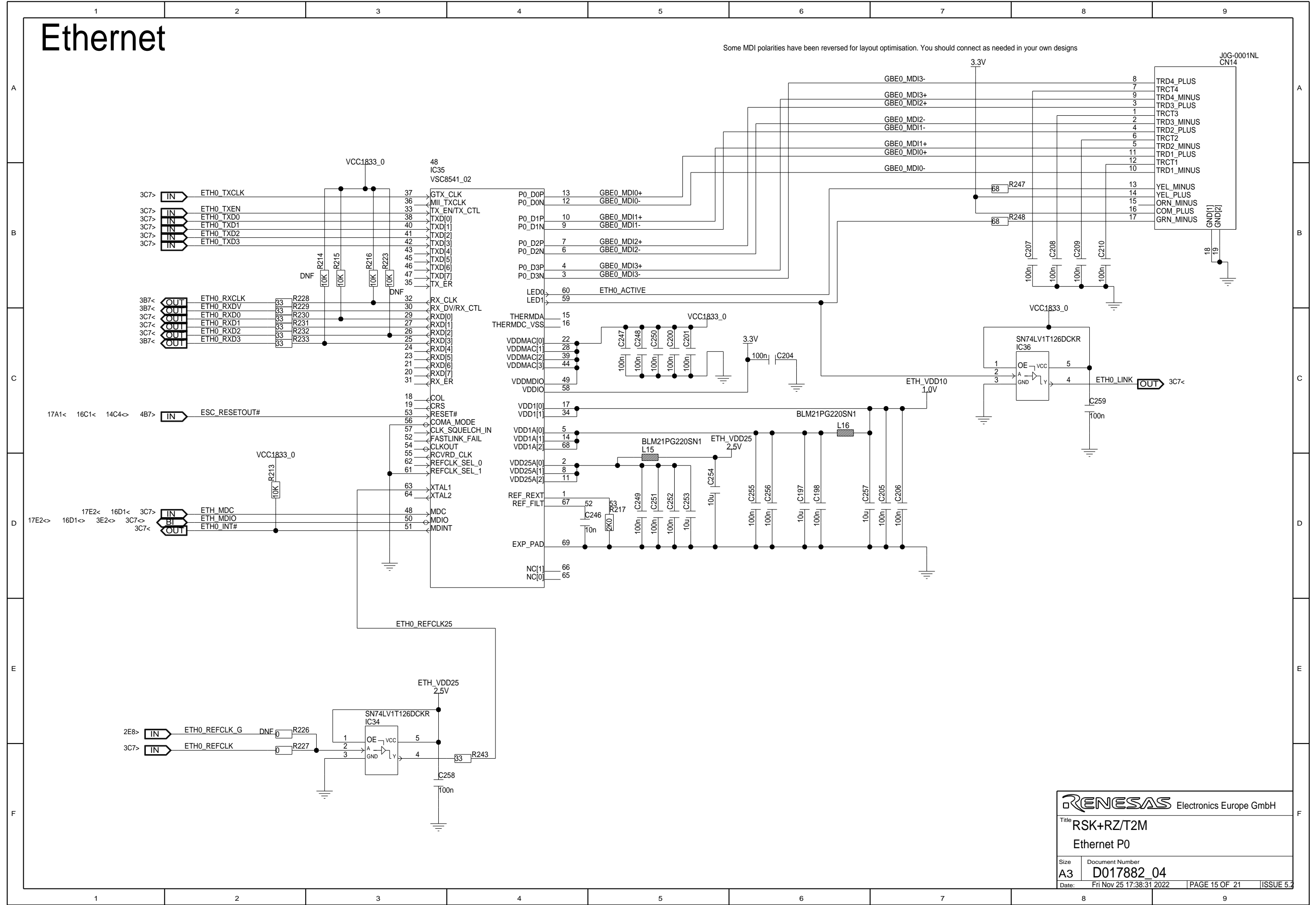


PIN HEADERS

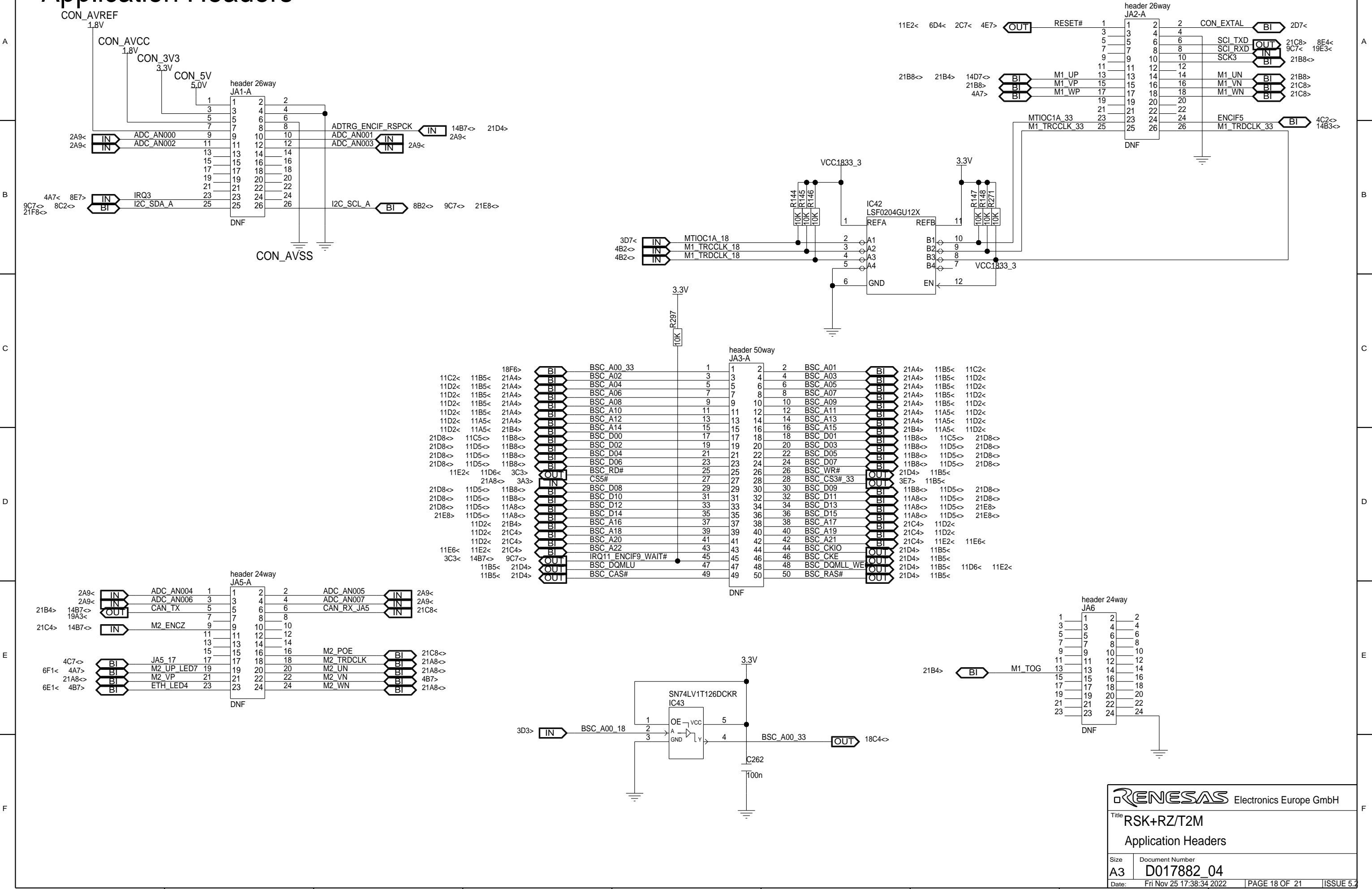


Ethernet

Some MDI polarities have been reversed for layout optimisation. You should connect as needed in your own designs

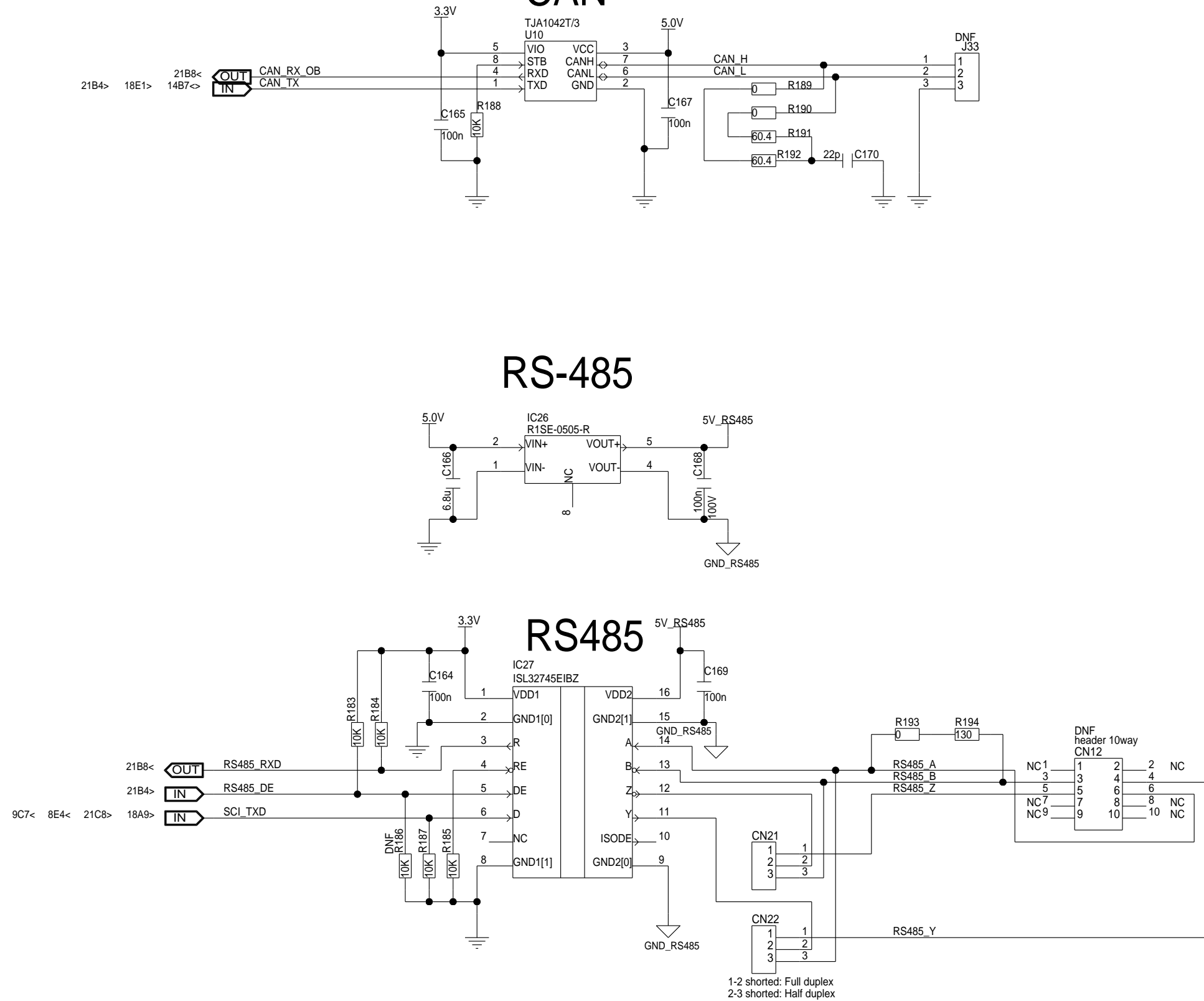


Application Headers



RS-485

RS485



RL78G1C USB to Serial

