

## DISCLAIMER

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Place near CN1

NCP15WF104F03RC

RT1

R17

16.2k 1%

cVBUS

R22

100k 1%

R26

15k 1%

BAT54BRW-7-F

D2

Place near U1

C11

0.1uF 0402

C93

0.1uF 0402

SW2

S-8110

R116

8.2k 1%

R117

6.8k 1%

R118

3.3k 1%

R119

1.8k 1%

R120

3.3k 1%

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Place footprints on back  
For MLCCs, use footprints that accomodate 0805 or 1210

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Connector to BMS EVB

Overlap C53 with C50  
and C51 on top layer

Place footprints  
on back

Place J5 & J6 on  
right side of board

### PROG Pin Setting (SW1)

SW Pos: Cell Count  
1: 2 Cell (61.9k)  
2: 3 Cell (93.1k)  
3: 4 Cell (133k)  
4: 5 Cell (178k) <= default  
5: 6 Cell (237k)  
6: 7 Cell (316k)

2S-7S Board  
Configuration  
AC LIM = 0.48A

## Renesas Electronics Confidential

Type-C PD Battery Charger: RAA489118

Document Number  
RTK0EUG011D09020BJ (2S to 7S)

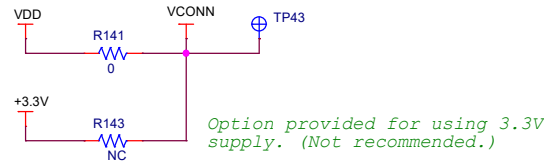
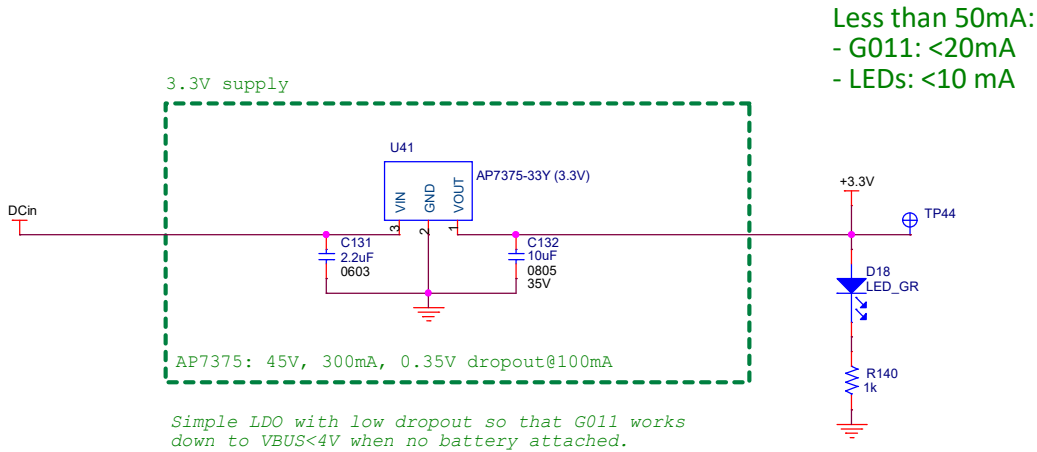
Date: Monday, May 06, 2024

Sheet 2 of 3

Rev A

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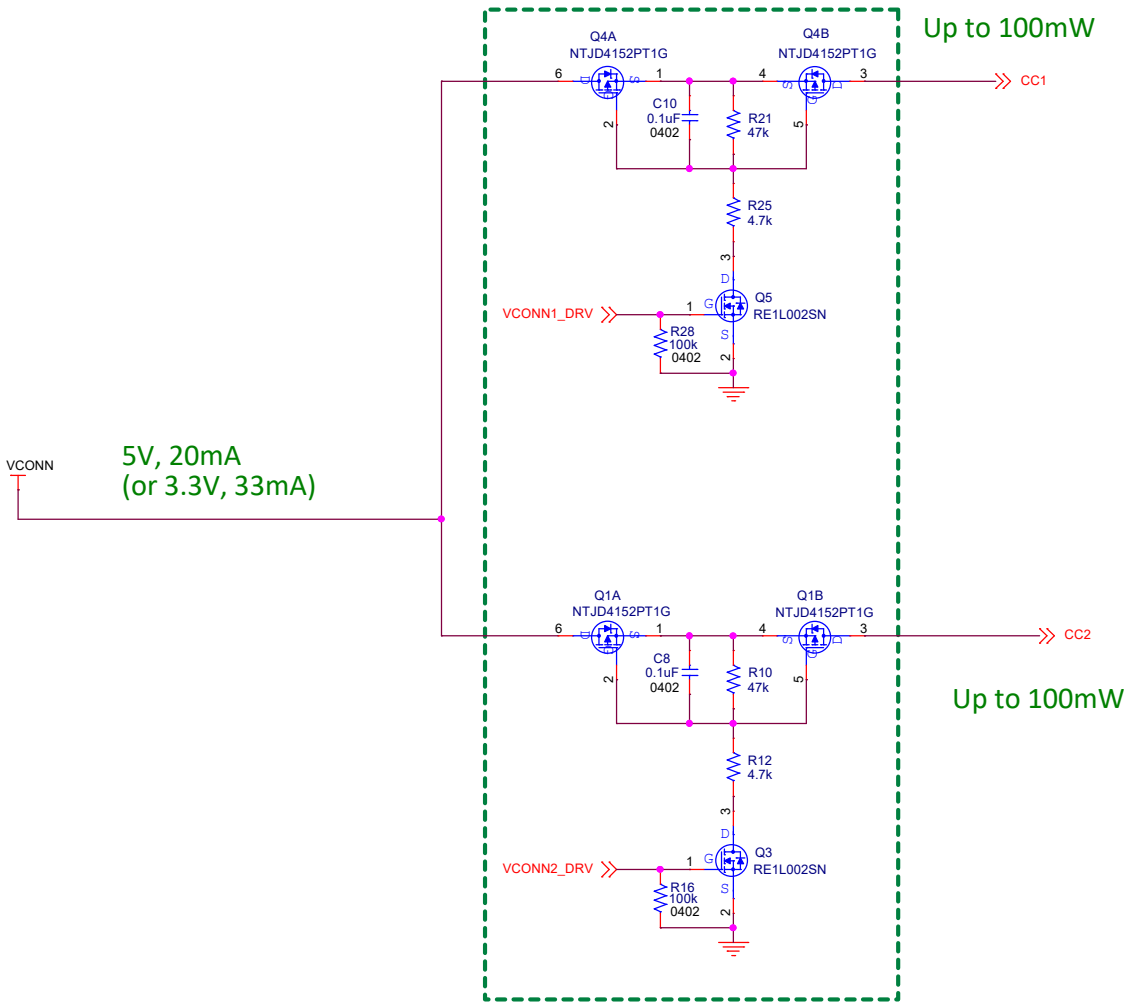
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Generally, it is not recommended to use 9238C/RAA489118 VDD for anything other than powering battery controller VDDP and related.

If VDD used to power VCONN (up to 20 mA), PD controller should avoid switching the power stage when VCONN is connected through to the USB cable.

Consider generating VCONN from buck converter driven from VBAT.



VCONN power supply circuits. For supporting E-marked cables. These parts can be removed if VCONN supply is not required, e.g., if E-marked cables are not supported.

VCONN powered devices (VPD up to 1W) are not supported.