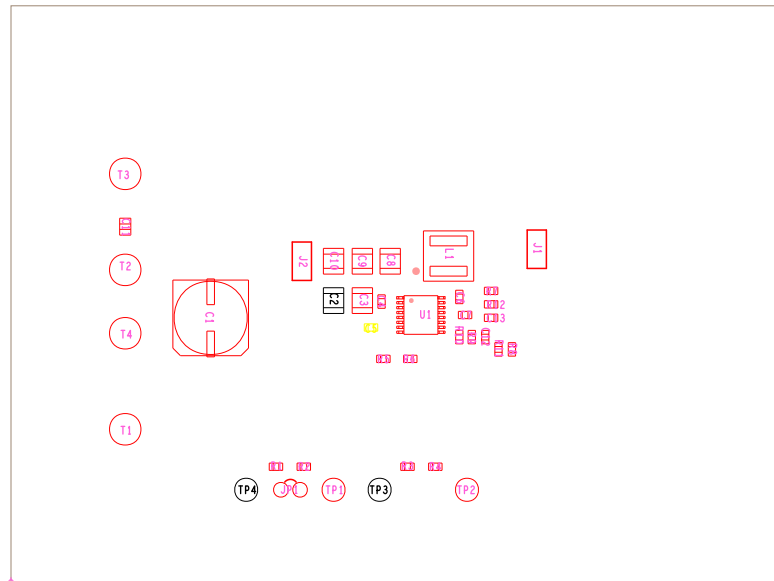
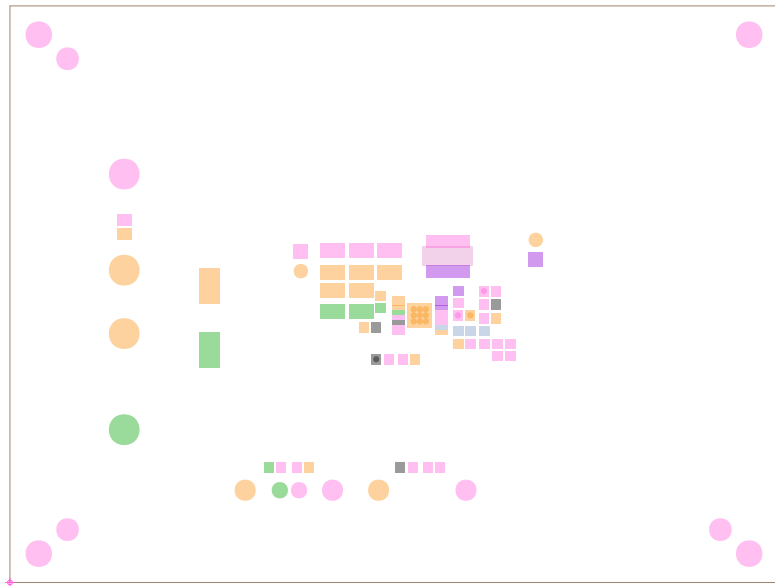


RAA211450EVAL_HTSSOP



SOLDER MASK TOP



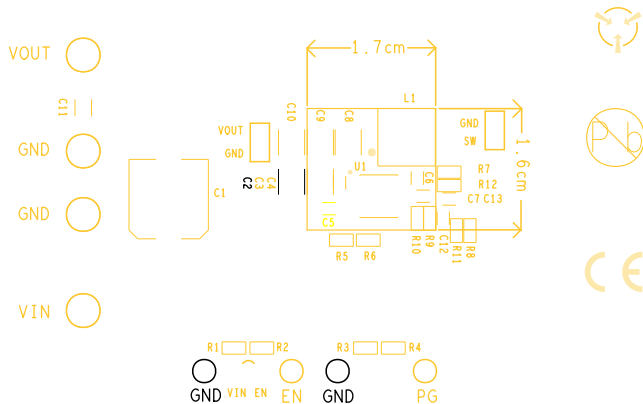
RAA211450EVAL_HTSSOP



PASTE MASK TOP

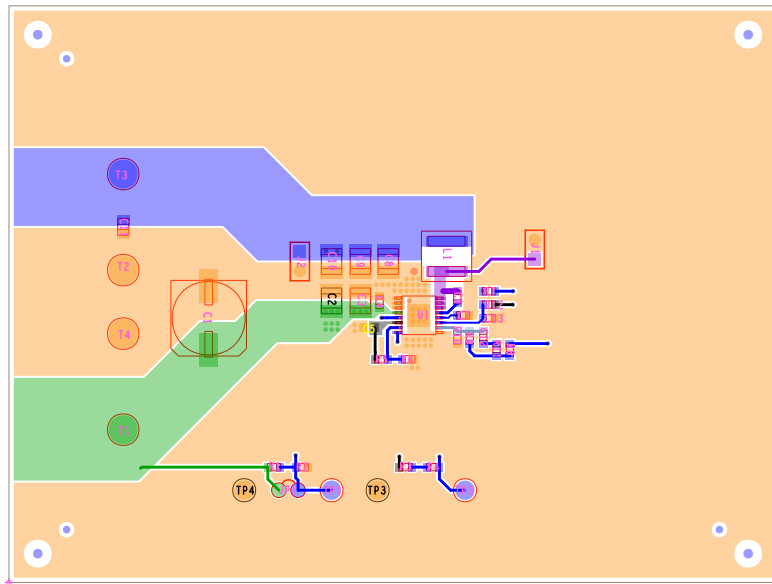
RAA211450EVAL_HTSSOP

RENESAS
 RTKA211450DE0010BU REVA



SILK SCREEN TOP

RAA211450EVAL_HTSSOP



TOP LAYER COMPONENT SIDE

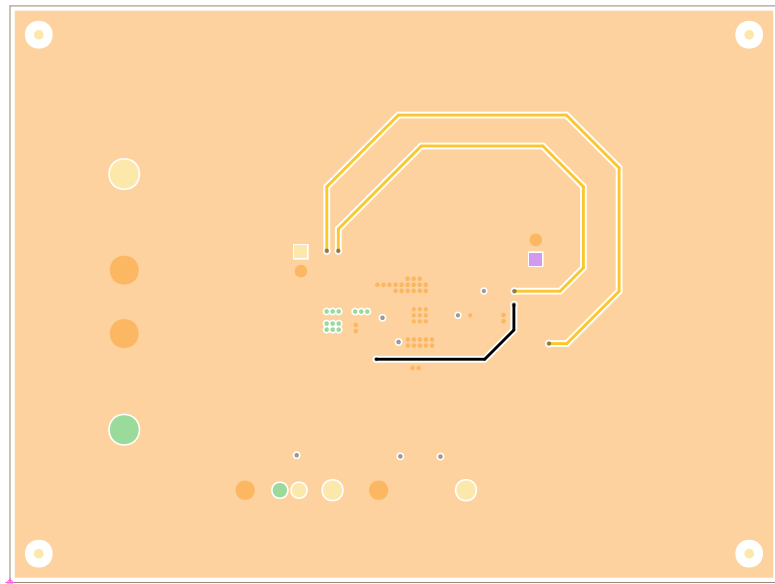
RAA211450EVAL_HTSSOP

LAYER 2

RAA211450EVAL_HTSSOP

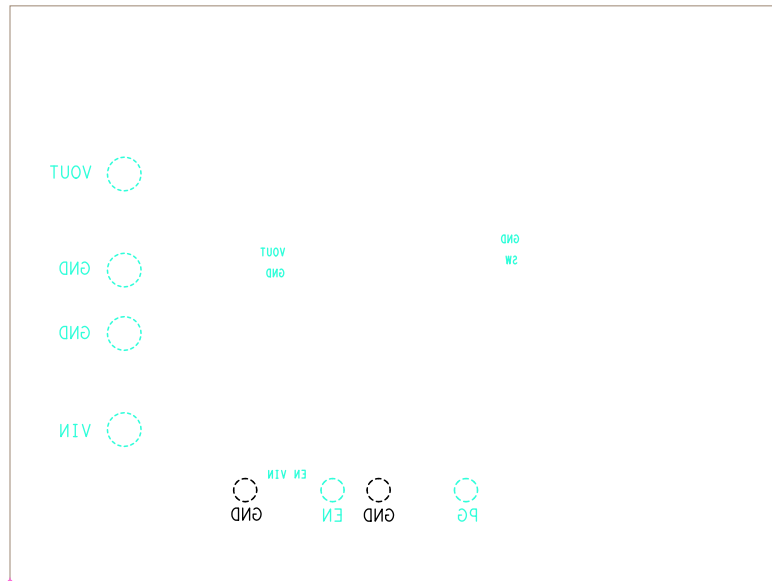
LAYER 3

RAA211450EVAL_HTSSOP



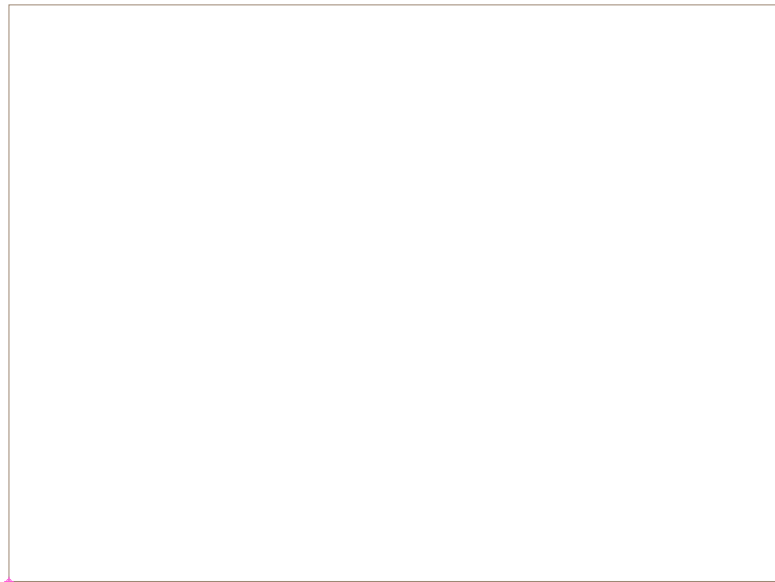
LAYER 4

RAA211450EVAL_HTSSOP



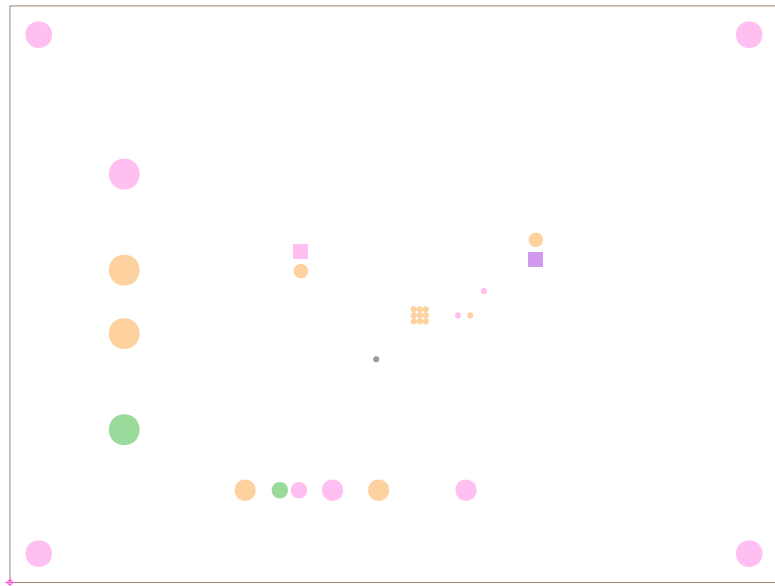
SILK SCREEN BOTTOM

RAA211450EVAL_HTSSOP



PASTE MASK BOTTOM

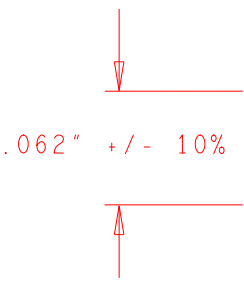
RAA211450EVAL_HTSSOP



SOLDER MASK BOTTOM

RAAS11420EVAL_HT20P

PHYSICAL BOARD DIMENSIONS
& LAYER STRUCTURE

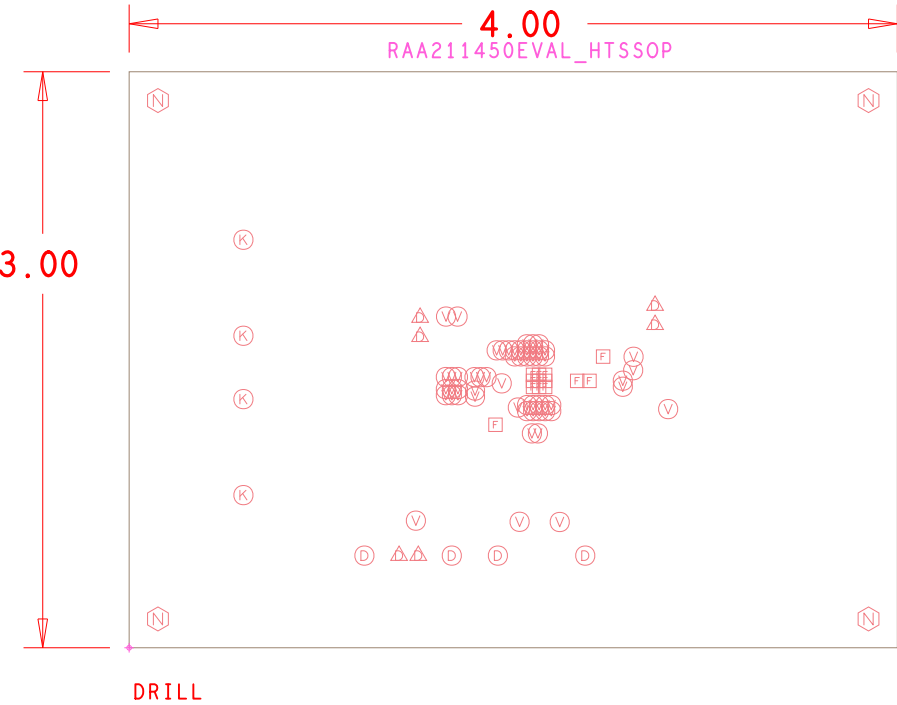


	SILK TOP	silkt.art
	MASK TOP	smaskt.art
	(TOP)COMPONENT	layer1.art
	LAYER 2	layer2.art
	LAYER 3	layer3.art
	(BOTTOM) SOLDER	layer4.art
	MASK BOTTOM	smaskb.art
	SILK BOTTOM	silkb.art

DRILL CHART: TOP to BOTTOM

ALL UNITS ARE IN MILS

FIGURE	SIZE	TOLERANCE	PLATED	QTY
F	12.0	+0.0/-0.0	PLATED	13
V	12.0	+3.0/-3.0	PLATED	56
D	40.0	+0.0/-0.0	PLATED	4
Δ	41.0	+0.0/-0.0	PLATED	6
K	100.0	+0.0/-0.0	PLATED	4
N	128.0	+0.0/-0.0	NON-PLATED	4



- NOTES:
- THIS BOARD IS RoHS COMPLIANT.
 - PRINTED WIRING BOARD DESIGN AND ACCEPTANCE CRITERIA SHALL BE IAW WITH THE REQUIREMENTS OF IPC-D-275 AND IPC-A-600.
 - MATERIAL: FR4 (RoHS COMPLIANT), 2 OZ COPPER FOR TOP AND BOTTOM LAYERS, 1 OZ COPPER FOR INTERNAL LAYERS
 - APPLY SOLDER MASK, BOTH SIDES OVER BARE COPPER IAW IPC-SM-840. CLASS 2 (LPI) (BLUE MASK).
 - ALL PATTERNS ARE VIEWED FROM THE PRIMARY SIDE LOOKING THROUGH THE BOARD.
 - UNLESS OTHERWISE SPECIFIED ALL HOLE DIAMETERS ARE AFTER PLATING.
 - APPLY SILKSCREEN USING WHITE NON-CONDUCTIVE EPOXY BASED INK.
 - PWB MUST BE 100% ELECTRICALLY TESTED FOR SHORTS AND CONTINUITY. USE NETLIST PROVIDED RTKA211450DE0010BU IPC356. IPC IAW IPC-D-356.
 - MARK DATE CODE AND MANUFACTURES IDENIFICATION ON SOLDER SIDE PER IPC-6011 AND IPC-6012.
 - TOLERANCE ON ALL DRILL HOLES SHALL BE IAW IPC-D-2221 & 2222 UNLESS OTHERWISE SPECIFIED.

Drawn By: Amnat Yakamna	Date Drawn: 10/14/2020	Engineer: Zhuangyao Tang		
Released By:	Date Released:	RAA211450 EVAL BOARD LAYOUT RTKA211450DE0010BU		
Updated By:	Date Updated:			
		MASK#	HDWR ID	REV. A
		FILENAME: RAA211450DE0010BU REVA		SHEET 1 OF 1