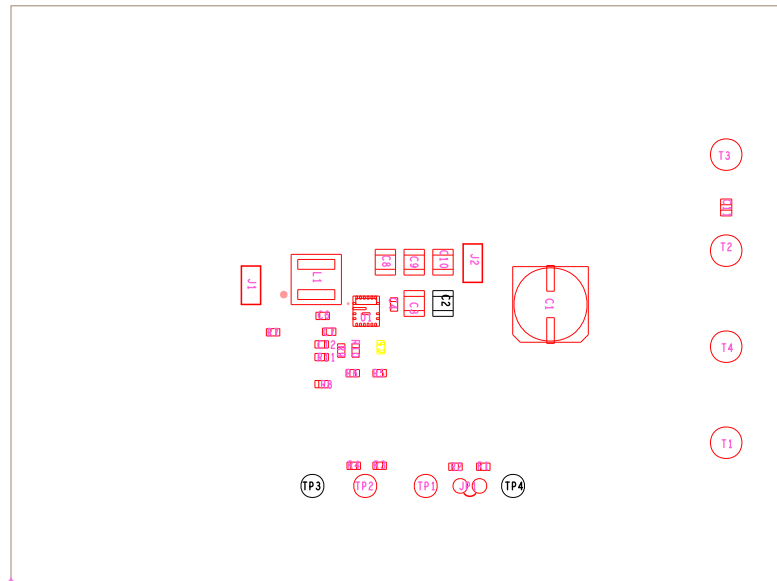
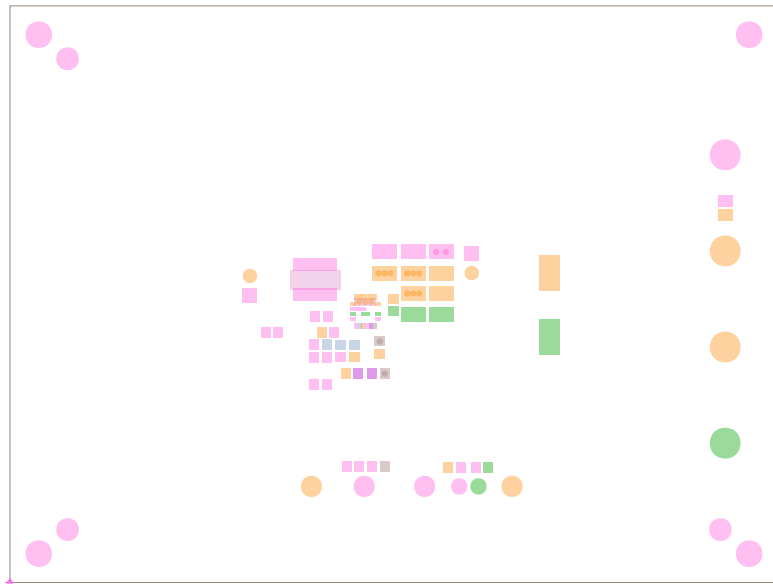


## RAA211630EVAL\_QFN



RAA211630EVAL\_QFN



SOLDER MASK TOP

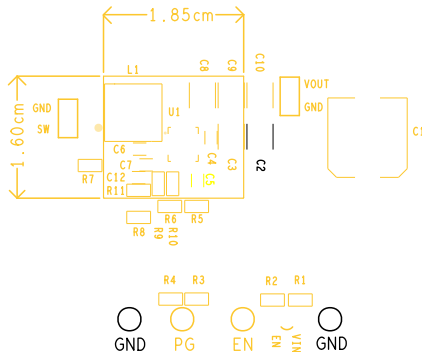
RAA211630EVAL\_QFN



PASTE MASK TOP

## RAA211630EVAL\_QFN

**RENESAS**  
 RTKA211630DE0000BU REVA



VOUT ○

C11 | |

GND ○

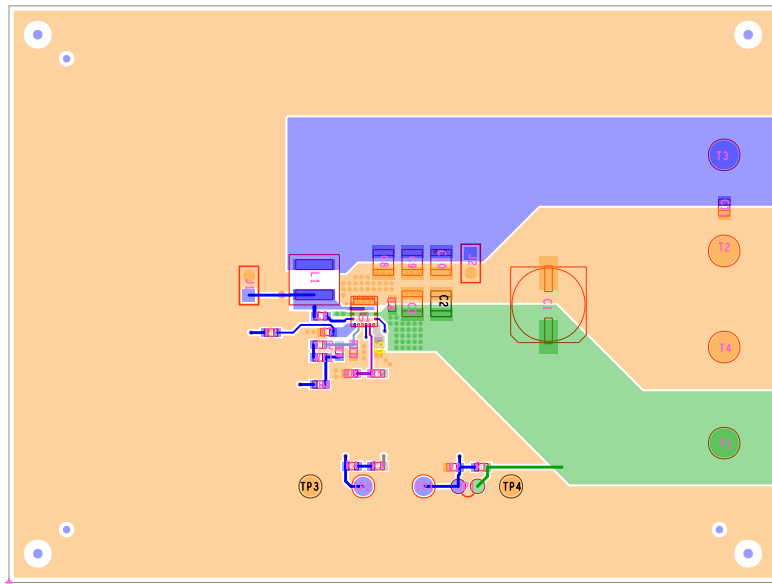
GND ○

VIN ○

○  
GND○  
PG○  
EN○  
VIN○  
GND

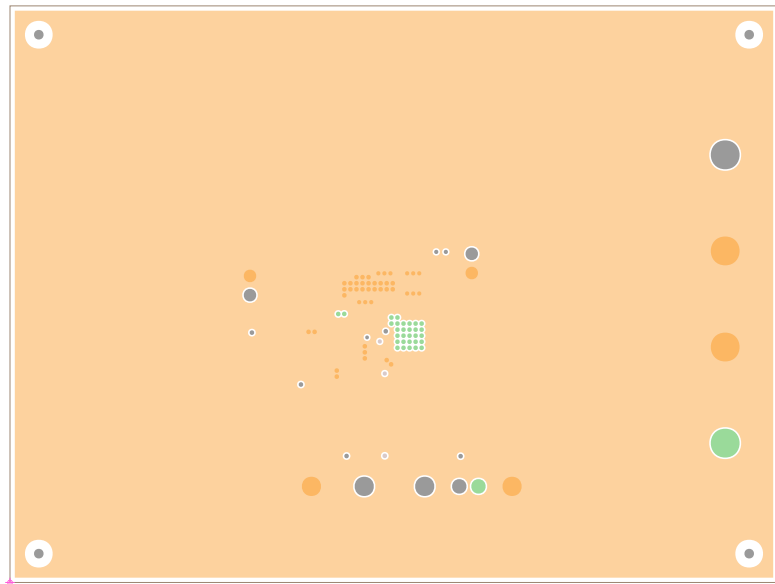
SILK SCREEN TOP

## RAA211630EVAL\_QFN



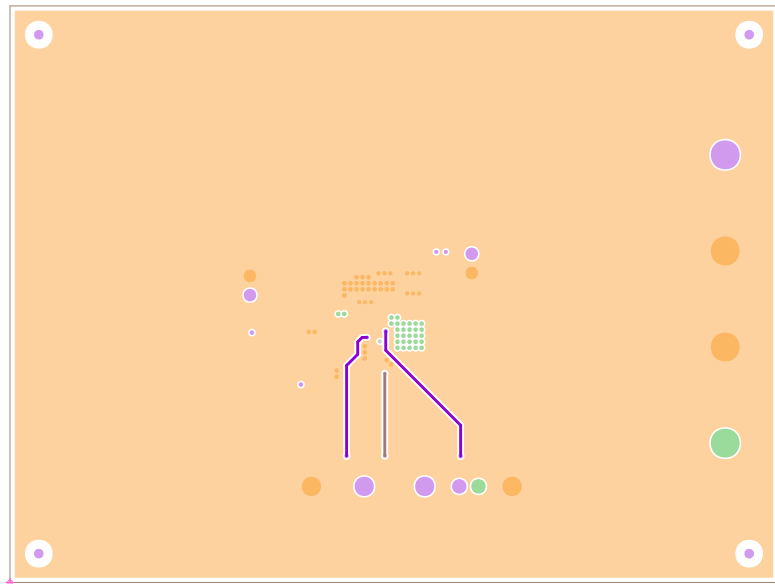
TOP LAYER COMPONENT SIDE

RAA211630EVAL\_QFN



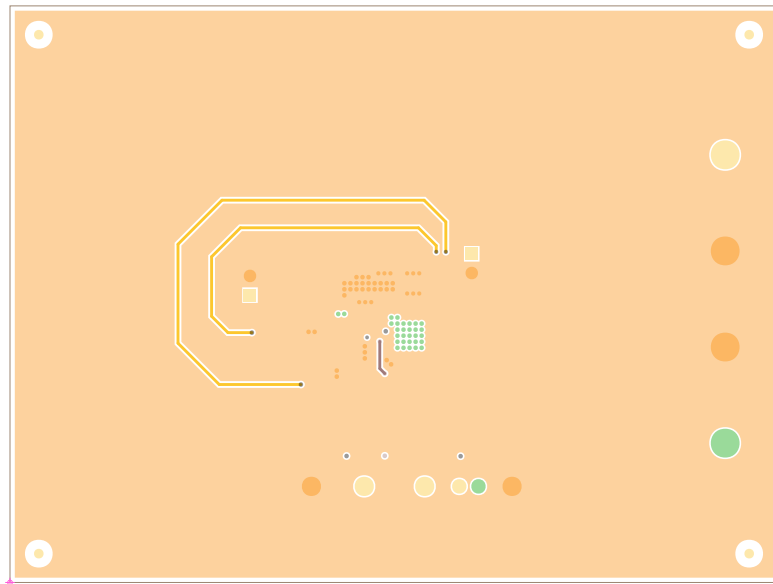
LAYER 2

RAA211630EVAL\_QFN



LAYER 3

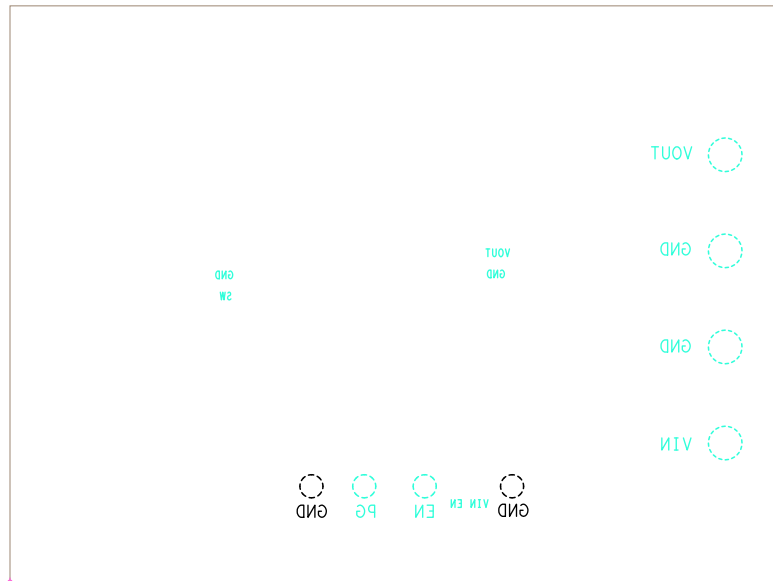
RAA211630EVAL\_QFN



LAYER 4

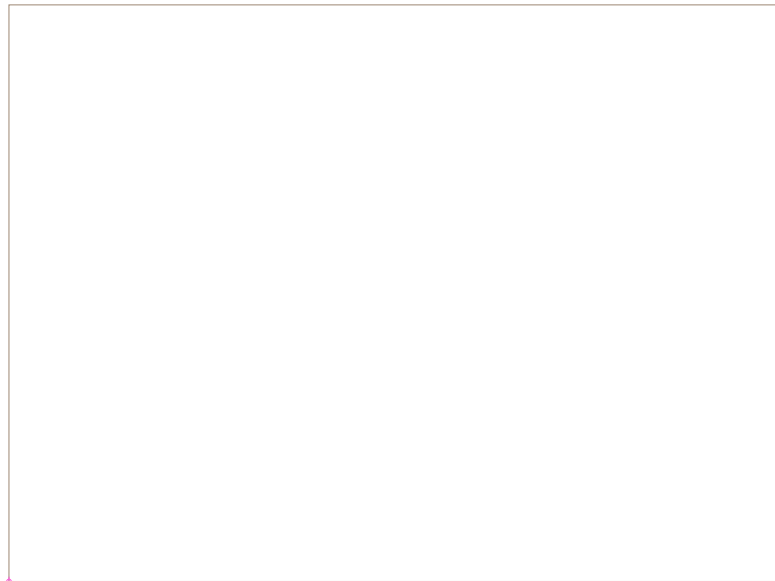


## RAA211630EVAL\_QFN



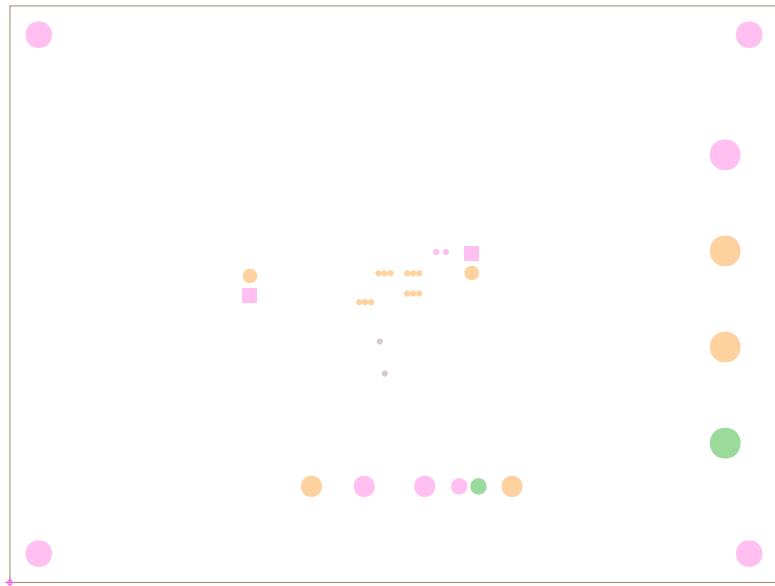
SILK SCREEN BOTTOM

RAA211630EVAL\_QFN



PASTE MASK BOTTOM

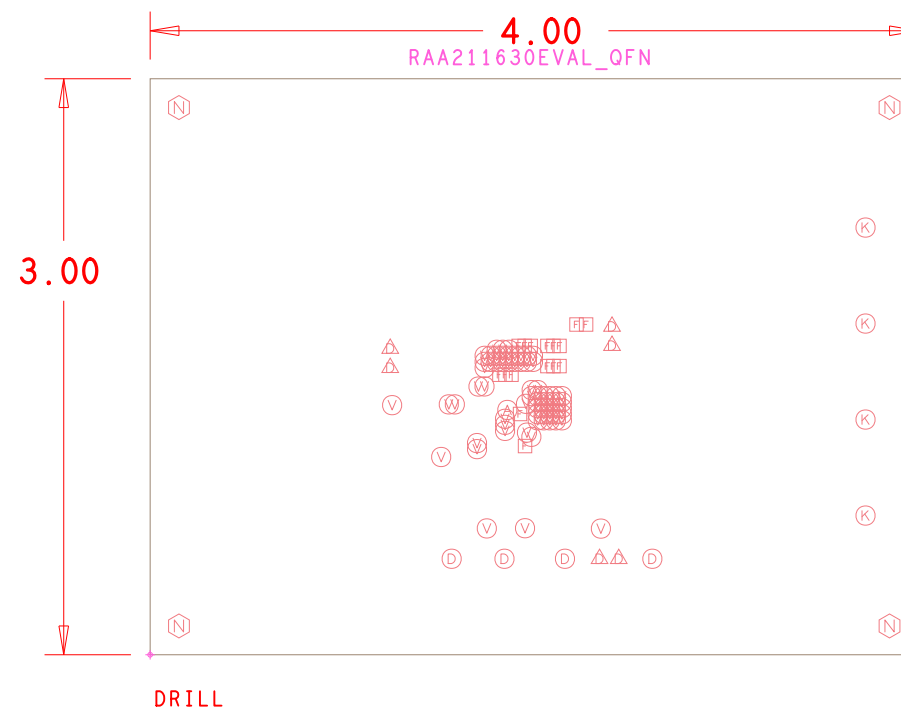
RAA211630EVAL\_QFN



SOLDER MASK BOTTOM

RAASII030EVAL\_QFN

## PHYSICAL BOARD DIMENSIONS & LAYER STRUCTURE




### DRILL CHART: TOP to BOTTOM

ALL UNITS ARE IN MILS

FIGURE	SIZE	TOLERANCE	PLATED	QTY
(A)	10.0	+0.0/-0.0	PLATED	1
(F)	12.0	+0.0/-0.0	PLATED	16
(V)	12.0	+3.0/-3.0	PLATED	67
(D)	40.0	+0.0/-0.0	PLATED	4
(A)	41.0	+0.0/-0.0	PLATED	6
(K)	100.0	+0.0/-0.0	PLATED	4
(N)	128.0	+0.0/-0.0	NON-PLATED	4

NOTES:

1. THIS BOARD IS RoHS COMPLIANT.
2. PRINTED WIRING BOARD DESIGN AND ACCEPTANCE CRITERIA SHALL BE IAW WITH THE REQUIREMENTS OF IPC-D-275 AND IPC-A-600.
3. MATERIAL: FR4 (RoHS COMPLIANT), 2 OZ COPPER FOR TOP AND BOTTOM LAYERS, 1 OZ COPPER FOR INTERNAL LAYERS
4. APPLY SOLDER MASK, BOTH SIDES OVER BARE COPPER IAW IPC-SM-840.  
CLASS 2 (LPI) (BLUE MASK).
5. ALL PATTERNS ARE VIEWED FROM THE PRIMARY SIDE LOOKING THROUGH THE BOARD.
6. UNLESS OTHERWISE SPECIFIED ALL HOLE DIAMETERS ARE AFTER PLATING.
7. APPLY SILKSCREEN USING WHITE NON-CONDUCTIVE EPOXY BASED INK.
8. PWB MUST BE 100% ELECTRICALLY TESTED FOR SHORTS AND CONTINUITY.  
USE NETLIST PROVIDED RTKA212821E00001U IPC356.IPC IAW IPC-D-356.
9. MARK DATE CODE AND MANUFACTURES IDENTIFICATION ON SOLDER SIDE  
PER IPC-6011 AND IPC-6012.
10. TOLERANCE ON ALL DRILL HOLES SHALL BE IAW IPC-D-2221 & 2222 UNLESS OTHERWISE SPECIFIED.

Drawn By: <b>Amnat Yakamna</b>	Date Drawn: 09/18/2020	Engineer: <b>Zhuangyao Tang</b>	
Released By:	Date Released:	RAA211630 EVAL BOARD LAYOUT	
Updated By:	Date Updated:	RTKA211630DE0000BU	
		MASK#	HDWR ID
			REV. A
FILENAME: <b>RAA211630DE0000BU REVA</b>			SHEET <b>1 OF 1</b>