
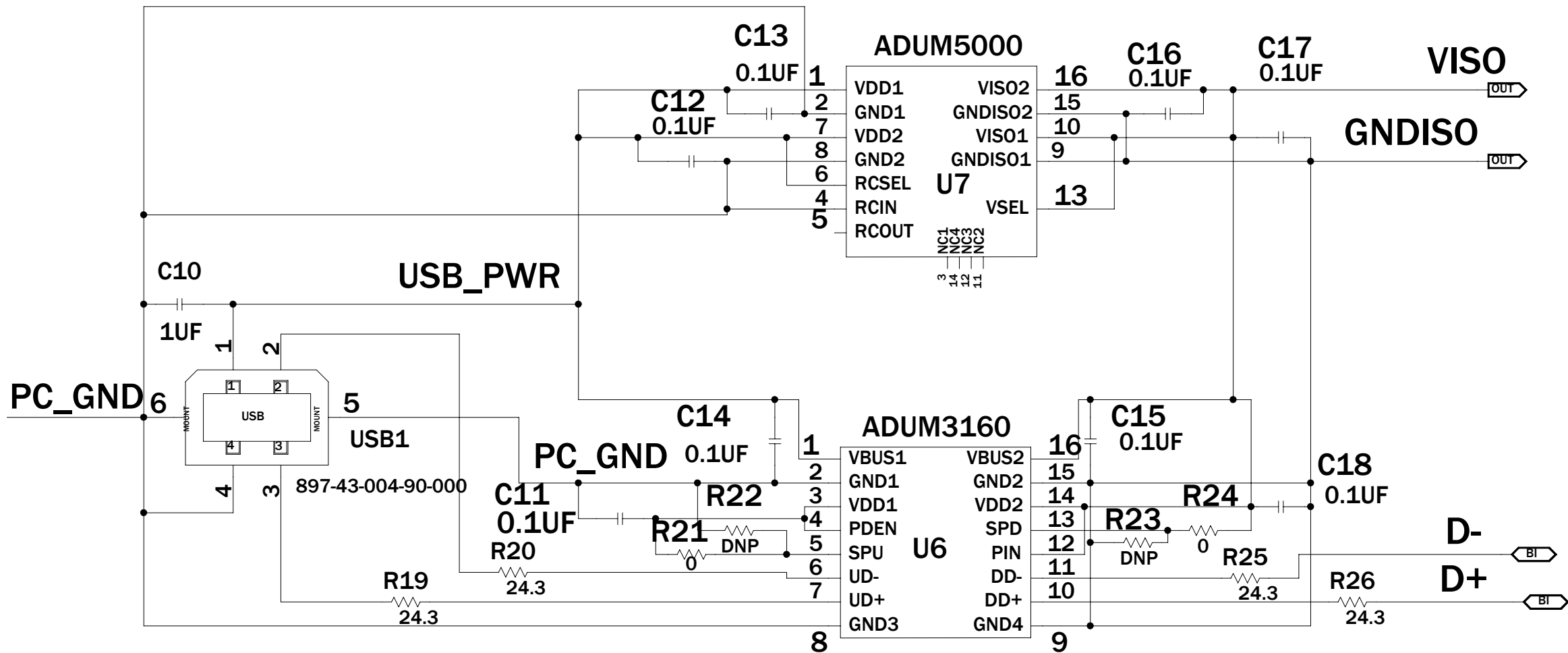



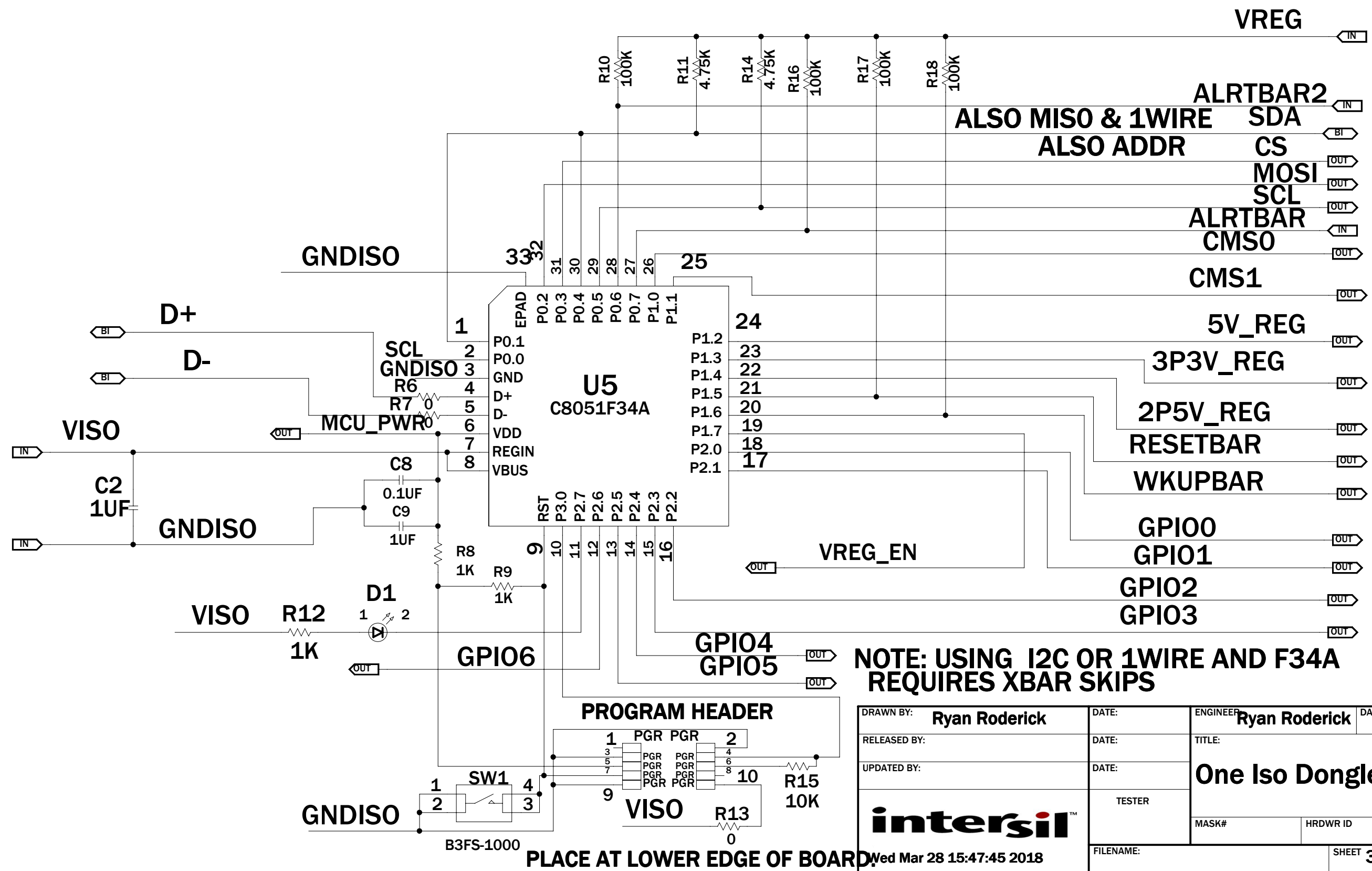
8	7	6	5	4	3	2	1
<div>ISOLATION DONGLE BOARD FOR ALL ISL BMS</div> <div>BY</div> <div>RYAN RODERICK</div>							
D							D
C							C
B							B
A							A
8	7	6	5	4	3	2	1

DRAWN BY: <b>Ryan Roderick</b>	DATE:	ENGINEER: <b>Ryan Roderick</b>	DATE:	
RELEASED BY:	DATE:	TITLE: <b>One Iso Dongle Ev Brd</b>		
UPDATED BY:	DATE:			
 <b>Wed Mar 28 15:45:02 2018</b>	TESTER	MASK#	HRDWR ID	REV. <b>C</b>
	FILENAME:			SHEET <b>1</b> OF <b>4</b>



DRAWN BY: <b>Ryan Roderick</b>	DATE:	ENGINEER: <b>Ryan Roderick</b>	DATE:
RELEASED BY:	DATE:	TITLE: <b>One Iso Dongle Ev Brd</b>	
UPDATED BY:	DATE:		
 <b>Wed Mar 28 15:47:45 2018</b>	TESTER	MASK#	HRDWR ID
	FILENAME:	REV. <b>C</b>	
		SHEET <b>2</b>	OF <b>4</b>

# SI LABS USB DONGLE FOR ALL ISIL BMS



DRAWN BY: <b>Ryan Roderick</b>	DATE:	ENGINEER: <b>Ryan Roderick</b>	DATE:
RELEASED BY:	DATE:	TITLE: <b>One Iso Dongle Ev Brd</b>	
UPDATED BY:	DATE:	TESTER:	MASK#
<b>intersil</b>		HRDWR ID	REV. <b>c</b>
FILENAME:		SHEET <b>3</b> OF <b>4</b>	

Wed Mar 28 15:47:45 2018

