

MXT Creation SRL

Title

STAMP Interface

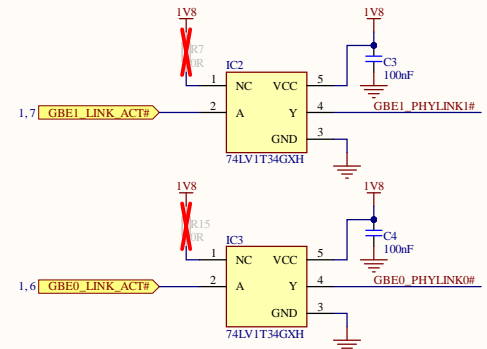
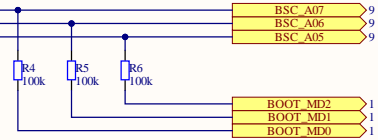
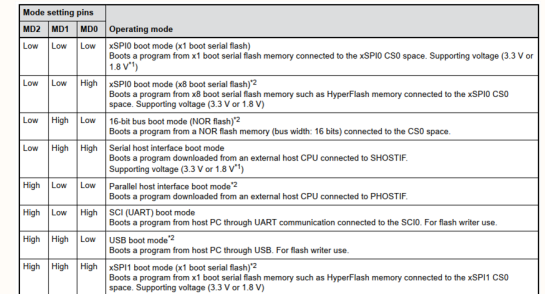
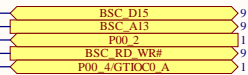
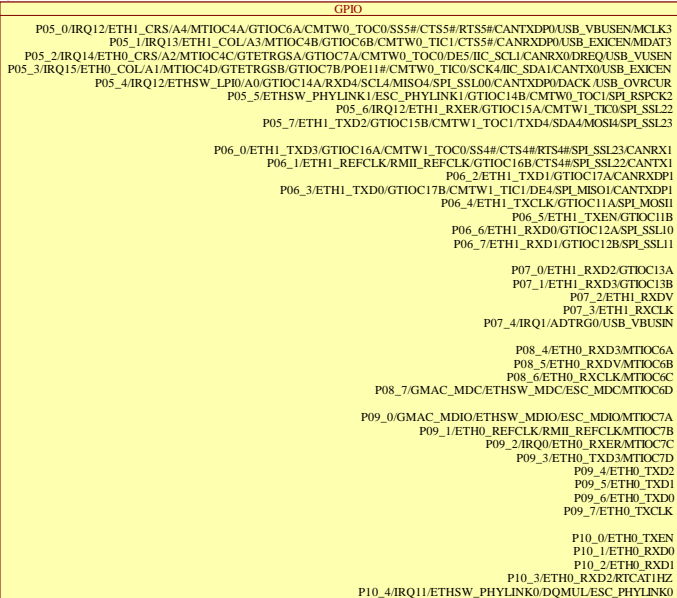
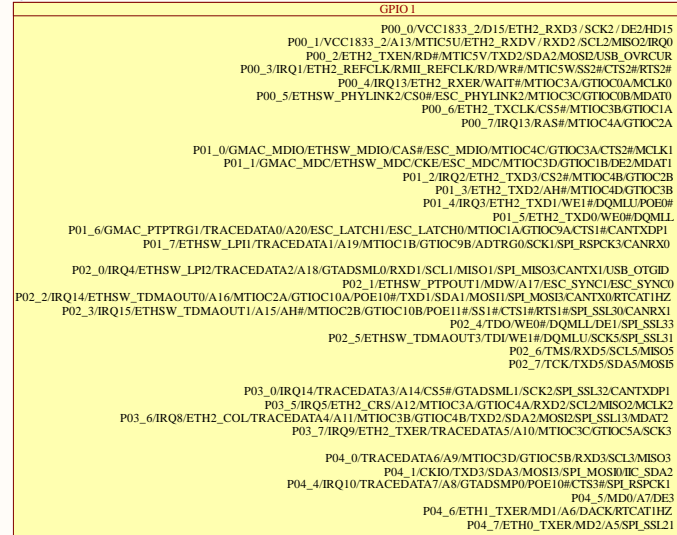
Date: 16-Jan-25

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Revision V3I2

File: STAMP Interface.SchDoc

Drawn by: Alexandru Ionita



MXT Creation SRL

| Title |
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RZ_N2L MCU 1

Date: 16-Jan-25

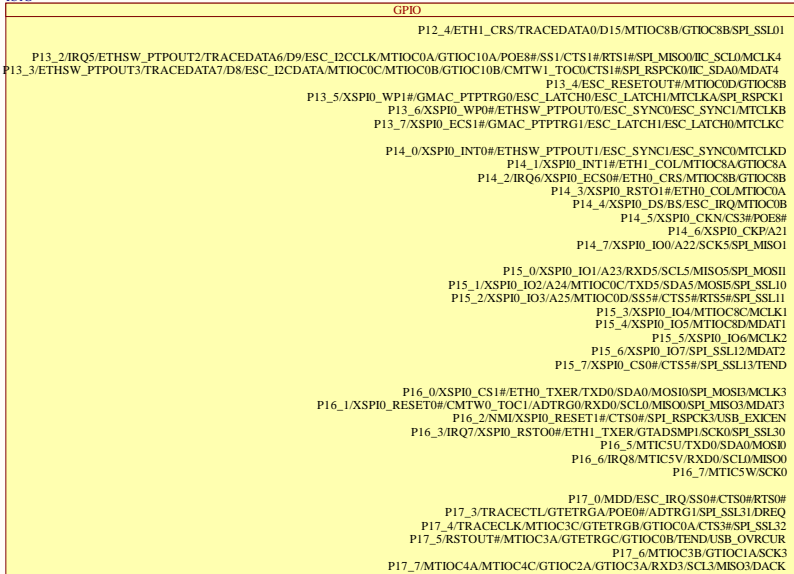
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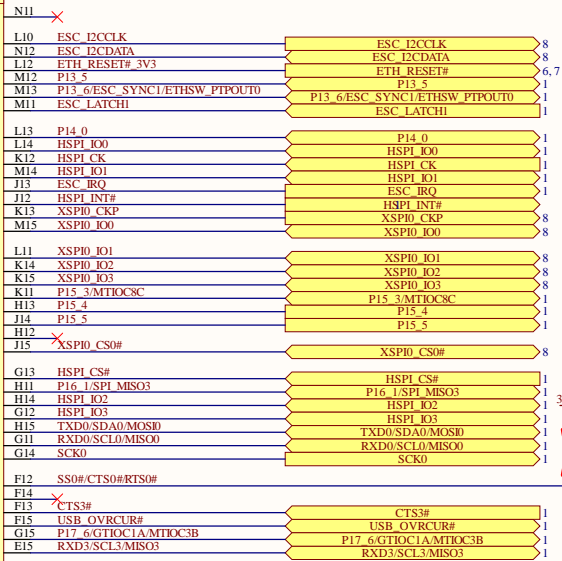
File:RZ_N2LMCU 1.SchDoc

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ICIG



R9A07G084M04GBG#AC0



3V3

R24

100k

R25

100k

SS0#CTS0#RTS0#

1

R27

100k

R29

100k

R30

100k

R31

100k

R32

100k

R33

100k

R34

100k

R35

100k

R36

100k

R37

100k

R38

100k

R39

100k

R40

100k

R41

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R42

100k

R43

100k

R44

100k

R45

100k

R46

100k

R47

100k

R48

100k

R49

100k

R50

100k

R51

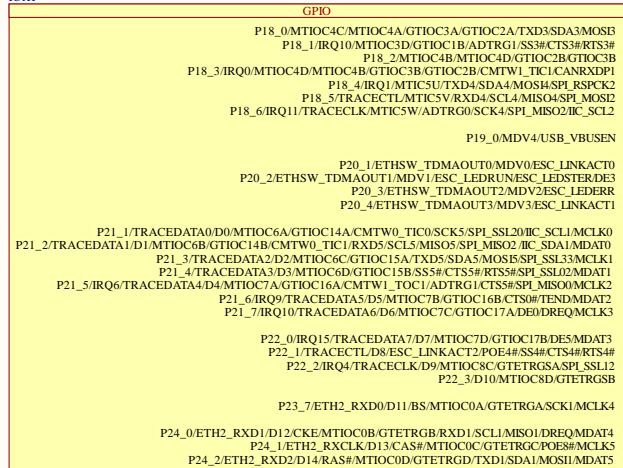
100k

R52

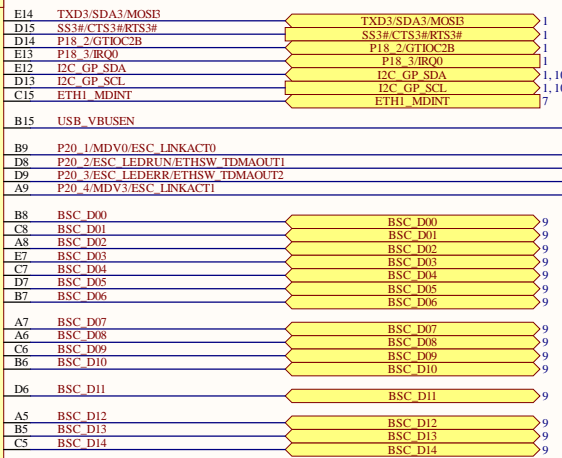
100k

| I/O Domain | MDV0 value | Operating voltage of I/O domain | |
|--------------|------------|---------------------------------|----------------|
| | | 225 FBGA | 121 FBGA |
| ETH0 (MDV0) | Low/High | High | 1.8 V or 3.3 V |
| ETH1 (MDV1) | Low/High | Low/High | 1.8 V or 3.3 V |
| ETH2 (MDV2) | Low/High | Low/High | 1.8 V or 3.3 V |
| XSPI0 (MDV3) | Low/High | High | 1.8 V or 3.3 V |
| XSPI1 (MDV4) | Low/High | High | 1.8 V or 3.3 V |

ICIH



R9A07G084M04GBG#AC0



3V3

R27

100k

R29

100k

R30

100k

R31

100k

R32

100k

R33

100k

R34

100k

R35

100k

R36

100k

R37

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R38

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R39

100k

R40

100k

R41

100k

R42

100k

R43

100k

R44

100k

R45

100k

R46

100k



MXT Creation SRL

Title

RZ_N2L MCU 2

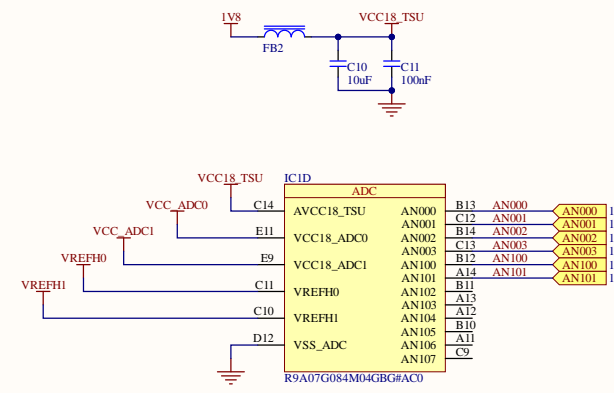
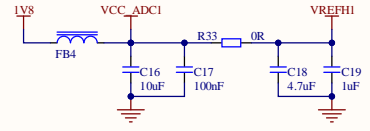
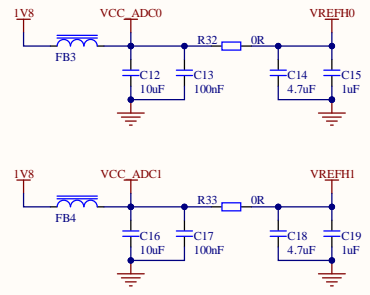
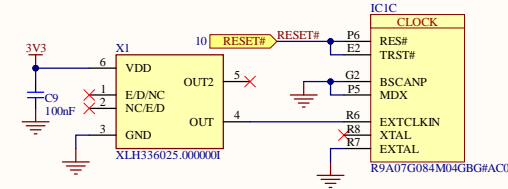
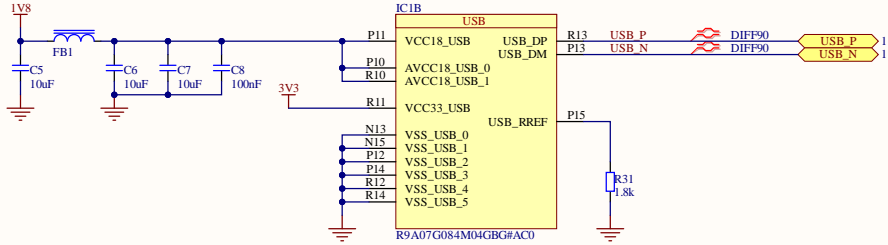
Date: 16-Jan-25

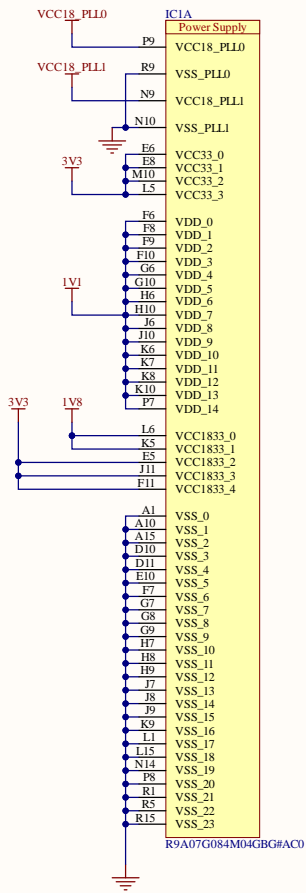
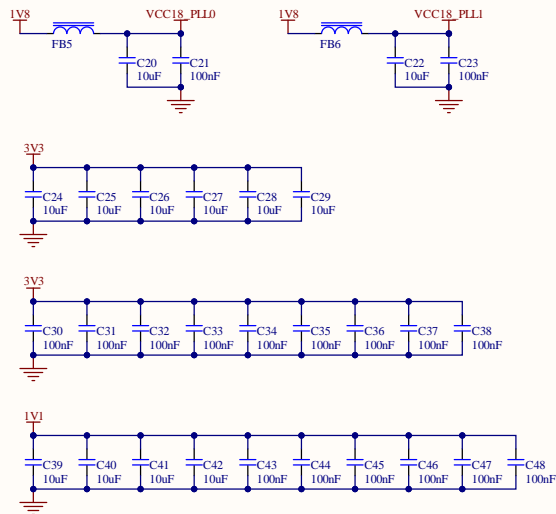
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File: RZ_N2L MCU 2.SchDoc

Drawn by: Alexandru Ionita





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|  MXT Creation SRL | | | |
| Title | | | |
| Power | | | |
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| File: Power.SchDoc | | Drawn by: Alexandru Ionita | |

