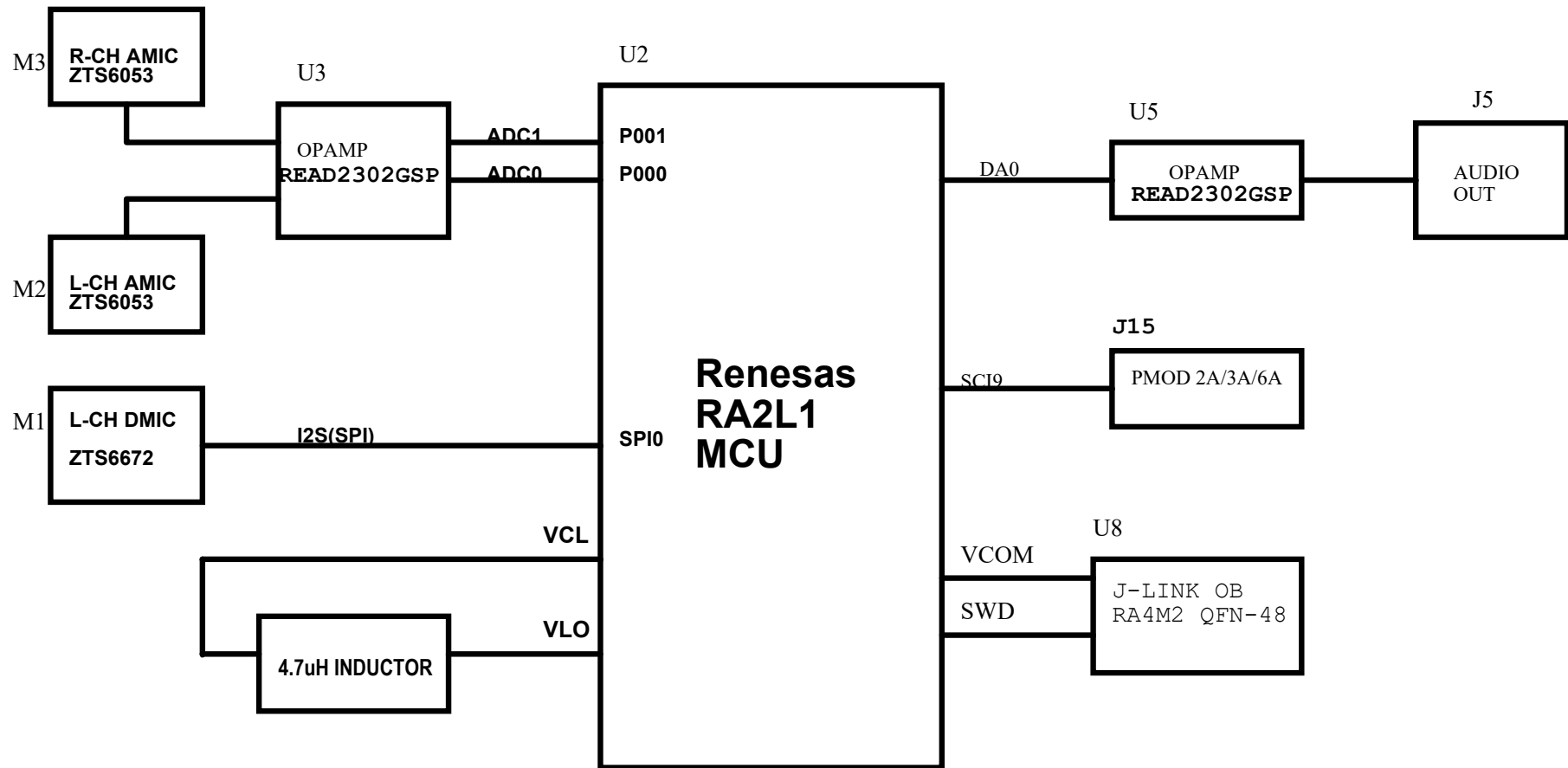


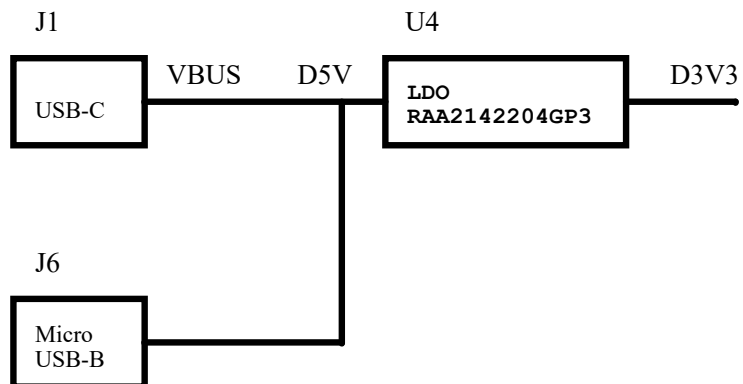
1	Title Page
2	Block Diagram
3	RA2L1 MCU
4	J-Link On Board with RA4M2-QFN48

[illegible]

<b>Renesas Electronics</b>			
<b>Title</b> <b>VOICE-RA2L1</b>			
<b>Size</b> <b>A</b>	<b>Document Number</b> <b>1.VUI_RA2L1_TitlePage</b>		<b>Rev</b> <b>1</b>
<b>Date:</b>	<b>Tuesday, September 06, 2022</b>	<b>Sheet</b>	<b>1 of 4</b>

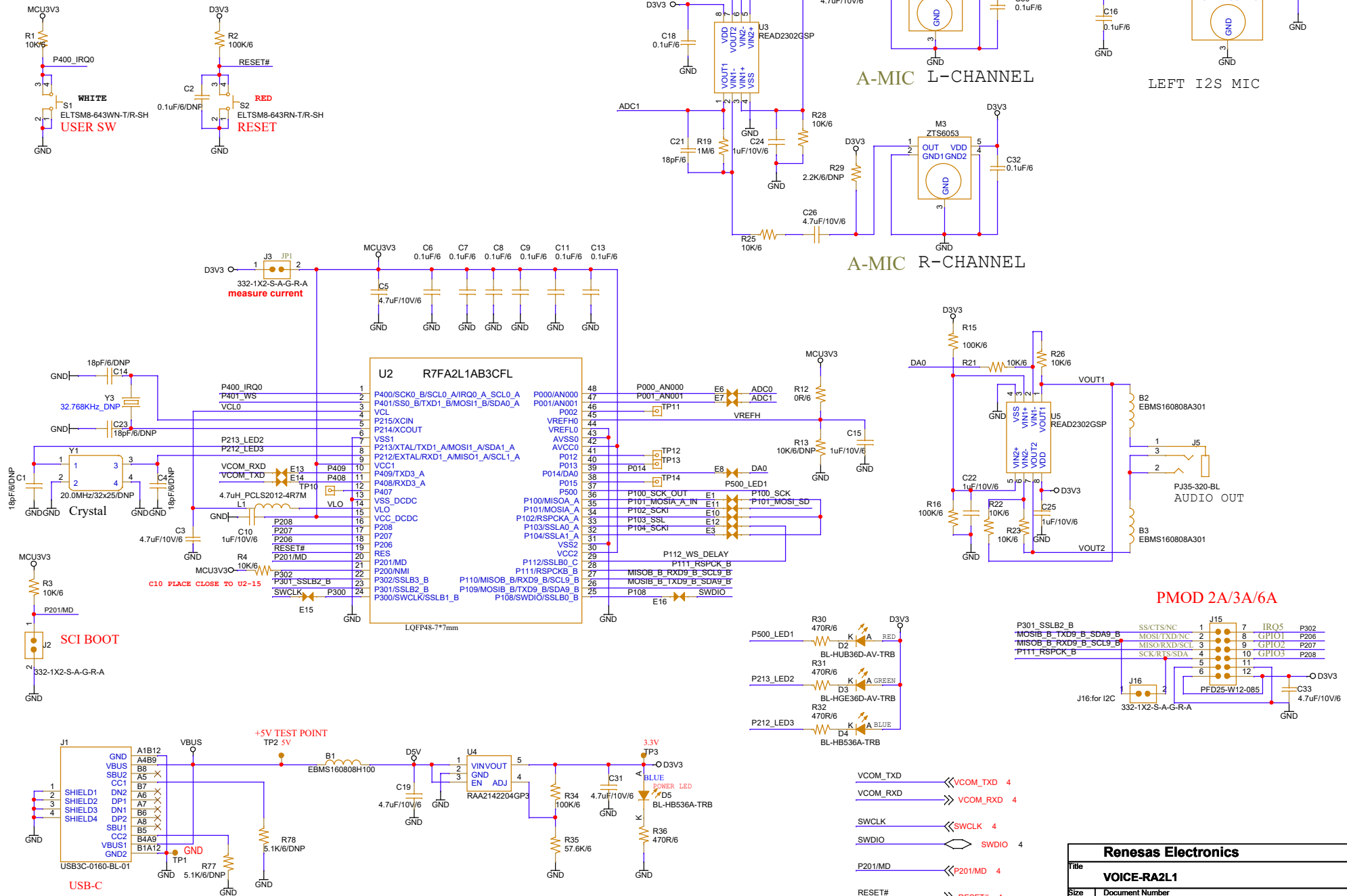


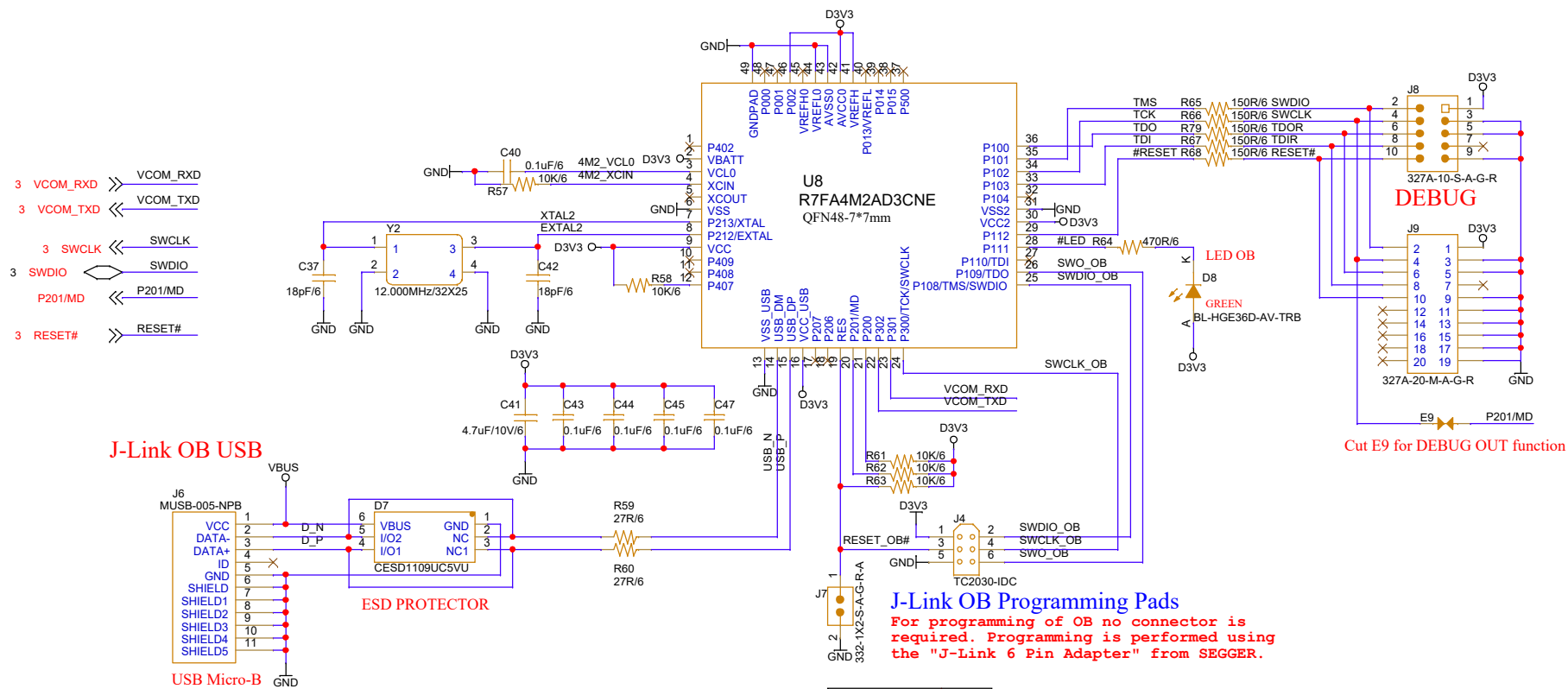
Power



Renesas Electronics		
Title		
VOICE-RA2L1		
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A	2.VUI_RA2L1_Block	1
Date: Tuesday, September 06, 2022		
Sheet 2 of 4		

NOTE : DNP is Not Populated





**J-Link OB Programming Pads**  
For programming of OB no connector is required. Programming is performed using the "J-Link 6 Pin Adapter" from SEGGER.

Debug Modes	J7
Debug on-board	Open
Debug in	Short

NOTES:

1. THE +5V AND GROUND TEST POINTS MAY BE ALTERNATELY USED TO SUPPLY +5V FROM AN EXTERNAL SOURCE.

Renesas Electronics			
Title			
VOICE-RA2L1			
Size	Document Number	Rev	
B	4.VUI_RA2L1_JLink_OB-RA4M2	1	
Date:	Tuesday, September 06, 2022	Sheet	4 of 4