Product Advisor (PA)

Subject: Correction to Standard Microcircuit Drawing (SMD) 5962-15228, Intersil Product ISL72026/7/8*
Publication Date: 11/8/2017
Effective Date: 11/8/2017

Revision Description:
Initial Release

Description of Change:
This notice is to inform you of corrections to the electrical specifications in DLA (Defense Logistics Agency) SMD (Standard Microcircuit Drawing) 5962-15228 for the listed ISL72026/7/8* products. The corrections are to the absolute maximum ratings of the receiver output current and the recommended operating conditions for IOH and IOL receiver as shown in Appendix A.

Affected Products:

Standard microcircuit drawing	Intersil Part Number	Standard microcircuit drawing	Intersil Part Number
	ISL72026SEHF/PROTO	5962L1522802V9A	ISL72027SEHVX
5962L1522801VXC	ISL72026SEHVF		ISL72027SEHX/SAMPLE
5962L1522801V9A	ISL72026SEHVX		ISL72028SEHF/PROTO
	ISL72026SEHX/SAMPLE	5962L1522803VXC	ISL72028SEHVF
	ISL72027SEHF/PROTO	5962L1522803V9A	ISL72028SEHVX
5962L1522802VXC	ISL72027SEHVF		ISL72028SEHX/SAMPLE

Reason for Change:
The change corrects the SMD to reflect the product characteristics. Detail regarding the change are contained in Appendix A. The updated SMD is available on the DLA web site at: https://landandmaritimeapps.dla.mil/programs/smcr/default.aspx

Product Identification:
There have been no changed to the die/silicon or product itself. There will be no change in to the external marking of the package parts.

Qualification status: Not applicable
Sample availability: 11/8/2017
Device material declaration: Available upon request

Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: PCN-US@INTERSIL.COM	Europe: PCN-EU@INTERSIL.COM	Japan: PCN-JP@INTERSIL.COM	Asia Pac: PCN-APAC@INTERSIL.COM

Appendix A

From:

1.3 Absolute maximum ratings. 1/

V_{CC} to GND with / without ion beam	-0.3 V to 5.5 V
CANH, CANL, VREF under ion beam	±16 V
CANH, CANL, VREF	±20 V
I/O voltages	
D, R, RS	-0.5 V to 7 V
Receiver output current	-50 mA to 50 mA
Output short circuit duration	Continuous
Storage temperature range	-65°C to +150°C
Junction temperature (T_J)	+175°C
Lead temperature (soldering, 10 seconds)	+300°C
Thermal resistance, junction-to-ambient (θ_{JA}) direct attach	39°C/W 2/
Thermal resistance, junction-to-case (θ_{JC}) direct attach	7°C/W 3/

1.4 Recommended operating conditions.

V_{CC} supply voltage	3.0 V to 3.6 V
Voltage on CAN I/O	-7 V to 12 V
V_{IH} D logic pin (D, LBK)	2 V to 5.5 V
V_{IL} D logic pin (D, LBK)	0 V to 0.8 V
I_{OH} driver (V_{OD} = 1.5 V, V_{CC} = 3.3 V)	-40 mA
I_{OH} receiver (V_{OH} = 2.4 V)	-8 mA
I_{OL} driver (V_{OD} = 1.5 V, V_{CC} = 3.3 V)	+40 mA
I_{OL} receiver	+8 mA
Ambient operating temperature range (T_A)	-55°C to +125°C

To:

1.3 Absolute maximum ratings. 1/

V_{CC} to GND with / without ion beam	-0.3 V to 5.5 V
CANH, CANL, VREF (with / without ion beam)	±20 V
I/O voltages	
D, R, RS	-0.5 V to 7 V
Receiver output current	-10 mA to 10 mA
Output short circuit duration	Continuous
Storage temperature range	-65°C to +150°C
Junction temperature (TJ)	+175°C
Lead temperature (soldering, 10 seconds)	+300°C
Thermal resistance, junction-to-ambient (θ_{JA}) direct attach	39°C/W 2/
Thermal resistance, junction-to-case (θ_{JC}) direct attach	7°C/W 3/

1.4 Recommended operating conditions.

V_{CC} supply voltage	3.0 V to 3.6 V
Voltage on CAN I/O	-7 V to 12 V
V_{IH} D logic pin (D, LBK)	2 V to 5.5 V
V_{IL} D logic pin (D, LBK)	0 V to 0.8 V
I_{OH} driver (V_{OD} = 1.5 V, V_{CC} = 3.3 V)	-40 mA
I_{OH} receiver (V_{OH} = 2.4 V)	-4 mA
I_{OL} driver (V_{OD} = 1.5 V, V_{CC} = 3.3 V)	+40 mA
I_{OL} receiver	+4 mA
Ambient operating temperature range (T_A)	-55°C to +125°C