

MODULAR, POWERFUL AND FUTURE PROVEN

Debugger & Trace Solutions for **RENESAS** Microprocessors & Microcontrollers

Renesas offers a comprehensive portfolio of Microcontrollers (MCUs) and Microprocessors (MPUs) to address requirements across a wide set of industries and applications. They implement several kinds of Arm®, RISC-V and inhouse-designed CPUs.

Lauterbach's market leading TRACE32® debug and trace development tools provide not only full insights into all today's Renesas chips for the whole SoC lifecycle: They also facilitate the path to certification for safety-critical applications in accordance with ISO 26262, IEC 61508 and other standards.

Thanks to the long-standing close partnership with Renesas, future chip developments are also accompanied by Lauterbach from the very beginning ensuring a future proof investment.

KEY FEATURES

Covering the Whole Renesas Chip Lifecycle

Besides to real silicon, TRACE32® tools can connect to various simulators, emulators, and virtual targets. Developers can reuse the scripts generated in this phase throughout the entire product life cycle because the user interface and scripting commands stay the same from simulations through use in the field by the customers.

Unlimited Multicore Debugging

Renesas MPUs and MCUs implement different kinds of Arm® CPUs of the Cortex-A/R/M families as well as RISC-V and inhouse-designed CPUs like RH850 or RX. No matter what kind of multicore system is used, Lauterbach's TRACE32® tools support them all.

OS-Aware Debugging of Any Core

Lauterbach's TRACE32® OS-aware debugging provides key insights into applications and the operating systems they are running on, no matter if rich operating systems like Linux, real-time operating systems (RTOS), or a mixture of all is used. With this, engineers can better understand how they are behaving and utilizing chip resources.

Simplifying Certification of Safety-Critical Applications

Lauterbach's certified Tool Qualification Support Kits (TQSK) provide everything developers need to qualify TRACE32® solutions according to ISO 26262, IEC 61508 and IEC 62304. Different TQSK variants prove the suitability of code coverage, debugging, and instruction set simulator to reduce time-to-market, effort, and costs.

DOWNLOAD OUR SOLUTIONS OVERVIEW



All you need to know about Lauterbach's debugging and tracing tools.



LEARN MORE @
lauterbach.com



RENESAS



Debug- and Trace-Solutions for All Renesas MCUs and MPUs

Chip-Family	Architectures	Debug (Virtual Targets + Chips)	Instruction Set Simulator	Trace Virtual Targets	On-Chip Trace	Off-Chip Trace	XCP Debugging
CHIPS		AVAILABLE TRACE32® SOLUTIONS					
R-Car including Gen. 5	Arm® Cortex-A/R, RH850*, Arc*	✓ 1/3	✓ 2	✓ 1	✓ 3	✓ 5	✓ 6
RH850	RH850, GTM*	✓ 1/3	✓ 2	✓ 1	✓* 3	✓* 5	✓ 6
RZ	Arm® Cortex-A/R/M, RISC-V®	✓ 1/3	✓ 2	✓ 1	✓* 3	✓* 5	✓ 6
RX	Renesas RX	✓ 3	✓ 2	—	✓ 3	✓ 5	✓ 6
RA	Arm® Cortex-M	✓ 1/3/4	✓ 2	✓ 1	✓* 3/4/5	✓* 4/5	✓ 6
RISC-V® 32-bit	Renesas RISC-V®	✓ 1/3/4	✓ 2	✓ 1	—	—	—
...further Renesas Chips	Please search Lauterbach's chip database – see QR code below						

* : Availability depending on the sub series

DISCOVER MORE

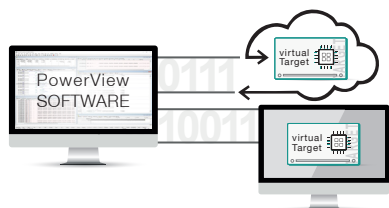


Right TRACE32®
Solution for Your Chip



Supported Emulators
and Virtual Targets

PowerView Software



1 Software Debugging and Tracing for virtual targets and Emulators

Instruction Set Simulator



2 ISS for developing or testing application code without target hardware

PowerDebug System



3 Powerful, modular, flexible debug system for 150+ microprocessor families

µTrace®



4 Cost-efficient all-in-one Debug- and Trace-Tool for Cortex®-M

Trace Extensions



5 Highest performance parallel and serial trace system for non-intrusive system profiling

XCP Debug & Trace



6 Software debugger via XCP measurement and calibration protocol