

September 25, 2014

Product Specifications of the RL78/G1G MCUs

| Group name | | RL78/G1G | | | |
|------------------------------|---|--|-------------|---|--|
| Pin count | | 30 pin | 32 pin | 44 pin | |
| Part # | | R5F11EA | R5F11EB | R5F11EF | |
| Code flash memory (Bytes) | | 8/16 K | | | |
| Data flash memory | | None | | | |
| RAM (Bytes) | | 1.5 K | | | |
| Main system clock | | External clock: 1-20 MHz On-chip high-speed system clock: 48/24/16/12/8/4/1 MHz On-chip low-speed system clock: 15 kHz | | | |
| СРИ | | RL78 CPU (Multiplication and Division/ Multiplication and Accumulation instructions are supported) | | | |
| I/O | | 26 | 28 | 40 | |
| Timer (Note 1) | | 16-bit: 7 ch 12-bit: 1 ch Watchdog timer: 1 ch | | | |
| 10-bit A/D | | 8 | | 12 | |
| Comparator | | 2 | | | |
| Serial I/F | • | 1 ch 2 ch 1 ch | | | |
| External interrupt | | 6 | | 7 | |
| ELC | | Event signals Event trigger | | Event signals: 19 Event trigger output: 6 | |
| Other peripherals | | On-chip volta (LVD), | ge detector | On-chip voltage detector (LVD), On-chip power-on-reset | |

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|-------------------------------|--|--|--|
| | On-chip power-on-reset (POR) circuit, Safety functions | (POR)circuit, Key return, Safety functions | |
| Power supply voltage | 2.7 V-5.5 V | | |
| Operating ambient temperature | −40°C-85°C | | |

(Note 1) The 12-bit timer is exclusively for use as an interval timer.

(Note 2) Maximum values for single use. The CSI and simplified I²C cannot be used when one UART channel is in use.

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