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**Product Specifications of the Two-Port USB 3.0 Hub Controller, the uPD720211**

Product Specifications of the Two-Port USB 3.0 Hub Controller, the uPD720211

- Compliant with Universal Serial Bus 3.0 Specification, Revision 1.0, established by USB Implementers Forum, Inc.
- Supports the following USB data transfer speeds:  
Low-speed (1.5 Mbps), Full-speed (12 Mbps), High-speed (480 Mbps), Super-speed (5 Gbps)
- Two downstream ports
- Supports two VBUS control methods:  
Individual and gang control (VBUS switching, overcurrent detection)
- Supports compound device settings (hub products including functional devices).
- Supports external clock output (24/12 MHz).
- Low-power configuration enabling Energy Star compliance
- 3.3 V single power supply operation
- On-chip regulator for 3.3 V to 1.05 V conversions
- System clock: 24 MHz crystal oscillator or external clock input
- Supports external ROM.

- Allows setting of custom information for the customer system, including vendor ID, product ID, and UUID.  
Possible to avoid connection compatibility problems through future firmware upgrades.
- Reduced mounting area  
QFN package effectively reduces the mounting area, and pin assignments take into account actual wiring patterns.  
Mounting area and overall cost are reduced by on-chip integration of peripheral functions.
- Includes suspend state output function.

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