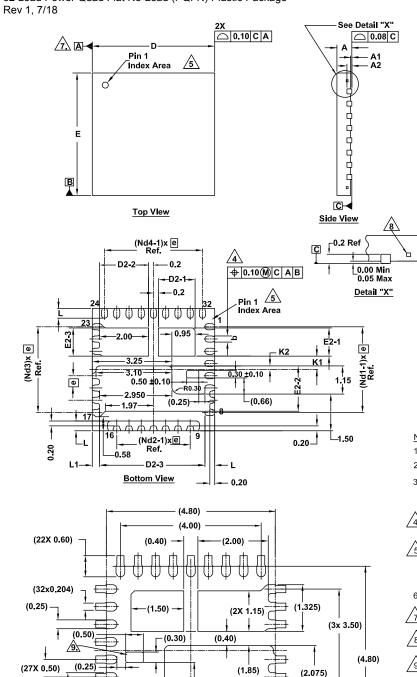
Plastic Packages for Integrated Circuits

Package Outline Drawing

L32.5x5W
32 Lead Power Quad Flat No-Lead (PQFN) Plastic Package



Symbol	Millimeters		
	Min.	Nom.	Max.
Α	0.70	0.75	0.80
A1	0.00	-	0.05
A2	0.20 Ref. 0.30		
b	0.20	0.25	
D	5.00 BSC		
D2-1	1.45	1.50	1.55
D2-2	1.95	2.00	2.05
D2-3	4.25	4.30	4.35
е	0.50 BSC		
E	5.00 BSC		
E2-1	1.10	1.15	1.20
E2-2	1.80	1.85	1.90
E2-3	1.10	1.15	1.20
K1	0.55 BSC		
K2	0.15 BSC		
L	0.35	0.40	0.45
L1	0.25	0.30	0.35
N (Note 3)	32		
Nd1 (Note 3)	8 (Pin 1~Pin 8)		
Nd2 (Note 3)	8 (Pin 9~Pin 15)		
Nd3 (Note 3)	7 (Pin 16~Pin 22)		
Nd4 (Note 3)	9 (Pin 23~Pin 31)		

NOTE:

- 1. Use millimeters as the primary measurement.
- 2. Dimensioning & tolerances conform to ASME Y14.5M. 1994.
- N is the number of terminals.
 Nd1 and Nd3 is the number of terminals in Y-direction &
- Nd2 and Nd4 is the number of terminals in X-direction.

 Dimension b applies to plated terminal and is measured between 0.20mm and 0.25mm from terminal tip.
- The configuration of the pin #1 identifier is optional, but must be located within the zone indicated.

The pin #1 identifier may be either a mold or mark feature.

- 6. Package warpage MAX 0.08mm.
- Applied only for terminals.
 - 8. Tiebar shown (if present) is a non-functional feature.
 - 9. Exposed gate pad in the land pattern "optional".



(3.00)

(0.70)

(0.66)

(1.25)

