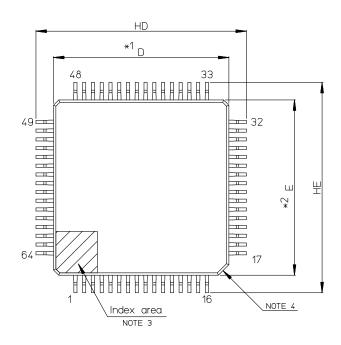
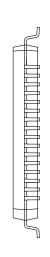
1/1

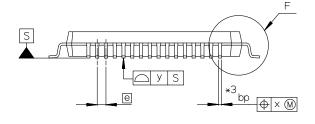
外形図 Outline Drawing (PLQP0064KB-C)

ルネサス エレクトロニクス株式会社 Renesas Electronics Corporation

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-LFQFP64-10×10-0.50	PLQP0064KB-C		0.3g









- DIMENSIONS '*1" AND '*2" DO NOT INCLUDE MOLD FLASH.
 DIMENSION '*3" DOES NOT INCLUDE TRIM OFFSET.
 PIN 1 VISUAL INDEX FEATURE MAY VARY, BUT MUST BE
 LOCATED WITHIN THE HATCHED AREA.
 CHAMFERS AT CORNERS ARE OPTIONAL; SIZE MAY VARY. 1. 2. 3.

<	A2		0.25
	A1		
		_	Lp
			L1_
		Detail F	

Reference	Dimension in Millimeters			
Symbol	Min	Nom	Max	
D	9.9	10.0	10.1	
Е	9.9	10.0	10.1	
A2		1.4		
HD	11.8	12.0	12.2	
HE	11.8	12.0	12.2	
А			1.7	
A1	0.05		0.15	
bp	0.15	0.20	0.27	
O	0.09		0.20	
θ	0 "	3.5"	8 "	
Ф		0.5		
×			0.08	
У			0.08	
Lp	0.45	0.6	0.75	
L1		1.0		