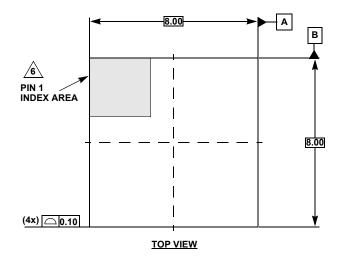
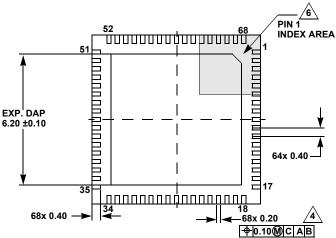
## **Plastic Packages for Integrated Circuits**

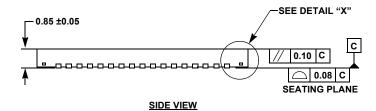
## 1. Package Outline Drawing

L68.8x8B 68 LEAD QUAD FLAT NO-LEAD PLASTIC PACKAGE Rev 1, 11/17



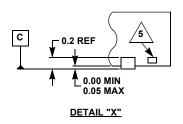


**BOTTOM VIEW** 



(7.80 TYP) 6.20 (68x 0.40) (68x 0.20) (68x 0.60)

TYPICAL RECOMMENDED LAND PATTERN



## NOTES:

- Dimensions are in millimeters.
  Dimensions in ( ) for Reference Only.
- 2. Dimensioning and tolerancing conform to AMSE Y14.5m-1994.
- 3. Unless otherwise specified, tolerance: Decimal ±0.05
- 4. Dimension applies to the metallized terminal and is measured between 0.15mm and 0.30mm from the terminal tip.
- 5. Tiebar shown (if present) is a non-functional feature.
- The configuration of the pin #1 identifier is optional, but must be located within the zone indicated. The pin #1 identifier may be either a mold or mark feature.

