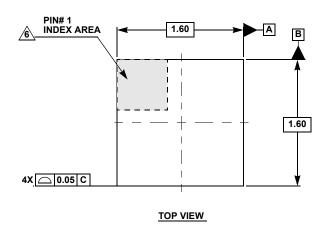
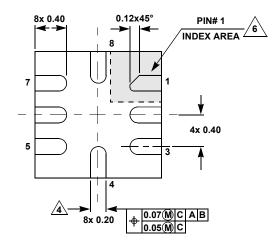
Plastic Packages for Integrated Circuits

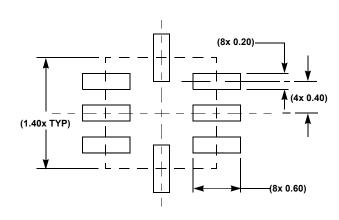
Package Outline Drawing

L8.1.6x1.6 8 LEAD QUAD FLAT NO-LEAD PLASTIC PACKAGE Rev 0, 7/14

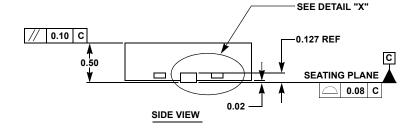


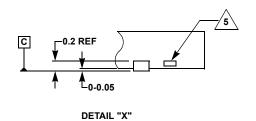


BOTTOM VIEW



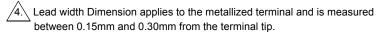
TYPICAL RECOMMENDED LAND PATTERN





NOTES:

- Dimensions are in millimeters.
 Dimensions in () for Reference Only.
- 2. Dimensioning and tolerancing conform to ASME Y14.5M-1994.
- 3. Unless otherwise specified, tolerance : Decimal ± 0.05



5. Tiebar shown (if present) is a non-functional feature.

The configuration of the pin #1 identifier is optional, but must be located within the zone indicated. The pin #1 identifier may be either a mold or mark feature.

