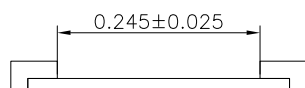
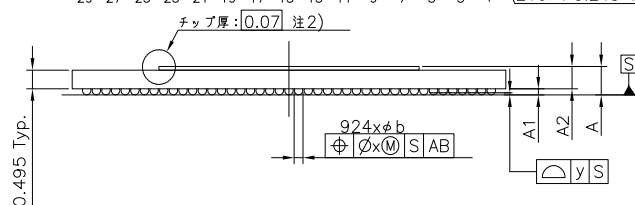
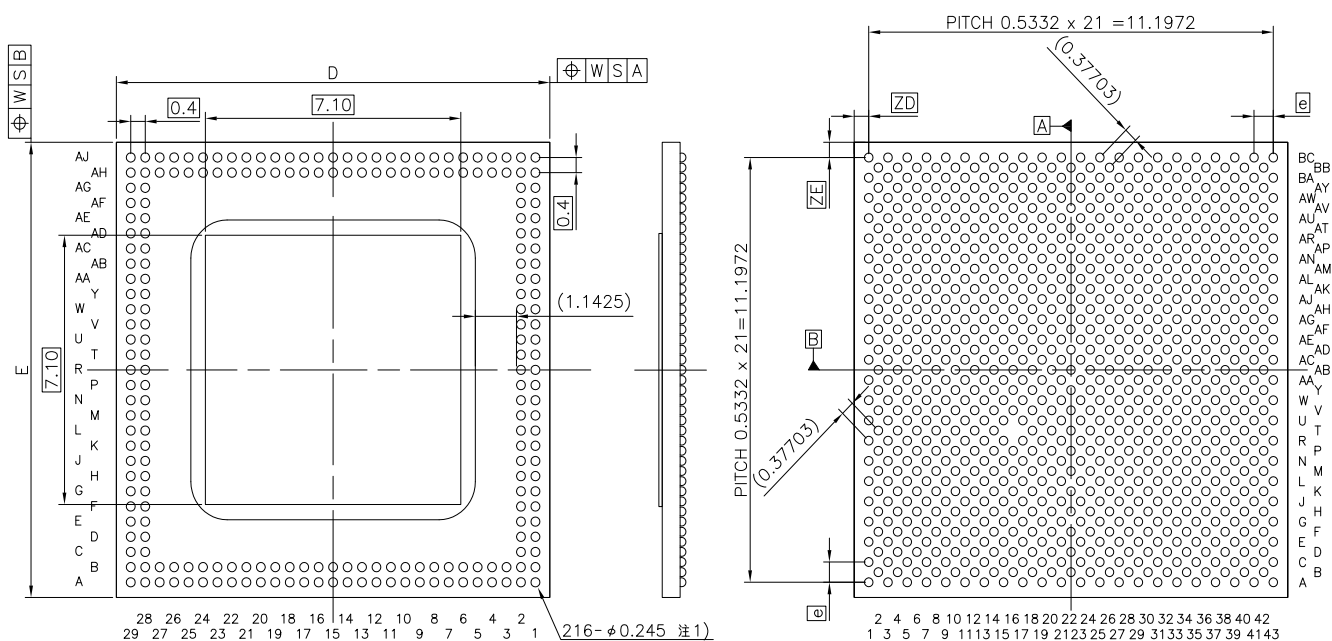


**外形図 Outline drawing**  
**PVBG0924ZB-A**

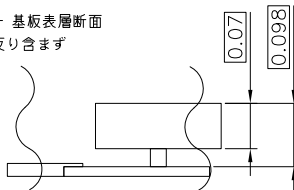
RDK-G-001636 1/1

ルネサス エレクトロニクス株式会社  
Renesas Electronics Corporation

| JEITA Package code    | RENESAS code | MASS(TYP.)[g] |
|-----------------------|--------------|---------------|
| P-WFBGA924-12x12-0.38 | PVBG0924ZB-A | 0.25          |



注2): チップ-基板表層断面  
PKG反り含まず



| Reference Symbol | Dimension in Millimeters |        |        |
|------------------|--------------------------|--------|--------|
|                  | Min                      | Nom    | Max    |
| D                | 11.925                   | 12.00  | 12.075 |
| E                | 11.925                   | 12.00  | 12.075 |
| w                | —                        | —      | 0.20   |
| e                | —                        | 0.5332 | —      |
| A                | —                        | 0.749  | 0.850  |
| A1               | 0.125                    | 0.16   | 0.195  |
| A2               | —                        | 0.589  | —      |
| b                | 0.20                     | 0.235  | 0.27   |
| x                | —                        | —      | 0.05   |
| y                | —                        | —      | 0.085  |
| ZD               | —                        | 0.4014 | —      |
| ZE               | —                        | 0.4014 | —      |