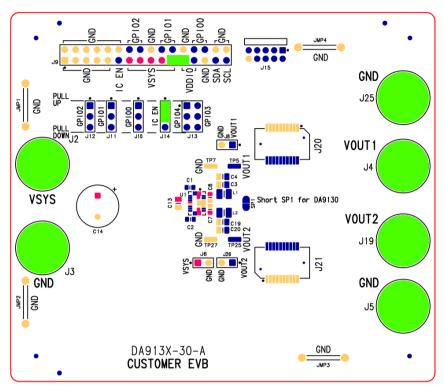**DA913x Quick Start**

1   Quick Start

Figure 1: DA913x-30 Jumpers and headers location

Connections from the table below are highlighted in green

Table 1: Jumpers and headers overview

Connection	Description	Default	Location
J14	IC_EN	Jumper fitted to pull-up position	Top-left, 4th jumper from the left
J9	GPIO header	Jumper between VSYS and VDDIO	TOP left, jumper on positions 21 and 23
J2 - J3	VSYS - GND	Supply from PSU, 4V - 5A recommended	Left of EVB
J4 - J25	VOUT1 - GND	Channel 1 output	Top right of EVB
J5 - J19	VOUT2 [Note 1] - GND	Channel 2 [Note 1] output	Bottom right of EVB

Note 1   VOUT2 is connected to VOUT1 on DA9130 boards.

1.1   Instructions

Make sure the jumper configuration is correct. IC_EN has to be on pull-up position and VDDIO connected to VSYS.

Connect a bench PSU to VSYS (J2, positive) and GND (J3, return) and make sure it can supply 4V and 5A. Measure the outputs on VOUT1 (J4) and VOUT2 (J19).

Pull-up and pull-down jumpers may be needed on GPIOs 0 to 2 depending on the configuration.

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Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

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