

TEST REPORT

ISL70591SEH, ISL73591SEH, ISL70592SEH, ISL73592SEH

Single Event Effects (SEE) Testing

TR067 Rev.0.00 May 25, 2018

Introduction

The intense proton and heavy ion environment encountered in space applications can cause a variety of Single-Event Effects (SEE) in electronic circuitry, including Single Event Upset (SEU), Single Event Transient (SET), Single Event Functional Interrupt (SEFI), Single Event Latch-Up (SEL), Single Event Gate Rupture (SEGR) and Single Event Burnout (SEB). SEE can lead to system-level performance issues including disruption, degradation, and destruction. For predictable and reliable space system operation, individual electronic components should be characterized to determine their SEE response. This report discusses the results of SEE testing performed on the ISL70591SEH and ISL70592SEH precision current sources.

Although this report is written for the ISL70591SEH and ISL70592SEH, it applies equally to the ISL73591SEH and ISL73592SEH as they are the same silicon die, differing only in radiation assurance testing. The ISL73591SEH and ISL73592SEH see only 75krad(Si) of low dose rate (0.01rad(Si)/s) assurance, while the ISL70591SEH and ISL70592SEH also see assurance testing to 100krad(Si) at high dose rate (50-300rad(Si)/s).

Product Description

The ISL70591SEH and ISL70592SEH are radiation hardened precision current sources. These parts provide precision (\pm 1%) current sources across voltages of 3V to 40V and across temperatures of -55°C to +125°C at 100µA and 1mA, respectively. The parts are manufactured using the proprietary Renesas PR40 SOI complementary bipolar process.

The parts used for the testing described here were from manufacturing wafer lot XAL2X and wafer 01 for the ISL70591SEH and wafer 13 for the ISL70592SEH. They were mounted without lids in the 4 Ld flat-pack production package with code KCW. They did not receive any of the production burn-ins or over-temperature testing because they did not have lids to allow SEE irradiation.

Related Literature

For a full list of related documents, visit our website

• ISL70591SEH, ISL73591SEH, ISL70592SEH, ISL73592SEH product pages

1. SEE Test Objectives

The ISL70591SEH and ISL70592SEH were tested to determine susceptibility to destructive single event effects (SEB) and to characterize Single Event Transient (SET) behavior over different operating conditions.

2. SEE Test Facility

Testing was performed at the Texas A&M University (TAMU) Radiation Effects Facility of the Cyclotron Institute heavy ion facility. This facility is coupled to a K500 super-conducting cyclotron, which is capable of generating a wide range of particle beams with the various energy, flux, and fluence levels needed for advanced radiation testing. Further details on the test facility can be found at <u>http://cyclotron.tamu.edu/</u>. The Devices Under Test (DUTs) were located in air outside an Aramica window for the ion beam. Ion Linear Energy Transfer (LET) values are quoted at the DUT surface. Signals were communicated to and from the DUT test fixture through 20 foot cables connecting to the control room. The testing reported here was conducted on March 15, 2018.

3. SEE Test Setup

For SEB testing, a current meter was placed in series with the Device Under Test (DUT) to monitor the current through the DUT. A voltage supply set the bias across the combination of the DUT and the current meter. The current was recorded before and after irradiation to monitor for permanent changes in the DUT current. The DUT temperature was set to +125°C using a thin film resistor heater on the back of the PCB.

For SET testing, a current sense resistor was placed in series with the DUT and the voltage across the resistor was monitored by oscilloscope. The DUT and resistor combination was biased with a voltage source. The voltage drop across the resistor was set to a nominal 1V by the resistor value selection. The ISL70591SEH used a $10k\Omega$ resistor for its 100μ A current. The ISL70592SEH used a $1k\Omega$ sense resistor with its 1mA current. The SET testing was done at ambient temperature, about +25°C. The oscilloscope was set to trigger trace capture and storage on ±10mV (±1%) deviations of the resistor voltage. The captured traces were reviewed at a later time.

The SEE test boards had four DUTs mounted in close proximity so that all four could be irradiated at the same time. Each part was monitored independently during the irradiations. This approach minimized ion beam time needed to accomplish the testing.

Destructive SEE Testing of the ISL70591SEH and ISL70592SEH with LET = 86MeV•cm²/mg

Destructive SEE (SEB) testing of the ISL70591SEH and ISL70592SEH was done by varying the supply voltage from 35V to 38V in 1V steps for sequential irradiations. Before and after irradiations, the device current was measured with a digital current meter. Four units of each type were exposed at each increasing voltage level to 1×10^7 ion/cm² of 2.954GeV normal incidence gold for 86MeV•cm²/mg in air at 30mm outside the Aramica window. All the units passed (no current change in excess of ±0.5%) at 35V. The ISL70591SEH encountered catastrophic failures at 36V and 37V for the ISL70591SEH, with currents going to milliamps. The ISL70592SEH encountered parametric failures at 37V and 38V. Table 1 summarizes the test data for the ISL70591SEH. Table 2 on page 3 summarizes the test data for the ISL70592SEH. Because the silicon design is identical for the two parts, the lower failure value should be applied to both devices.

$\Gamma_{CASE} = \pm 125^{\circ}C \pm 10^{\circ}C$. The Highlights indicate Fallures						
ISL	70591SEH	ID (µA) at 35V	ID (µA) at 36V	ID (μA) at 37V		
DUT1	Pre-irradiation	101.3	101.9	101.2		
	Post-irradiation	101.7	101.8	101.3		
	Delta	0.4%	-0.1%	0.1%		

Table 1. ISL70591SEH SEB Monitor Measurements for LET = 86MeV·cm ² /mg to a Fluence of 1x10 ⁷ ion/cm ² with
T _{CASE} = +125°C ±10°C. The Highlights Indicate Failures

ISL7	0591SEH	ID (μA) at 35V	ID (μA) at 36V	ID (μA) at 37V	
DUT2	Pre-irradiation	100.1	100.1	100.2	
	Post-irradiation	100.2	100.4	100.0	
	Delta	0.1%	0.3%	-0.2%	
DUT3	Pre-irradiation	101.3	101.4	-	
	Post-irradiation	101.4	2mA	-	
	Delta	0.1%	-	-	
DUT4	Pre-irradiation	101.2	101.4	101.7	
	Post-irradiation	101.4	101.6	3mA	
	Delta	0.2%	0.2%	-	

Table 1. ISL70591SEH SEB Monitor Measurements for LET = 86MeV·cm²/mg to a Fluence of 1x10⁷ion/cm² with T_{CASE} = +125°C ±10°C. The Highlights Indicate Failures (Continued)

Table 2. ISL70592SEH SEB Monitor Measurements for LET = 86MeV·cm²/mg to a Fluence of 1x107ion/cm² with T_{CASE} = +125°C ±10°C. The Highlights Indicate Failures

ISL70	591SEH	ID (μA) at 35V	ID (µA) at 36V	ID (µA) at 37V	ID (µA) at 38V
DUT5	Pre-irradiation	1.003	1.004	1.004	1.003
	Post-irradiation	1.003	1.004	1.002	1.032
	Delta	0.0%	0.0%	-0.2%	2.9%
DUT6	Pre-irradiation	0.999	1.002	1.006	1.01
	Post-irradiation	1.002	1.006	1.009	1.01
	Delta	0.3%	0.4%	0.3%	0.0%
DUT7	Pre-irradiation	1.000	0.995	0.996	0.999
	Post-irradiation	0.998	0.996	0.999	1.001
	Delta	-0.2%	0.1%	0.3%	0.2%
DUT8	Pre-irradiation	1.002	1.003	1.006	1.029
	Post-irradiation	1.003	1.005	1.027	1.025
	Delta	0.1%	0.2%	2.1%	-0.4%

The data presented in <u>Tables 1</u> and <u>2</u> supports the conclusion that the ISL70591SEH and ISL70592SEH are immune to damaging SEE for operation at V+ = 35V with case temperature of +125°C and irradiation with gold ions of LET 86MeV•cm²/mg at normal incidence.

5. SET Testing of the ISL70591SEH and ISL70592SEH Current Sources

The SET testing of the ISL70591SEH and ISL70592SEH focused on looking for perturbations of ± 10 mV (oscilloscope trigger and capture) on a nominal signal of 1V across the appropriated current sense resistor for the 100µA and 1mA currents, respectively. The oscilloscope was programmed to capture the event from 10µs before trigger to 90µs after trigger. Three channels captured the event, one scaled to 20mV/division (± 80 mV span), one to 1V/division (± 4 V span), and the last at 10V/division (± 40 V span). Biasing was done at both 4V and the SEB limit of 35V applied across the series combination of the DUT and the resistor. This resulted in DUT biases of 3V and 34V. Four parts were each irradiated at LET of 86 (Au), 28 (Kr), 20 (Cu), 8.5 (Ar), and 2.7 (Ne) MeV•cm²/mg at +25°C case temperature. The irradiations were all done at normal incidence. For each irradiation the capture of SET was terminated at just over 1000 counts or at a fluence of 1x10⁷ion/cm², whichever came first. Table 3 summarizes the ISL70591SEH SET capture counts. Table 4 on page 5 summarizes the ISL70592SEH SET capture counts.

ISL70591SEH			4V Bias, 10kΩ		35V Bias, 10kΩ		
Species LET (MeV·cm ² /mg)	DUT	SET Count	Fluence (ion/cm ²)	X-Section (cm ²)	SET Count	Fluence (ion/cm ²)	X-Section (cm ²)
Au	5	1006	7.50x10 ⁵	1.34x10 ⁻³	1017	5.96x10 ⁵	1.71x10 ⁻³
86	6	1059	7.70x10 ⁵	1.38x10 ⁻³	1022	5.96x10 ⁵	1.71x10 ⁻³
	7	1010	9.60x10 ⁵	1.05x10 ⁻³	1008	9.40x10 ⁵	1.07x10 ⁻³
	8	1351	7.80x10 ⁵	1.73x10 ⁻³	1021	5.47x10 ⁵	1.87x10 ⁻³
Kr	9	1002	1.14x10 ⁶	8.79x10 ⁻⁴	1013	8.80x10 ⁵	1.15x10 ⁻³
28	10	1003	1.22x10 ⁶	8.22x10 ⁻⁴	1002	9.10x10 ⁵	1.10x10 ⁻³
	11	1002	1.33x10 ⁶	7.53x10 ⁻⁴	1025	1.01x10 ⁶	1.01x10 ⁻³
	12	1023	9.42x10 ⁵	1.09x10 ⁻³	1005	7.87x10 ⁵	1.28x10 ⁻³
Cu	13	1001	1.79x10 ⁶	5.59x10 ⁻⁴	1004	1.18x10 ⁶	8.51x10 ⁻⁴
20	14	1000	1.85x10 ⁶	5.41x10 ⁻⁴	1009	1.23x10 ⁶	8.20x10 ⁻⁴
	15	1015	1.93x10 ⁶	5.26x10 ⁻⁴	1020	1.38x10 ⁶	7.39x10 ⁻⁴
	16	1004	1.32x10 ⁶	7.61x10 ⁻⁴	1011	1.06x10 ⁶	9.54x10 ⁻⁴
Ar	13	1036	2.50x10 ⁶	4.14x10 ⁻⁴	1168	2.04x10 ⁶	5.73x10 ⁻⁴
8.5	14	1031	2.50x10 ⁶	4.12x10 ⁻⁴	1076	2.04x10 ⁶	5.27x10 ⁻⁴
	15	1005	2.50x10 ⁶	4.02x10 ⁻⁴	1007	2.04x10 ⁶	4.94x10 ⁻⁴
	16	1111	2.50x10 ⁶	4.44x10 ⁻⁴	1250	2.04x10 ⁶	6.13x10 ⁻⁴
Ne	13	1006	6.71x10 ⁶	1.50x10 ⁻⁴	1008	1.00x10 ⁷	1.01x10 ⁻⁴
2.7	14	1012	7.18x10 ⁶	1.41x10 ⁻⁴	1008	1.00x10 ⁷	1.01x10 ⁻⁴
	15	1014	9.10x10 ⁶	1.11x10 ⁻⁴	1057	1.00x10 ⁷	1.06x10 ⁻⁴
	16	1060	3.47x10 ⁶	3.05x10 ⁻⁴	1009	1.00x10 ⁷	1.01x10 ⁻⁴

Table 3. ISL70591SEH ±10mV Perturbation Counts Across a 10Ω Resistor at +25°C

ISL70592SEH			4V Bias, 1kΩ		35V Bias, 1kΩ		
Species LET (MeV·cm²/mg)	DUT	SET Count	Fluence (ion/cm²)	X-section (cm²)	SET Count	Fluence (ion/cm ²)	X-section (cm ²)
Au	5	1004	6.76x10 ⁵	1.49x10 ⁻³	1023	5.35x10 ⁵	1.91x10 ⁻³
86	6	1006	8.58x10 ⁵	1.17x10 ⁻³	1004	6.88x10 ⁵	1.46x10 ⁻³
	7	1009	1.02x10 ⁶	9.89x10 ⁻⁴	1013	9.01x10 ⁵	1.12x10 ⁻³
	8	1016	6.08x10 ⁵	1.67x10 ⁻³	1026	5.15x10 ⁵	1.99x10 ⁻³
Kr	9	1015	1.13x10 ⁶	8.98x10 ⁻⁴	1010	8.53x10 ⁵	1.18x10 ⁻³
28	10	1010	1.08x10 ⁶	9.35x10 ⁻⁴	1005	8.45x10 ⁵	1.19x10 ⁻³
	11	1034	1.18x10 ⁶	8.76x10 ⁻⁴	1006	9.58x10 ⁵	1.05x10 ⁻³
	12	1007	1.02x10 ⁶	9.87x10 ⁻⁴	1012	7.45x10 ⁵	1.36x10 ⁻³
Cu	13	1008	1.68x10 ⁶	6.00x10 ⁻⁴	1011	1.10x10 ⁶	9.19x10 ⁻⁴
20	14	1006	1.74x10 ⁶	5.78x10 ⁻⁴	1012	1.15x10 ⁶	8.80x10 ⁻⁴
	15	1034	1.81x10 ⁶	5.71x10 ⁻⁴	1030	1.35x10 ⁶	7.63x10 ⁻⁴
	16	1028	1.35x10 ⁶	7.61x10 ⁻⁴	1018	1.01x10 ⁶	1.01x10 ⁻³
Ar	13	1084	2.11x10 ⁶	5.14x10 ⁻⁴	1192	2.00x10 ⁶	5.96x10 ⁻⁴
8.5	14	1051	2.11x10 ⁶	4.98x10 ⁻⁴	1025	2.00x10 ⁶	5.13x10 ⁻⁴
	15	1007	2.11x10 ⁶	4.77x10 ⁻⁴	1044	2.00x10 ⁶	5.22x10 ⁻⁴
	16	1123	2.11x10 ⁶	5.32x10 ⁻⁴	1246	2.00x10 ⁶	6.23x10 ⁻⁴
Ne	13	1114	3.12x10 ⁶	3.57x10 ⁻⁴	1019	2.71x10 ⁶	3.76x10 ⁻⁴
2.7	14	1149	3.23x10 ⁶	3.56x10 ⁻⁴	1010	2.92x10 ⁶	3.46x10 ⁻⁴
	15	1047	4.67x10 ⁶	2.24x10 ⁻⁴	1193	3.22x10 ⁶	3.70x10 ⁻⁴
	16	1021	2.81x10 ⁶	3.63x10 ⁻⁴	1010	4.36x10 ⁶	2.32x10 ⁻⁴

Table 4. ISL70592SEH ±10mV Perturbation Counts Across a 1k Ω Series Resistor at +25°C

Figure 1 plots the SET cross section data for both parts (ISL70591SEH and ISL70592SEH) and the two bias conditions (4V and 35V). The two parts exhibit very similar cross sections, as would be expected because the designs are essentially the same. The cross sections are higher for the 35V bias case than for the 4V bias case by about 17% under gold irradiation. The maximum DUT cross section observed was $1.99x10^{-3}$ cm² seen for the ISL70592SEH, DUT8, irradiated with gold (86MeV•cm²/mg). The minimum cross section encountered was $1.01x10^{-4}$ cm² for the ISL70591SEH DUT13 with neon (2.7MeV•cm²/mg). The testing did not establish a threshold LET for the ±10mV events defined. In fact, three of the four conditions extrapolate linearly to negative thresholds based on the mean cross sections of the lowest two LET.



Figure 1. SET cross section versus LET for ±10mV SET criteria. Means are connected between LET, with minimum and maximum by DUT also plotted

The SET can be partitioned by sign of the voltage deviation. This partitioning was done by post processing the SET capture data to determine whether the largest deviation was a positive or negative deviation. This partition of the SET by deviation sign is presented in <u>Tables 5</u> and <u>6</u> for the two parts for the 35V bias case, which yielded the larger cross sections.

	Au	Kr	Cu	Ar	Ne
Total Fluence (ion/cm ²)	2.68x10 ⁶	3.59x10 ⁶	4.85x10 ⁶	8.16x10 ⁶	4.00x10 ⁷
Positive 10mV SET Count	2846	3076	3081	3772	2989
Negative 10mV SET Count	1166	969	964	729	1093
Positive 10mV Cross Section (cm ²)	1.06x10 ⁻³	8.58x10 ⁻⁴	6.35x10 ⁻⁴	4.62x10 ⁻⁴	7.47x10 ⁻⁵
Negative 10mV Cross Section (cm ²)	4.35x10 ⁻⁴	2.70x10 ⁻⁴	1.99x10 ⁻⁴	8.93x10 ⁻⁵	2.73x10 ⁻⁵
Total ±10mV SET Cross Section (cm ²)	1.50x10 ⁻³	1.13x10 ⁻³	8.34x10 ⁻⁴	5.52x10 ⁻⁴	1.02x10 ⁻⁴

Table 5. ISL70591SEH ±10mV SET Partitioned by Sign for the 35V Bias Case

Table 6. ISL70592SEH ±10mV SET Partitioned by Sign for the 35V Bias Case

	Au	Kr	Cu	Ar	Ne
Total Fluence (ion/cm ²)	2.64x10 ⁶	3.40x10 ⁶	4.61x10 ⁶	8.00x10 ⁶	1.32x10 ⁷
Positive 10mV SET Count	2898	2919	2996	3049	2929
Negative 10mV SET Count	1167	1115	1076	1458	1303
Positive 10mV Cross Section (cm ²)	1.10x10 ⁻³	8.58x10 ⁻⁴	6.50x10 ⁻⁴	3.81x10 ⁻⁴	2.22x10 ⁻⁴
Negative 10mV Cross Section (cm ²)	4.42x10 ⁻⁴	3.28x10 ⁻⁴	2.33x10 ⁻⁴	1.82x10 ⁻⁴	9.86x10 ⁻⁵
Total ±10mV SET Cross Section (cm ²)	1.54x10 ⁻³	1.19x10 ⁻³	8.83x10 ⁻⁴	5.63x10 ⁻⁴	3.20x10 ⁻⁴

Note that the SET criteria of ±10mV captures many small SET and comparatively few very large SET. This means that the SET cross sections do not represent one type of SET, but rather represent an entire spectrum of SET magnitudes. Because the 35V bias provided the largest cross sections, the nature of the SET spectrum was further explored for the 35V bias cases. Each SET was analyzed to find the larger of the positive and negative deviations to assign that SET a sign. The resulting SET magnitudes for both signs and all four DUTs at each LET were then combined to yield a distribution of SET magnitudes for each LET and sign. These distributions were then plotted in a form similar to a cumulative probability plot.

The distribution of positive SET deviation magnitudes is depicted in Figure 2 for the ISL70591SEH and a 35V bias with an included $10k\Omega$ series resistor. Because the SET criteria was a $\pm 10mV$ deviation, 100% of the observed events were larger than 10mV deviation. The maximum SET deviations depend on the LET of the irradiating species with gold ($86MeV \cdot cm^2/mg$) presenting very few (<0.6%) deviations closely approaching the bias voltage. For the ISL70591SEH, this means a current of about 3.5mA flowed at the peak of the SET. Krypton ($28MeV \cdot cm^2/mg$) produced deviations topping out about 10V (1mA). For the lowest LET of 2.7MeV $\cdot cm^2/mg$ with neon, the SET magnitude topped out about 107mV ($10.7\mu A$). For the case of Cu ($20MeV \cdot cm^2/mg$), only about 14% of the SET exceeded 100mV in size, while approximately 1% exceeded 1V in magnitude. Only about 4% of the gold SET exceeded 10V, meaning that the net cross section of $1.06 \times 10^{-3} cm^2$, or $4.24 \times 10^{-5} cm^2$ for the 10V gold events.



Figure 2. Distributions of the ISL70591SEH positive SET magnitudes across a $10k\Omega$ series resistor (1V nominal on the resistor) with a 35V bias on the combination

The positive SET magnitude distributions for the ISL70592SEH are shown in Figure 3. The general trends are the same as with the ISL70591SEH, but the lower LET seem to favor somewhat larger SET magnitudes. With neon (2.7MeV•cm²/mg), the SET magnitude tops out at about 340mV instead of the 107mV seen with the ISL70591SEH. About 15% of the neon SET population was larger than 100mV for the ISL70592SEH, while for the ISL70591SEH only 10% was larger than 32mV. For both parts about 3.7% of the events exhibited larger than 10V deviations when irradiated with gold (86MeV•cm²/mg). For both parts, about 46% of the SET with gold were greater than 100mV in magnitude.



Figure 3. Distributions of the ISL70592SEH positive SET magnitudes across a $1k\Omega$ series resistor (1V nominal on the resistor) with a 35V bias on the combination

Similar distribution plots are possible for the negative SET deviations. The negative deviations are limited to the 1V nominal resistor voltage. A negative deviation distributions plot is shown in Figure 4 for the ISL70591SEH. Again, the LET influences the maximum deviations seen as gold produces the largest maximum deviation (634mV), while neon produces the smallest maximum deviation (121mV).



Figure 4. Distributions of the ISL70591SEH negative SET magnitudes across a $10k\Omega$ series resistor (1V nominal on the resistor) with a 35V bias on the combination

The distributions of negative SET magnitudes for the ISI70592SEH are shown in <u>Figure 5</u>. The distributions for the ISL70592SEH are consistently larger than for the ISL70591SEH shown in <u>Figure 4 on page 10</u>.



Figure 5. Distributions of the ISL70592SEH negative SET magnitudes across a $1k\Omega$ series resistor (1V nominal on the resistor) with a 35V bias on the combination

The data provided so far does not address the SET durations or the form of the SET. To provide useful information, some composite plots of the actual SET waveforms for gold irradiation are provided in <u>Figures 6</u> and <u>7</u>. These plots are composites of the twenty largest positive SET, the twenty largest negative SET, and the twenty longest duration SET for a particular DUT irradiated with gold. The plots provide a worst case envelope for what an SET looks like for ions of high LET.

The gold SET for the ISL70591SEH, shown in Figure 6, show an abrupt onset for positive SET and a recovery with a time constant of about 25μ s. The recoveries of positive SET are almost totally complete at 90μ s, the end of the SET capture time range. The negative SET have a more gradual onset, reaching the largest deviation at about 20μ s. The recovery from the negative SET appears to be of a similar time constant to the recovery from positive transients. The evidence implies that recovery from an SET due to gold should be complete by 125μ s.



Figure 6. Composite plot of the twenty largest positive, twenty largest negative, and twenty longest duration SET captured for DUT5 of the ISL70591SEH with gold irradiation

The SET due to gold for the ISL70592SEH, shown in Figure 7, are qualitatively different from the SET for the ISL70591SEH. All the events are basically recovered in 50μ s. The recoveries of positive-going SET seem to include excursions into negative deviations. Conversely, some negative-going deviations lead to subsequent small positive going deviation periods. Some negative SET appear to be solely negative deviations.



Figure 7. Composite plot of the twenty largest positive, twenty largest negative, and twenty longest duration SET captured for DUT5 of the ISL70592SEH with gold irradiation

6. Discussion and Conclusions

The ISL70591SEH and ISL70592SEH were immune to destructive SEE when tested at a supply voltage of 35V, at a case temperature of $\pm 125^{\circ}$ C, and irradiated with normal incidence gold for an LET of 86MeV·cm²/mg to a fluence of 1×10^{7} ion/cm² on each of four parts for each part type. Catastrophic failures were seen for the ISL70591SEH when tested at bias voltages of 36V and 37V. Parametric failures (delta in excess of $\pm 0.5\%$) were seen on the ISL70592SEH at 37V and 38V. The destructive SEE testing data is presented in <u>Tables 1</u> and <u>2</u>.

When defined as voltage deviations larger than 10mV on the series resistor delivering a nominal drop of 1V, the SET proved to be quite common, even when tested with neon (2.7MeV•cm²/mg). With neon and a bias of 35V, the ±10mV SET cross sections averaged 1.02×10^{-4} cm² and 3.20×10^{-4} cm² for the ISI70591SEH and ISL70592SEH, respectively. The average cross sections for neon were 1.55×10^{-4} cm² and 3.13×10^{-4} cm², respectively, for the 4V bias condition. For gold irradiations at 35V bias, the respective average cross sections for ±10mV SET were 1.52×10^{-3} cm² and 1.54×10^{-3} cm². At a 4V bias and gold irradiations the respective average cross sections for ±10mV SET were 1.36×10^{-3} cm².

The distributions of the SET magnitudes are presented in Figures 2 through 5. These plots give the portion of the SET population that register SET magnitudes larger than a given magnitude. This allows insight into the distribution of the SET magnitudes and the relative cross sections for a particular magnitude SET. For example, with reference to Figure 2, approximately 4% of the total positive SET population with gold registered SET magnitudes larger than 10V. This puts the cross section for 10V gold events at $4.24 \times 10^{-5} \text{ cm}^2$ when the $1.06 \times 10^{-3} \text{ cm}^2$ total positive cross section is applied from Table 5. A similar calculation predicts that the cross section for positive gold events greater than 100mV is 46% of the $1.06 \times 10^{-3} \text{ cm}^2$ or $4.88 \times 10^{-4} \text{ cm}^2$.

The durations of SET are bounded by the forms depicted in Figure 6 on page 12 for the ISL70591SEH and Figure 7 on page 13 for the ISL70592SEH. In the former case, the longest SET seen for gold irradiations on the ISL70591SEH recovered in 125μ s. The latter case indicates the longest gold SET on the ISL70592SEH recovered in 50μ s. These represent the worst case SET durations seen in testing.

7. Revision History

	Date	Rev.	Description
ſ	May 25, 2018	0.00	Initial release

Notice

- 1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information
- 2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
- 3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
- 4. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
- 5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
 - Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic "Standard": equipment; industrial robots; etc.

"High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.

Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.

- 6. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges
- 7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
- 8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations
- 9. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or
- 10. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
- 11. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
- 12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.
- (Note 1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries
- (Note 2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics

(Rev.4.0-1 November 2017)

RENESAS

Renesas Electronics Corporation

http://www.renesas.com

SALES OFFICES

Refer to "http://www.renesas.com/" for the latest and detailed information.

Renesas Electronics America Inc.

1001 Murphy Ranch Road, Milpitas, CA 95035, U.S.A. Tel: +1-408-432-8888, Fax: +1-408-434-5351

Renesas Electronics Canada Limited 9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3 Tel: +1-905-237-2004

Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +44-1628-651-700, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd. Room 1709 Quantum Plaza, No.27 ZhichunLu, Haidian District, Beijing, 100191 P. R. China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.

Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, 200333 P. R. China Tel: +86-21-2226-0888, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited Unit 1601-1611, 16/F., Tower 2, Grand Cent Tel: +852-2265-6688, Fax: +852 2886-9022 ntury Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong

Renesas Electronics Taiwan Co., Ltd.

13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malavsia Sdn.Bhd. Unit 1207, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jin Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HAL 2nd Stage, Indiranagar, Bangalore 560 038, India Tel: +91-80-67208700, Fax: +91-80-67208777

Renesas Electronics Korea Co., Ltd. 17F, KAMCO Yangjae Tower, 262, Gangnam-daero, Gangnam-gu, Seoul, 06265 Korea Tel: +82-2-558-3737, Fax: +82-2-558-5338