

QE for Lighting & Power V1.2.0

Release Note

Thank you very much for using the QE for Lighting & Power V1.2.0, a QE (Quick and Effective Tool Solution) product of Renesas Solution Toolkit - Development Support Tool for Various Applications.

This release note covers product installation, restrictions, and so on. Please read this document before using the product.

Contents

| | | |
|-------|---|----|
| 1. | About QE for Lighting & Power | 2 |
| 1.1 | Summary | 2 |
| 1.2 | Functions | 2 |
| 1.3 | New Functions | 2 |
| 1.3.1 | Support custom board | 2 |
| 1.3.2 | Specify Smart Configurator location in [Preferences] menu | 3 |
| 1.4 | Supported Environment | 3 |
| 1.5 | Supported Microcontrollers and Boards | 3 |
| 2. | Installation and Uninstallation | 4 |
| 2.1 | Installing This Product | 4 |
| 2.2 | Updating This Product | 4 |
| 2.3 | Uninstalling This Product | 4 |
| 3. | Notes / Restrictions | 5 |
| 3.1 | Usage Considerations | 5 |
| 3.1.1 | Notes on power control evaluation | 5 |
| 3.1.2 | Notes on DALI configuration default value and generated program | 7 |
| 3.1.3 | Notes on opening [Lighting & Power Workflow (QE)] view | 7 |
| 3.1.4 | Notes on integrated Renesas libraries | 7 |
| 3.1.5 | Notes on using IAR compiler | 8 |
| 3.1.6 | Notes on using the Smart Configurator | 9 |
| 3.2 | Functional Restrictions | 10 |
| 3.2.1 | Restrictions on writing the evaluation program on board | 10 |
| | Revision History | 11 |

1. About QE for Lighting & Power

1.1 Summary

QE for Lighting & Power is an assistance tool for smart lighting system solution development, which seamlessly integrates with Renesas' lighting system libraries, solution programs, various compilers, and evaluation boards. Using this tool, you can configure lighting communication setting, and adjust the power control parameters in real-time. Therefore, it makes the development of the smart lighting solution simple, quick, and efficient.

1.2 Functions

The main functions and support scenarios are listed in Table 1-1:

Table 1-1

| Functions | Using Renesas Board | Using Custom Board |
|--|---------------------|--------------------|
| Configure dimming protocol parameters with graphical UI | Support | Support |
| Evaluate power control parameters on the board | Support | Not Support |
| Generate the source code using various compilers | Support | Support |
| Build and program the lighting system solution to the evaluation board | Support | Not Support |

1.3 New Functions

1.3.1 Support custom board

From QE for Lighting & Power V1.2.0, the custom board is newly supported that allows users to perform the following operations.

- ① Configure DALI protocol parameters.
- ② Generate configuration files based on UI configuration.
- ③ Generate initialization files for the DALI library.
- ④ Generate peripheral driver code through a one-click launch of “Smart Configurator”.

If the custom board is equipped with an RL78/G24 MCU, users can open a Smart Configurator project with peripheral settings based on the existing RL78/G24 DC/DC LED Control Evaluation Board.

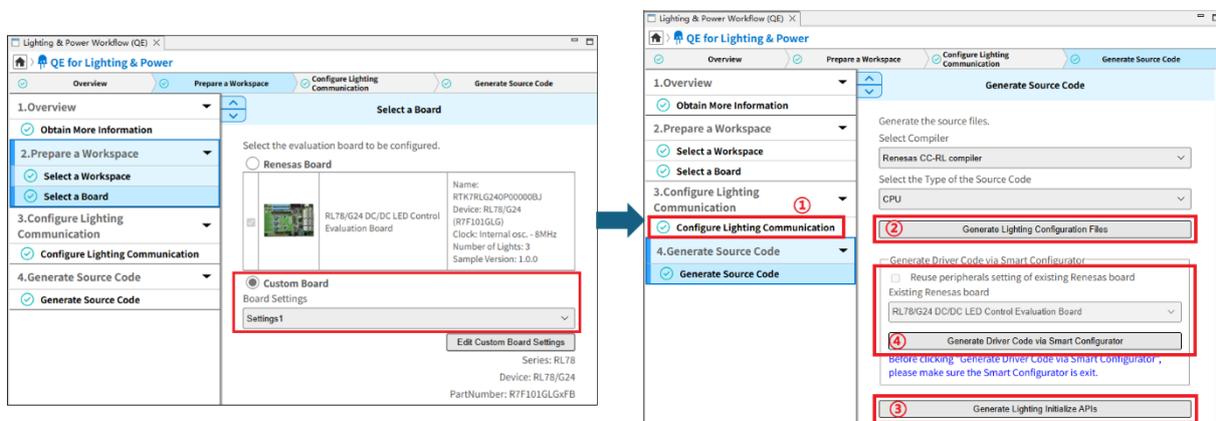


Figure 1-1. Support custom board

1.3.2 Specify Smart Configurator location in [Preferences] menu

The Smart Configurator for RL78 installation location can be specified in “[Windows] > [Preferences] > [QE for Lighting & Power]” menu.

By default, this information is displayed automatically if this tool is installed.

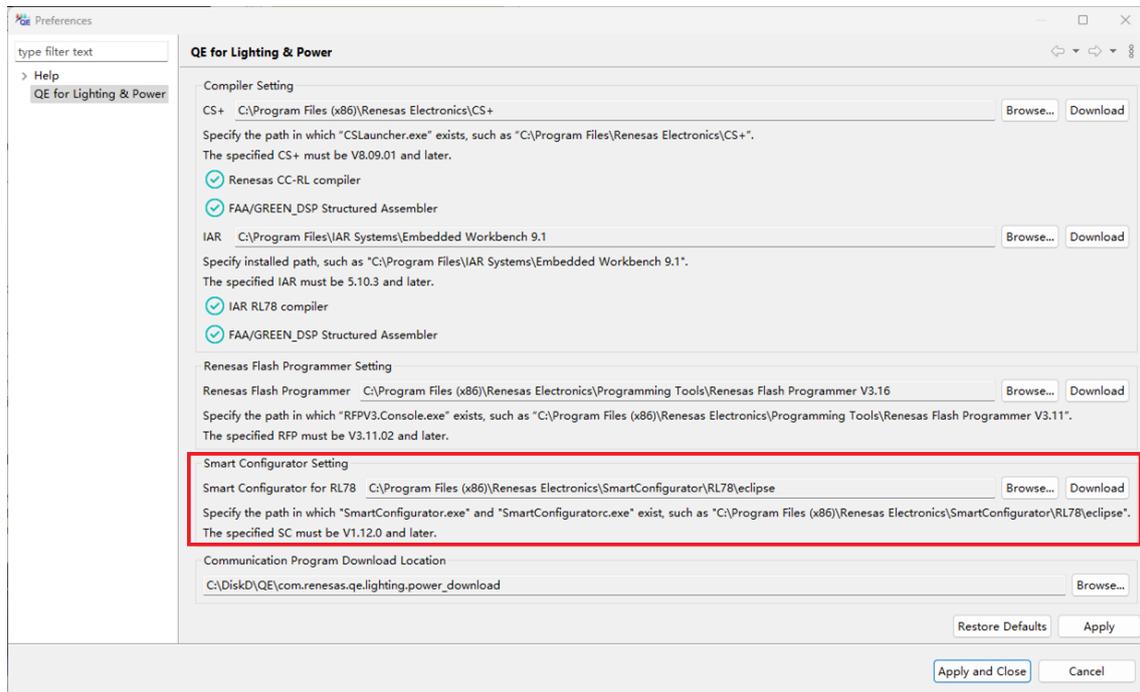


Figure 1-2. To specify Smart Configurator installation location

1.4 Supported Environment

- Windows 10 (64-bit version), Windows 11 (64-bit version)
- CS+ for CC V8.09.01 or later
- IAR Embedded Workbench for Renesas RL78 V5.10.3 or later
- FAA/GREEN_DSP Structured Assembler V1.04.02 or later
- Renesas Flash Programmer V3.11.02 or later
- Smart Configurator for RL78 V1.12.0 or later

1.5 Supported Microcontrollers and Boards

The supported microcontrollers and their corresponding boards are listed in Table 1-2:

Table 1-2

| Microcontrollers | Boards |
|--|---|
| <ul style="list-style-type: none"> • RL78/G24 | RL78/G24 DC/DC LED Control Evaluation Board (P/N: R7F101GLG2DFB) |
| | Custom Board |
| <ul style="list-style-type: none"> • RL78/G22 • RL78/G23 | Custom Board |

2. Installation and Uninstallation

2.1 Installing This Product

QE for Lighting & Power is provided as a compressed package (.zip). Follow the procedure below to use this product.

1. Extract ".zip" file to a user-specified location on the PC.
Note: Please do not put the tool under OS program folder (C:\Program Files)
2. Start this product by double clicking "\QE-Lighting-Power\eclipse\qe-lighting-power.exe".
3. For the first time this product starts, there is a dialog of license agreement. After checking the license, you can select "Agree" or "Disagree".

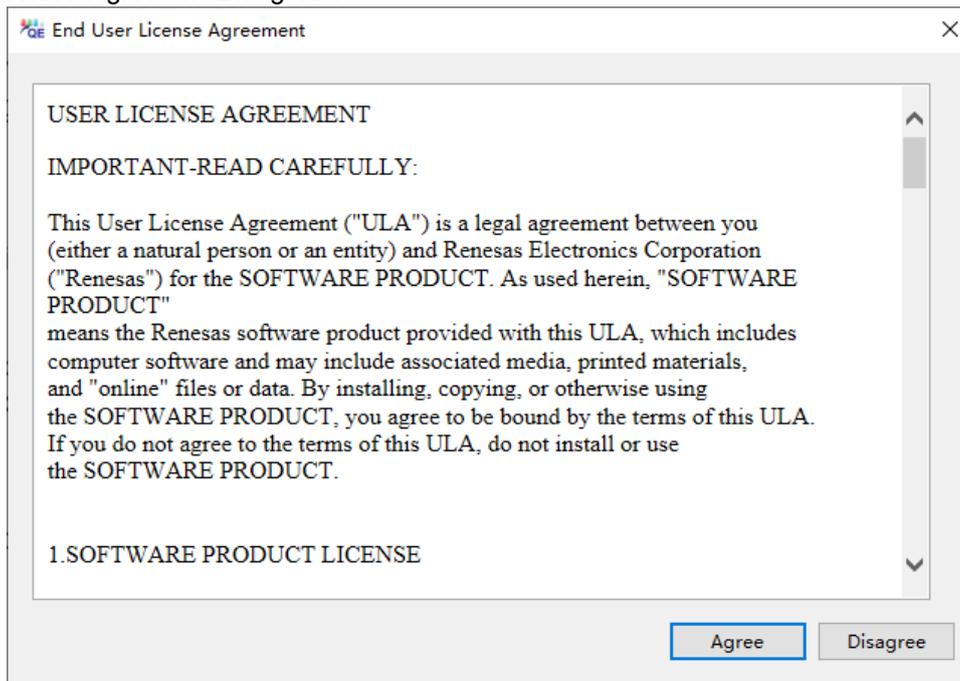


Figure 2-1. License agreement dialog

4. If "Agree" is selected in above step, the product is launched then you can use it; if "Disagree" is selected in above step, the product won't be launched.

2.2 Updating This Product

If you have already used this product, you can update it in the same way as the procedure for installation.

2.3 Uninstalling This Product

Delete the entire folder which .zip package is extracted.

3. Notes / Restrictions

3.1 Usage Considerations

Please pay attention to the following items.

3.1.1 Notes on power control evaluation

- 1) When selecting FAA program type, it is possible to monitor up to 4 power control parameters. Please add no more than 4 parameters in [Variable Tuning] tab. When selecting CPU program type, it is possible to monitor up to 8 power control parameters.

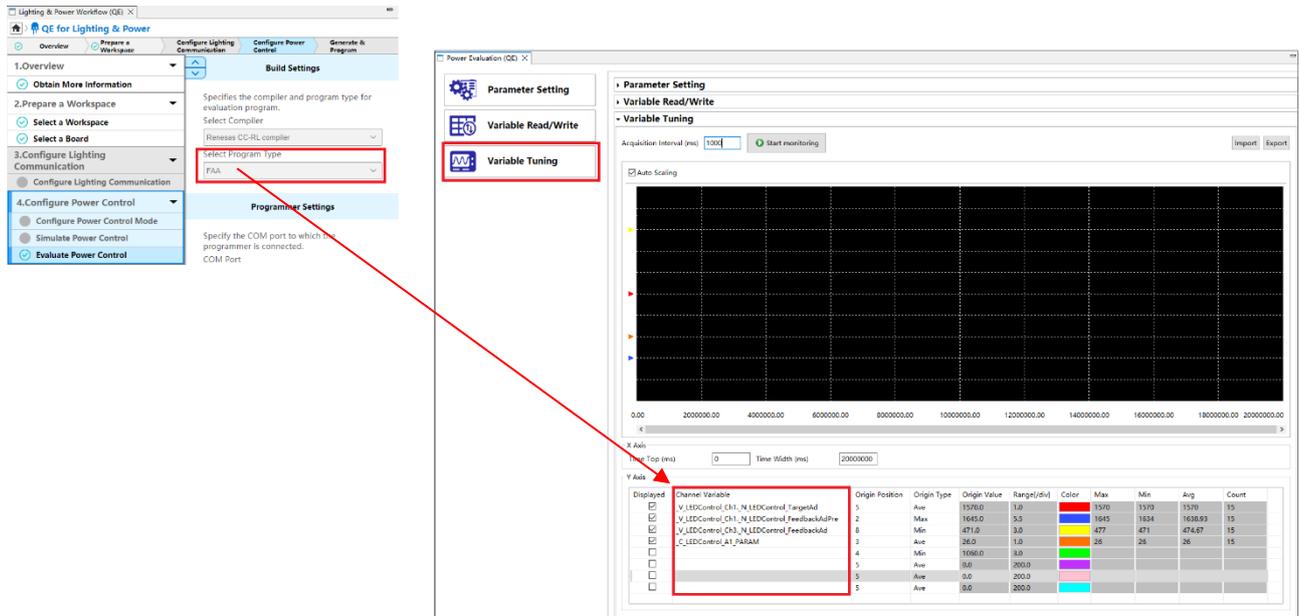


Figure 3-1 (1). Variable tuning when FAA program type is selected

- 2) When the [Acquisition Interval] is set to a smaller value, the sampling rate increases, resulting in a larger amount of tuning data. This large volume of data may affect the monitoring stop operation. To ensure timely stopping after clicking the [Stop monitoring] button, it is recommended to set the [Acquisition Interval] to one of the following values: 1000ms, 500ms, or 200ms.

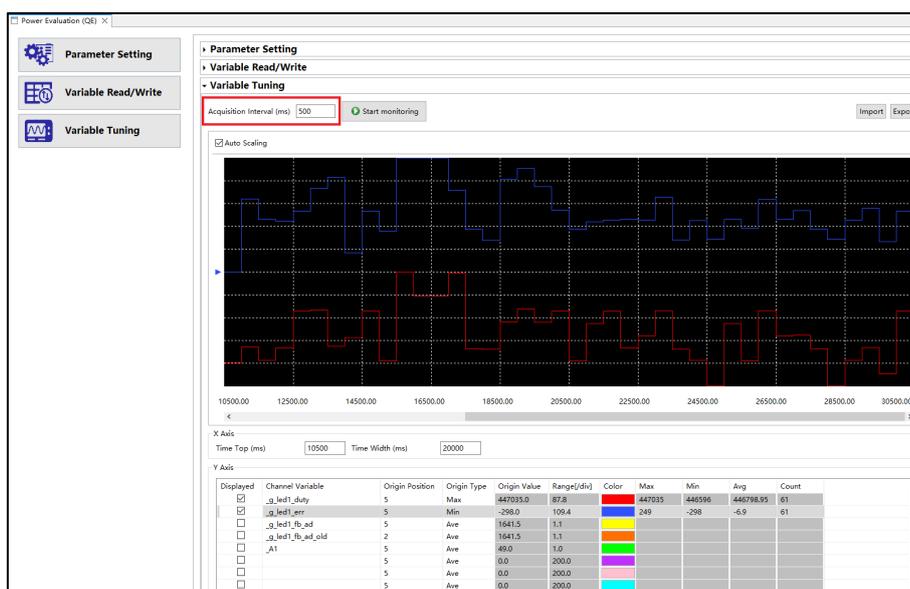


Figure 3-1 (2). [Acquisition Interval] option

If a smaller [Acquisition Interval] is set, the message “[Error]STOP command failed to execute.” is output in console view. In this situation, please click [Stop monitoring] button manually.

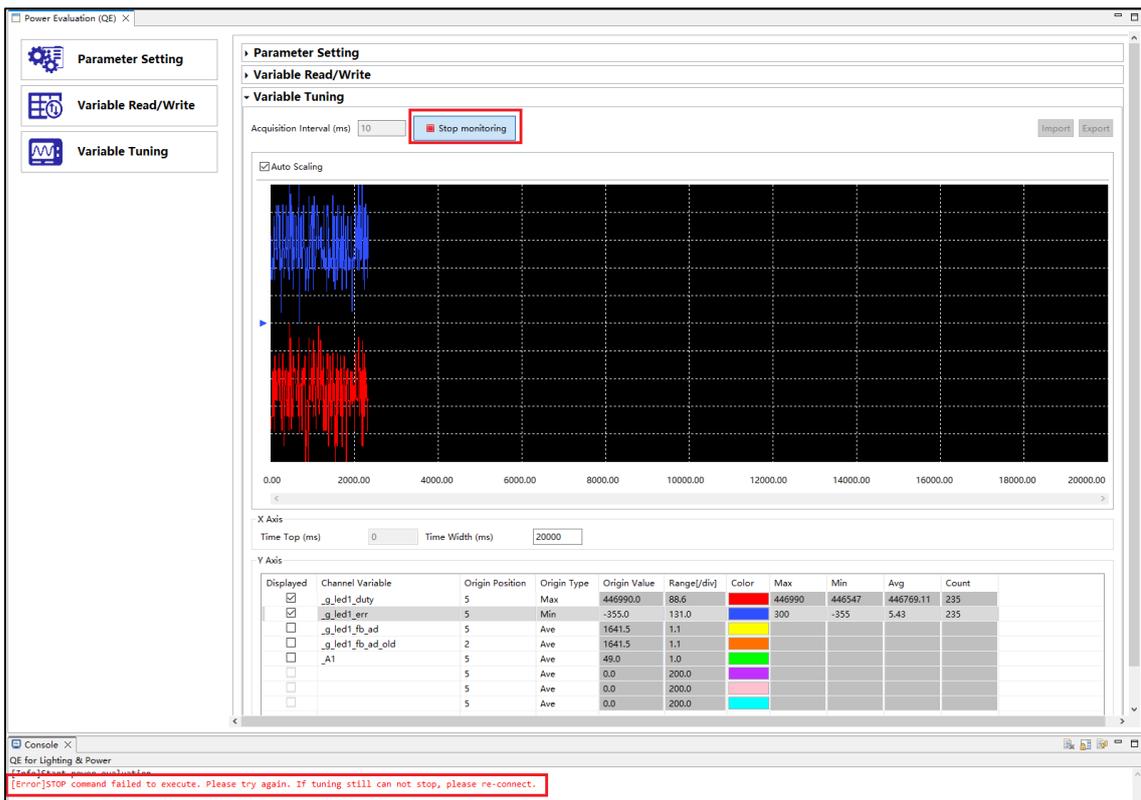


Figure 3-1 (3). Stop monitoring manually.

- To read, write or monitor an SFR, please first select the target SFPs from the [SFR List] table. Then, select the target SFRs in the [Variable Read/Write] - [Variable/SFR Data] table or [Variable Tuning] - [Channel Variable] column before performing read, write or monitor.

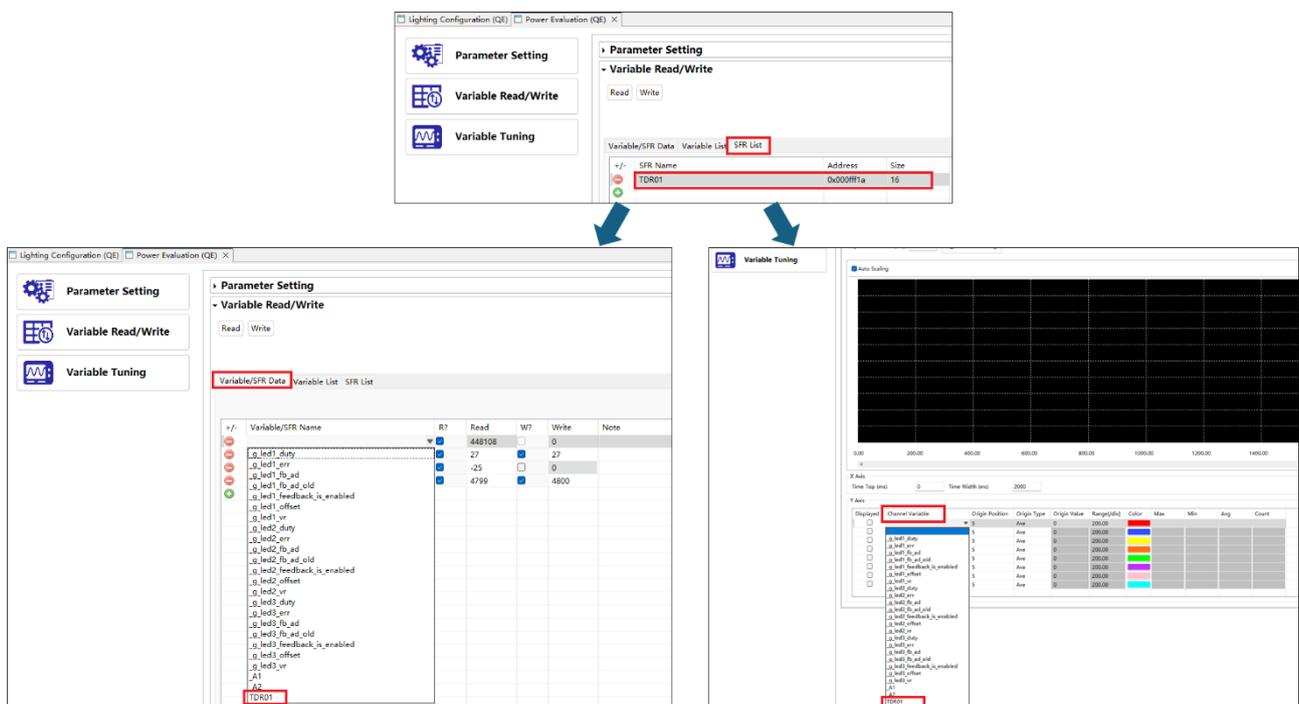


Figure 3-1 (4). SFR read/write/monitor procedures.

3.1.2 Notes on DALI configuration default value and generated program

Both the default value on the DALI configuration UI and the generated program are based on RL78/G24 Sample Application (R01AN7043EJ0100). When modifying DALI configuration, please consider the change matches the DALI standard (IEC 62386), modify the user-defined part in the generated code according to the user's purpose if necessary, and evaluate it thoroughly.

3.1.3 Notes on opening [Lighting & Power Workflow (QE)] view

The workflow cannot be opened if WebView2 Runtime is not installed on your PC.

[Workaround]

Download and install WebView2 (x64 version) from the Microsoft web page. (FAQ: [3000670](#))

3.1.4 Notes on integrated Renesas libraries

The following Renesas libraries are integrated into the QE for Lighting & Power tool.

- RL78 Family DALI-2 Control Gear Library User's Manual: Basic (R01US0535EJ0102)
- RL78 Family DALI-2 Control Gear Library User's Manual: LED (R01US0536EJ0102)
- RL78 Family DALI-2 Control Gear Library User's Manual: Colour Control (R01US0537EJ0102)
- Renesas Flash Driver RL78 Type 01 Package V1.20 for RL78/G2x

3.1.5 Notes on using IAR compiler

When using IAR RL78 compiler, please ensure that the license status is valid in IAR before proceeding with the following operations.

- 1) Click on [Write the Evaluation Program on Board] button in the workflow.
- 2) Click on [Build Source Code] button in the workflow.

These operations will trigger the IAR build process. So if the IAR license is invalid, the operation will cause the QE for Lighting & Power tool getting stuck.

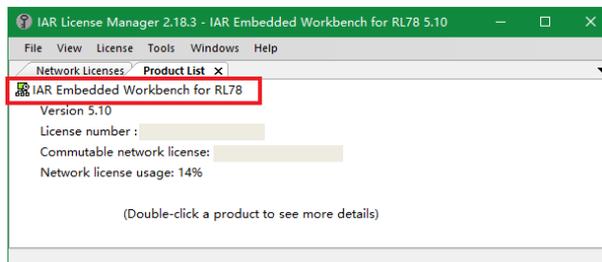


Figure 3-2 (1). IAR license manager dialog

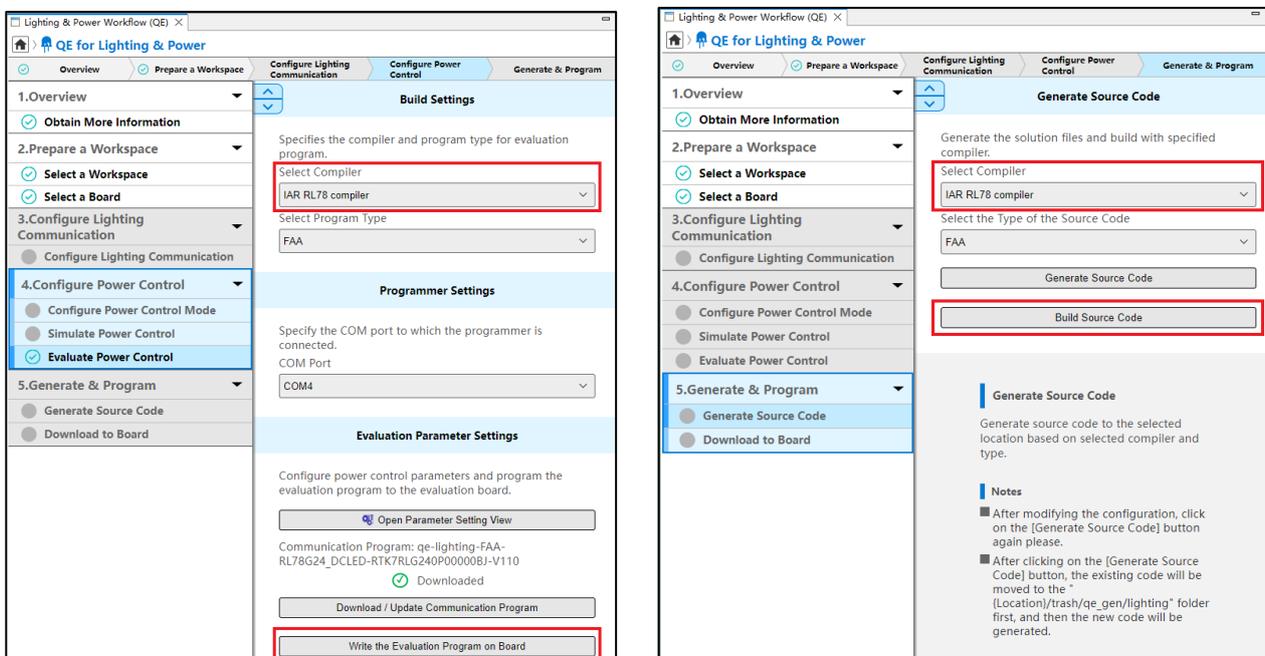


Figure 3-2 (2). 2 cases of IAR RL78 compiler selection

3.1.6 Notes on using the Smart Configurator

When using custom board, before clicking [Generate Driver Code via Smart Configurator] button in the workflow, please ensure the Smart Configurator tool path is correctly set via “[Windows] > [Preferences] > [QE for Lighting & Power]” menu.

If the specified installation path is invalid, this operation will cause Smart Configurator to fail to launch or the Smart Configurator project not being imported correctly.

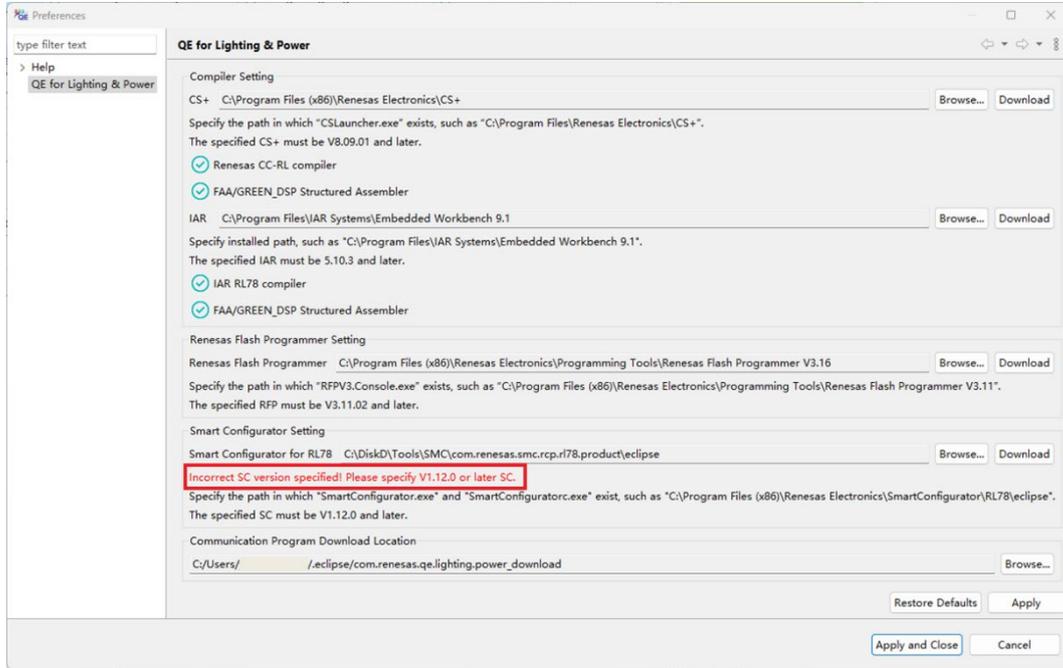


Figure 3-3. Error message when Smart Configurator tool path in Preferences is invalid

3.2 Functional Restrictions

There is 1 restriction in QE for Lighting & Power V1.2.0. This restriction will be solved in the next or future version.

3.2.1 Restrictions on writing the evaluation program on board

Please open the [Power Evaluation (QE)] page before clicking on [Write the Evaluation Program on Board] button in [Lighting & Power Workflow (QE)] view.

If the [Power Evaluation (QE)] page is closed, it will result in an empty address in “[Power Evaluation (QE)] > [Variable Read/Write] > [Variable List]”. Consequently, the variable read, write, and tuning function will not work.

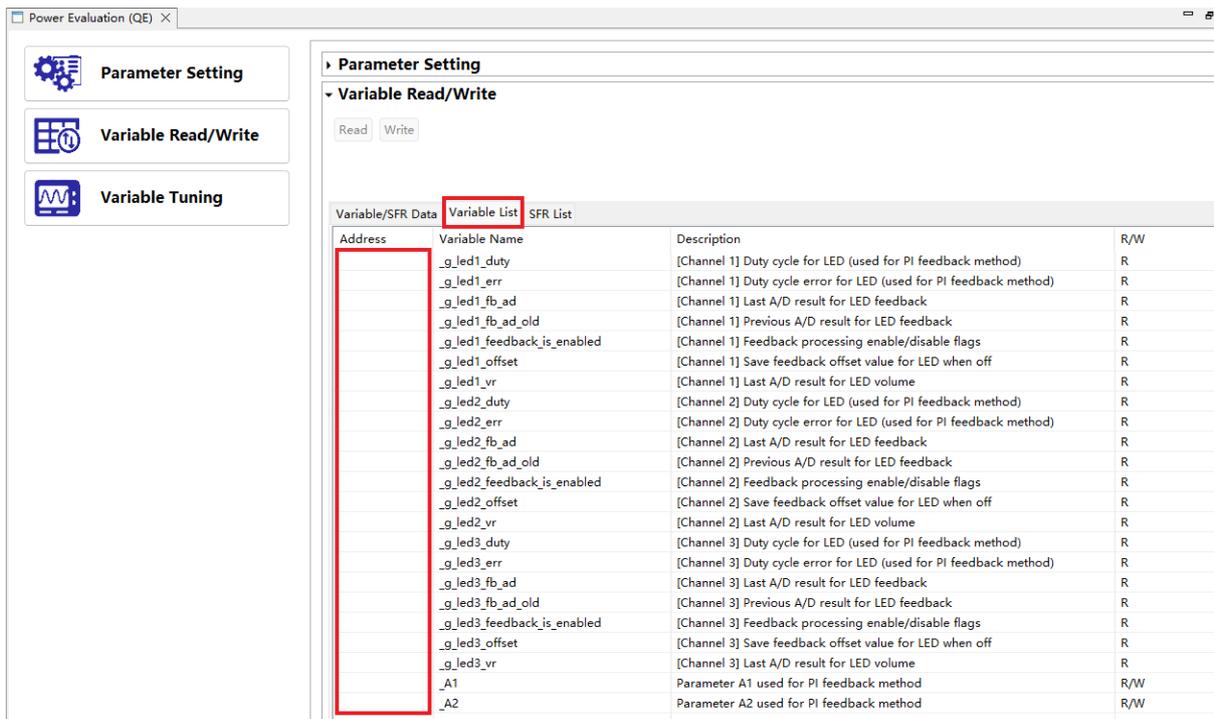


Figure 3-4. Phenomenon of closing [Power Evaluation (QE)] view when writing

Revision History

| Rev. | Date | Description | |
|------|-----------|-------------|-----------------------|
| | | Page | Summary |
| 1.00 | Jun.30.25 | - | First edition issued. |

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity.

Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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