

# H8SX E100 Emulator Software

## V.1.01 Release 00

REJ10J1871-0300

Rev.3.00

Aug 16, 2010

### Release Notes

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Thank you for purchasing our product. There are several precautions which need to be taken while using this product. Please also read the precautions on the High-performance Embedded Workshop described in the High-performance Embedded Workshop Release Notes.

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## 1. MCU Unit and Target MCU

Table 1.1 shows the names of the MCU unit and target MCU for the H8SX E100 emulator software.

Table 1.1 MCU Unit and Target MCU

MCU Unit	Target MCU	
	Group	MCU* <sup>1</sup>
R0E417250MCU00	H8SX/1720	R5F61725

\*1: This is the MCU selectable from the [Device] drop-down list box in the [Device setting] dialog box.

## 2. Descriptions of Revision

### 2.1 Problem Fixed

The following known problem has been fixed:

If this product is used with High-performance Embedded Workshop V.4.06.00 or later on Windows Vista®, it cannot communicate with the E100 emulator.

For details of the problem, see Renesas Tool News Document No. 090916/tn3 at:

<http://tool-support.renesas.com/eng/toolnews/090916/tn3.htm>

### 2.2 High-performance Embedded Workshop Updated

The High-performance Embedded Workshop included in the package have been updated from V.4.05.01 to V.4.07.01.

### 3. Note

#### 3.1 Installing the H8SX E100 Emulator software

H8SX E100 Emulator Software V.1.01 Release 00 contains High-performance Embedded Workshop V.4.07.01.

When H8SX E100 Emulator Software V.1.01 Release 00 is installed on the host computer where V.4.07.00 or earlier versions has been installed, the High-performance Embedded Workshop will be updated to V.4.07.01.

### 4. Restriction

#### 4.1 Action to be Taken When an Error Message is Displayed

When error message "The command is not executable because the FCU operating clock is out of operating range." is displayed, the flash ROM or EEPROM cannot be accessed from the emulator debugger.

Select (1) or (2) given below.

- (1) Execute the MCU reset command on the emulator debugger.
- (2) Set the MCU registers through the emulator debugger so that the frequency of  $F\Phi$  will not exceed the guaranteed range.

### 5. Real-Time OS Aware Debugging

For details of the real-time OS aware debugging, refer to the following page.

<http://www.renesas.com/ecxos>

## 6. Operating Environment

Table 5.1 Operating Environment (Windows® XP or Windows® 2000)

PC Environment	
PC	IBM PC/AT compatible
OS	Windows® XP 32-bit editions* <sup>2</sup> Windows® 2000
CPU	Pentium 4 running at 1.6 GHz or more recommended
Interface	USB2.0
Memory	768 Mbytes or larger (more than 10 times the file size of the load module) recommended
Hard disk	Installation of the emulator debugger requires free space of 200 Mbytes or larger. Also keep additional free space that is at least twice the memory capacity (four times or larger recommended) for use as swap space.
Display resolution	1024 × 768 or higher recommended

\*2: The 64-bit editions of Windows® XP are not supported.

Table 5.2 Operating Environment (Windows Vista®)

PC Environment	
PC	IBM PC/AT compatible
OS	Windows Vista® 32-bit editions* <sup>3</sup>
CPU	Pentium 4 running at 3 GHz or Core 2 Duo running at 1 GHz or more recommended
Interface	USB2.0
Memory	1.5 Gbyte or larger (more than 10 times the file size of the load module) recommended
Hard disk	Installation of the emulator debugger requires free space of 200 Mbytes or larger. Also keep additional free space that is at least twice the memory capacity (four times or larger recommended) for use as swap space.
Display resolution	1024 × 768 or higher recommended

\*3: The 64-bit editions of Windows Vista® are not supported.

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