

QE for Display[RX] V1.1.0

Release Note

Thank you very much for your interest in QE for Display[RX] V1.1.0.

This document describes this product installation, restrictions and so on. Please read this document before using the product.

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1. About QE for Display[RX]

1.1 Summary

QE for Display[RX] is one of several Solution Toolkit which operate under the e² studio integrated development environment. This product allows for easy initial adjustment of display connection (display timing adjustment and image quality adjustment), by using the image display function of the Graphic LCD Controller (a display controller mounted on the RX family of MCUs). This shortens the development period.

For details on Solution Toolkit and QE (Quick and Effective tool solution), refer to the URL below.

<https://www.renesas.com/qe>

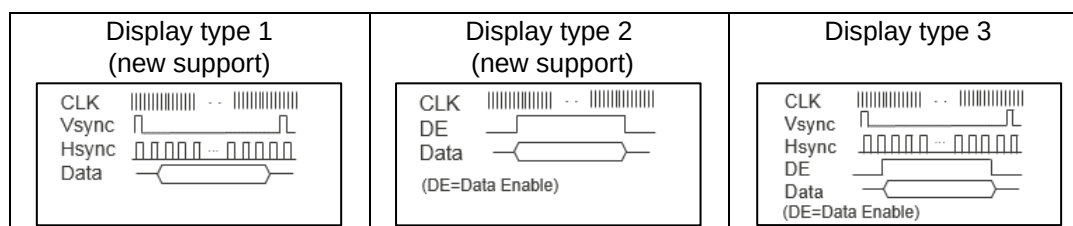
1.2 New Functions

➤ Supports RX66N, RX72M and RX72N groups

RX66N, RX72M and RX72N groups are now supported. See section 1.5, Supported Sample Projects for supported sample programs of each microcontroller.

➤ Supports all Display types

In QE for Display[RX], select the display type that matches the LCD panel specifications and make adjustment. Only one display type is supported in QE for Display[RX] V1.0.0, however, QE for Display[RX] V1.1.0 supports two more new display types.



1.3 Supported Environment

Windows 8.1, Windows 10

Renesas e² studio V7.7.0 (or later)

1.4 Supported Microcontroller

RX family

- RX600 Series RX65N groups*, RX651 groups*, RX66N groups
(* ROM capacity: 1.5 MB to 2 MB only)
- RX700 Series RX72M groups, RX72N groups

1.5 Supported Sample Program

QE for Display[RX] works with the following sample program.

- RX Family QE for Display [RX] Sample Program (No. R20AN0487EJ)
<https://www.renesas.com/search/keyword-search.html#genre=sampleprogram&q=r20an0487>

2. Installation and Uninstallation

QE for Display[RX] is included in RenesasQE_display_package_V130.zip. This zip file is QE for Display package which is an integrated package of RX version and RZ version.

2.1 Installing This Product

Use the following procedure to install this product.

1. Start e² studio.
2. From the [Help] menu, select [Install New Software...] to open the [Install] dialog box.
3. Click the [Add...] button to open the [Add Repository] dialog box.
4. Click the [Archive] button, select "RenesasQE_display_package_V130.zip" in the opened dialog box, and click the [Open] button.
5. Click the [OK] button in the [Add Repository] dialog box.
6. Expand the [Renesas QE] item shown in the [Install] dialog box, select the [Renesas QE for Display[RX]] check box, and then click the [Next>] button.
* If you check off the [Contact all update sites during install to find required software] checkbox, you can shorten the installation time.
7. Check that [Renesas QE for Display[RX]] is selected as the target of installation, and click the [Next] button.
8. After confirming the license agreements, select the [I accept the terms of the license agreements] radio button, and click the [Finish] button.
9. If the dialog of the trust certificate is displayed, check that certificate, and click the [OK] button to continue installation.
10. When prompted to restart e² studio, restart it.
11. Start this product from the [Renesas Views] - [Renesas QE] menu of e² studio. For details about how to use this product, see the [Help] menu of e² studio.

2.2 Updating This Product

If you have already installed this product, you can update it in the same way as the procedure for installation.

2.3 Uninstalling This Product

Use the following procedure to uninstall this product.

1. Start e² studio.
2. Select [Help -> About e² studio] to open the [About e² studio] dialog box.
3. Click the [Installation Details] button to open the [e² studio Installation Details] dialog box.
4. Select [Renesas QE for Display[RX]] displayed on the [Installed Software] tabbed page and click the [Uninstall...] button to open the [Uninstall] dialog box.
5. Check the displayed information and click the [Finish] button.
6. When prompted to restart e² studio, restart it.

3. Notes / Restrictions

3.1 Usage Considerations

There are no usage considerations for this product.

3.2 Functional Restrictions

There are no functional restrictions for this product.

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Apr.07.20	-	First edition issued.

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity.

Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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