

**RZ/A2M Evaluation Board Kit**

R20UT4400EJ0100

Rev.1.00

Sep 28, 2018

## Release Note

**Introduction**

This release note explains the limitations of the MCU specification mounted the RZ/A2M Evaluation Board Kit.

**1. Limitation**

<b>Affected Product</b>	Products affected are identified with the CPU board serial number 00000205783 to 00000205882.  (All products are affectable as of September 28, 2018)
<b>Limitation No.1</b>	In the Clock Pulse Generator, SSCG (Spread Spectrum Clock Generator) function is not enabled. As the frequency modulation of the output clock does not work, the effect of reducing the radiation noise becomes ineffective.
<b>Limitation No.2</b>	In the Interrupt Controller, ERI interrupt (receive error) and BRI interrupt (overrun) of serial communication interface with FIFO (SCIFA) will not occur.  If you want to detect receive error and overrun error, execute software such as polling the receive error flag of SCIFA serial status register and overrun error flag of line status register by software.

**2. Time to ship the product with a repaired MCU**

The repaired MCUs will be mounted in products in all future production lots. We will inform you of the first shipping date of products in the next production lot by RZ/A2M Evaluation Board Kit product page or Renesas Tool News as soon as fixed. Please note that we cannot accept any support request for replacement to a product in the next production lot, and modification for replacement to a repaired MCU.

**Website and Support**

Renesas Electronics Website

<http://www.renesas.com/>

RZ/A2M Evaluation Board Kit product page

<https://www.renesas.com/rza2mevb>

Inquiries

<http://www.renesas.com/contact/>

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