

# Smart Configurator for RZ V1.2.0

## Release Note

R20UT4190EJ0100

Rev.1.00

Sep 25, 2017

Thank you for using the Smart Configurator for RZ.

This document describes the restrictions and points for caution. Read this document before using the product.

### Contents

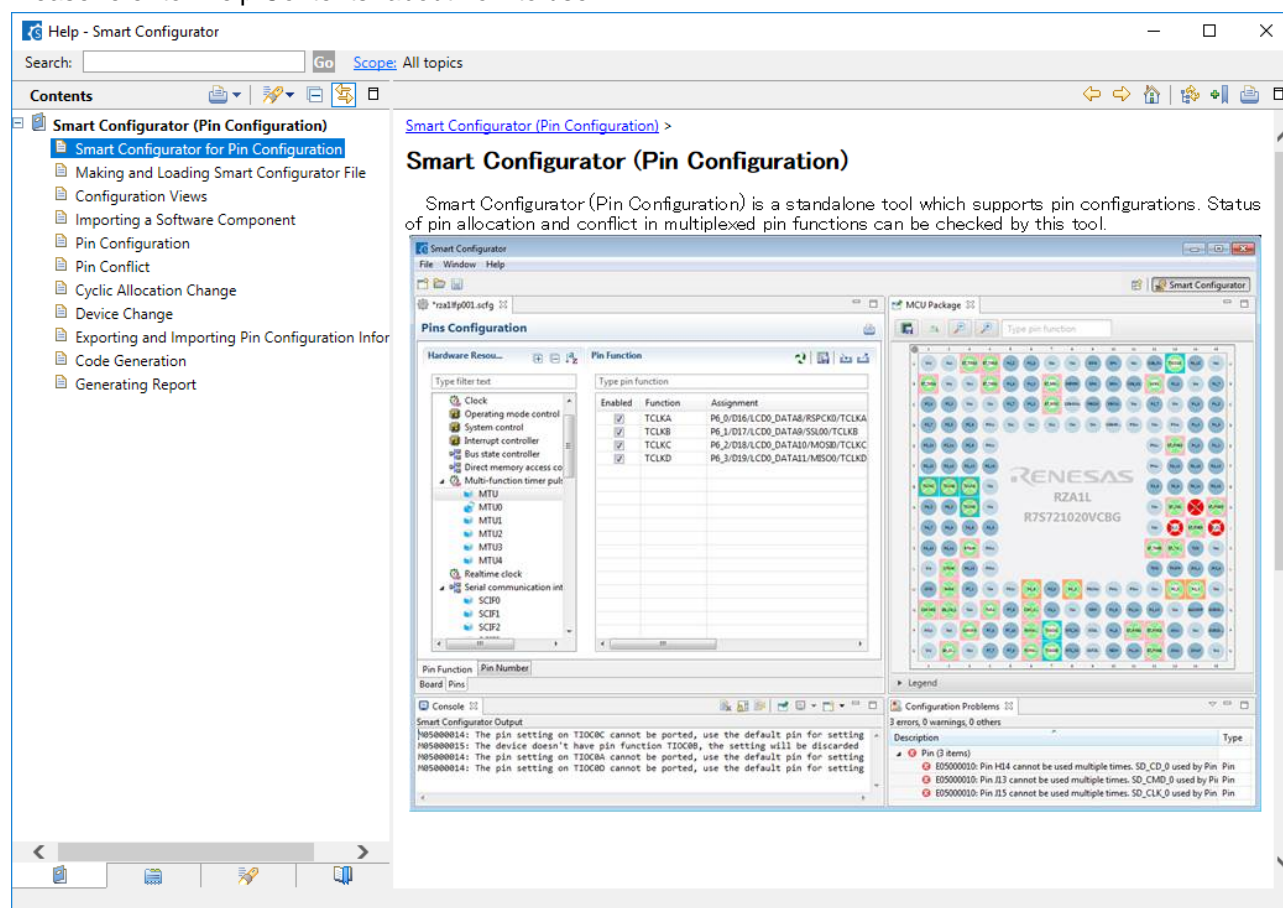
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## Chapter 1. Introduction

The Smart Configurator for RZ is a standalone GUI-based tool for setting the assignments of pin functions to pins in the design of hardware specifications. "Smart" refers to it also checking the pin configurations and states of the settings for the pin functions of the MCU, and being able to resolve these where this is readily done. The allocation of pin functions of peripheral modules to pins can be set up in tables, and figures indicate cases of contention between multiplexed functions and produce tables indicating the problems and solutions where possible.

This tool also can be used for checking for modifications to software which are required due to changes to packages or in the selection of packages.

Please refer to "Help Contents" about how to use.



## Chapter 2. Target Devices

Below is a list of devices supported by the Smart Configurator for RZ V1.2.0.

RZ/A1L group	
PIN	Device name
176pin	R7S721020VCBG, R7S721020VCFP, R7S721020VLFP
208pin	R7S721021VCFP, R7S721021VLFP
RZ/A1LU group	
PIN	Device name
176pin	R7S721030VCBG, R7S721030VCFP, R7S721030VLFP
208pin	R7S721031VCFP, R7S721031VLFP
RZ/A1LC group	
PIN	Device name
176pin	R7S721034VCBG
Following documents.	
Manual Name	Document Number
RZ/A1L, RZ/A1LU, RZ/A1LC Group User's Manual: Hardware	R01UH0437EJ0300

RZ/A1H group	
PIN	Device name
256pin	R7S721000VCBG, R7S721000VCFP, R7S721000VLFP
324pin	R7S721001VCBG, R7S721001VLBG
RZ/A1M group	
PIN	Device name
256pin	R7S721010VCBG, R7S721010VCFP, R7S721010VLFP
324pin	R7S721011VCBG, R7S721011VLBG
Following documents.	
Manual Name	Document Number
RZ/A1H, RZ/A1M Group User's Manual: Hardware	R01UH0403EJ0200

RZ/G1M group	
PIN	Device name
831pin	R8A77430HA01BG
Following documents	
Manual name	Document Number
RZ/G1M User's Manual: Hardware	R01UH0626EJ0100

## Chapter 3. Operating Environment

### ▪ Host machine

- IBM PC/AT compatibles (Windows® 10, Windows® 8.1, Windows® 7)
- Processor: 1 GHz or higher (must support hyper-threading, multi-core CPUs)
- Memory capacity: 2 GB or more recommended. Minimum requirement is 1 GB or more (64-bit Windows requires 2 GB or more)
- Hard disk capacity: 200 MB or more spare capacity
- Display: 1024 x 768 or higher resolution, 65,536 or more colors
- All other necessary software environments in addition to Windows OS
  - .NET Framework version4.5
  - Microsoft Visual C++ 2010 SP1 runtime library

## Chapter 4. Point for Caution

This chapter describes the cautions for Smart Configurator for RZ V1.2.0.

No.	Descriptions																																																																																								
1	<ul style="list-style-type: none"><li>For the channel 1 of 'High Speed Serial Communication Interface with FIFO' (<b>HSCIF1</b>), Group E pin names (HCTS1#_E、HRTS1#_E、HRX1_E、HSC1_E、HTX1_E) can't be selected by Smart configurator.</li><li>For the channel 2 of 'High Speed Serial Communication Interface with FIFO' (<b>HSCIF2</b>), Group D pin names (HTX2_D、HRX2_D) can't be selected by Smart configurator.</li><li>For the channel 1 of 'Clock-Synchronized Serial Interface with FIFO' (<b>MSIOF1</b>), Group D pin names (MSIOF1_RXD_D、SIOFO1_SDA_D、MSIOF1_SS1_D、MSIOF1_SYNC_D、MSIOF1_TXD_D) can't be selected by Smart configurator.</li></ul> <p>e.g.) In case of HRX1</p> <p>Pin number AC29 can't be assigned pin name HRX1_E.</p> <table><tr><th colspan="7">Function</th><th rowspan="2">GPIO</th><th rowspan="2"></th></tr><tr><th>1</th><th>2</th><th>3</th><th>4</th><th>5</th><th>6</th><th>7</th></tr><tr><td>No.</td><td>Module</td><td></td><td></td><td></td><td></td><td></td><td></td><td>During POR</td></tr><tr><td>Pin No.</td><td>Pin Name</td><td></td><td></td><td></td><td></td><td></td><td></td><td>V<sub>I/OH</sub></td></tr><tr><td>Mode Pin</td><td>I/O</td><td></td><td></td><td></td><td></td><td></td><td></td><td>Pull-up</td></tr><tr><td>390</td><td>INTC</td><td>HSCIF1</td><td>I2C4</td><td>MSIOF2</td><td>HSCIF1</td><td>Reserved</td><td>-</td><td>I(GPIO)</td></tr><tr><td>AC29</td><td>IRQ4</td><td>HRX1 C</td><td>I2C4 SDA C</td><td>MSIOF2 RXD E</td><td>HRX1 E</td><td>-</td><td>-</td><td>GP7 14</td></tr><tr><td></td><td></td><td></td><td>IO</td><td></td><td></td><td>-</td><td>-</td><td>3.3V/4mA</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>IO(I)</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>On</td></tr></table> <p>【Applicable Devices】</p> <p>RZ/G1M</p>	Function							GPIO		1	2	3	4	5	6	7	No.	Module							During POR	Pin No.	Pin Name							V <sub>I/OH</sub>	Mode Pin	I/O							Pull-up	390	INTC	HSCIF1	I2C4	MSIOF2	HSCIF1	Reserved	-	I(GPIO)	AC29	IRQ4	HRX1 C	I2C4 SDA C	MSIOF2 RXD E	HRX1 E	-	-	GP7 14				IO			-	-	3.3V/4mA									IO(I)									On
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2	<ul style="list-style-type: none"><li>Smart Configurator for RZ has no functions of code generation.</li></ul> <p>【Applicable Devices】</p> <p>RZ/A1H, A1L, A1LC, A1LU, A1M</p> <p>RZ/G1M</p>																																																																																								

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