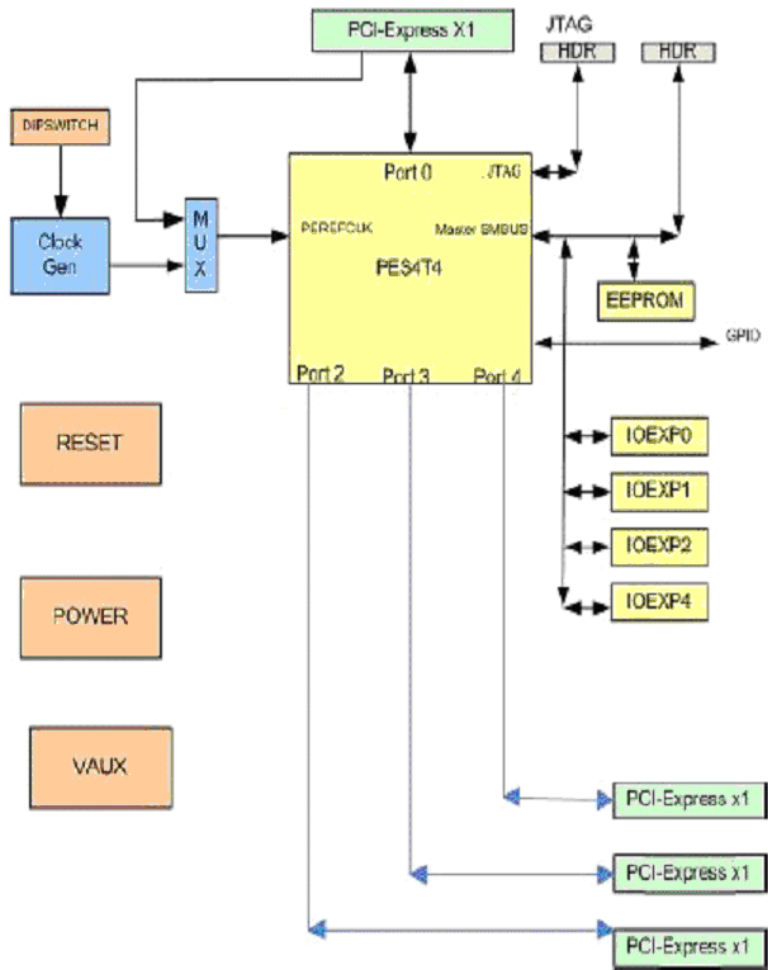



REVISIONS				
DCN	REV	DESCRIPTION	DATE	CHANGE BY
STGC-0127R03	1.0	INITIAL RELEASE	2007-09-05	J. CARRILLO



SHEET	DESCRIPTION
1	TABLE OF CONTENTS
2	POWER REGULATORS PES4T4
3	POWER MOSFETS FOR 3.3VAUX
4	CLOCKS
5	RESET ,SMBUS ,JTAG ,DIPSW
6	APWRDISN TIMING CIRCUIT
7	EPROM ATTN_SW WAKE
8	IO EXPANDERS
9	IO EXPANDER LEDS
10	HOT SWAP CONTROL PORT 2/4
11	HOT SWAP CONTROL PORT 3
12	PES4T4 - CLOCK ,SMBUS ,GPIO
13	PES4T4 - PORT 0
14	PES4T4 - DOWNSTREAM PORTS
15	DOWNSTREAM PORT 2/3 CONNECTORS
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TITLE 89EBPES4T4 Evaluation Board

89EBPES4T4 Evaluation Board

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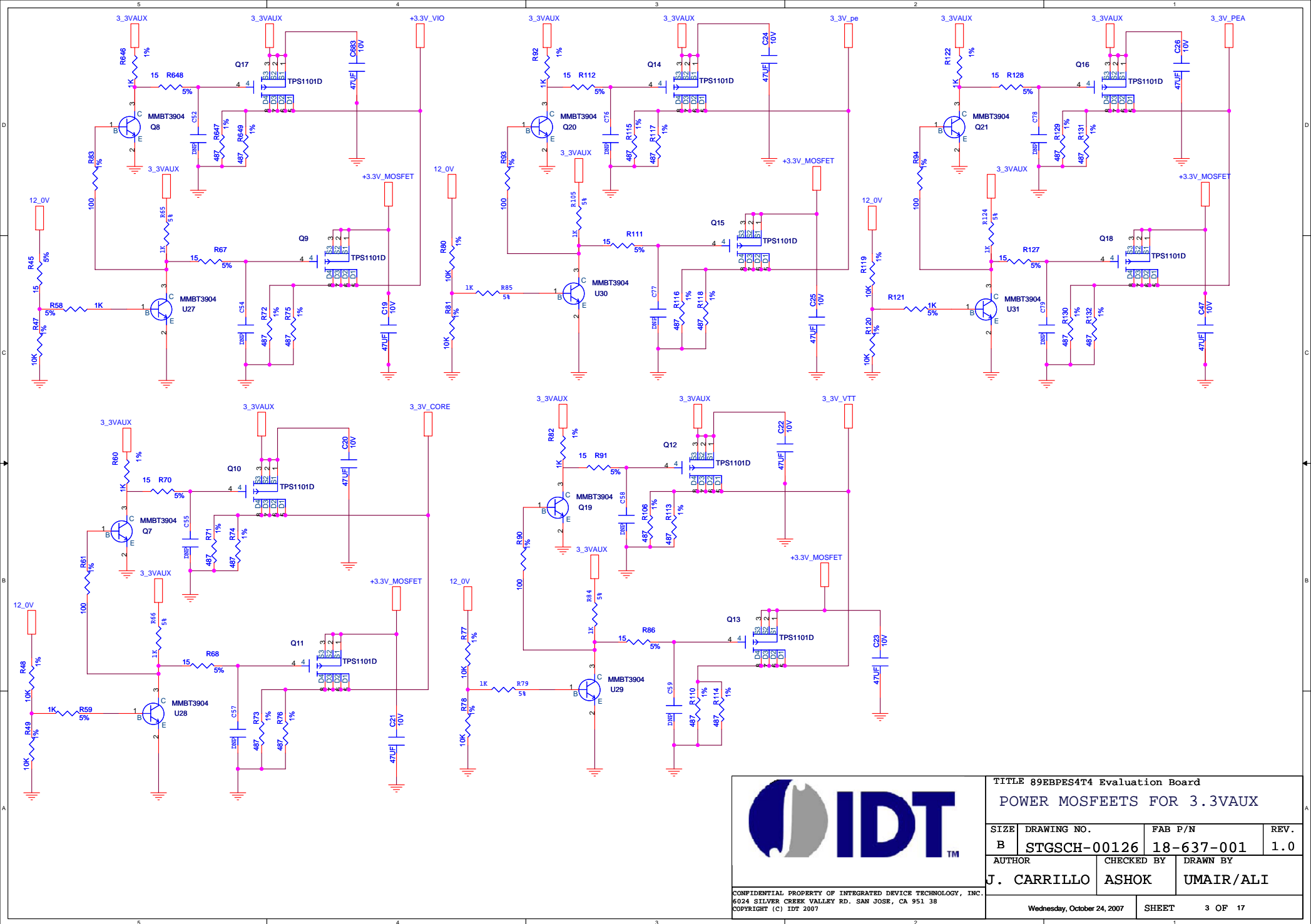
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POWER MOSFEETS FOR 3.3VAUX

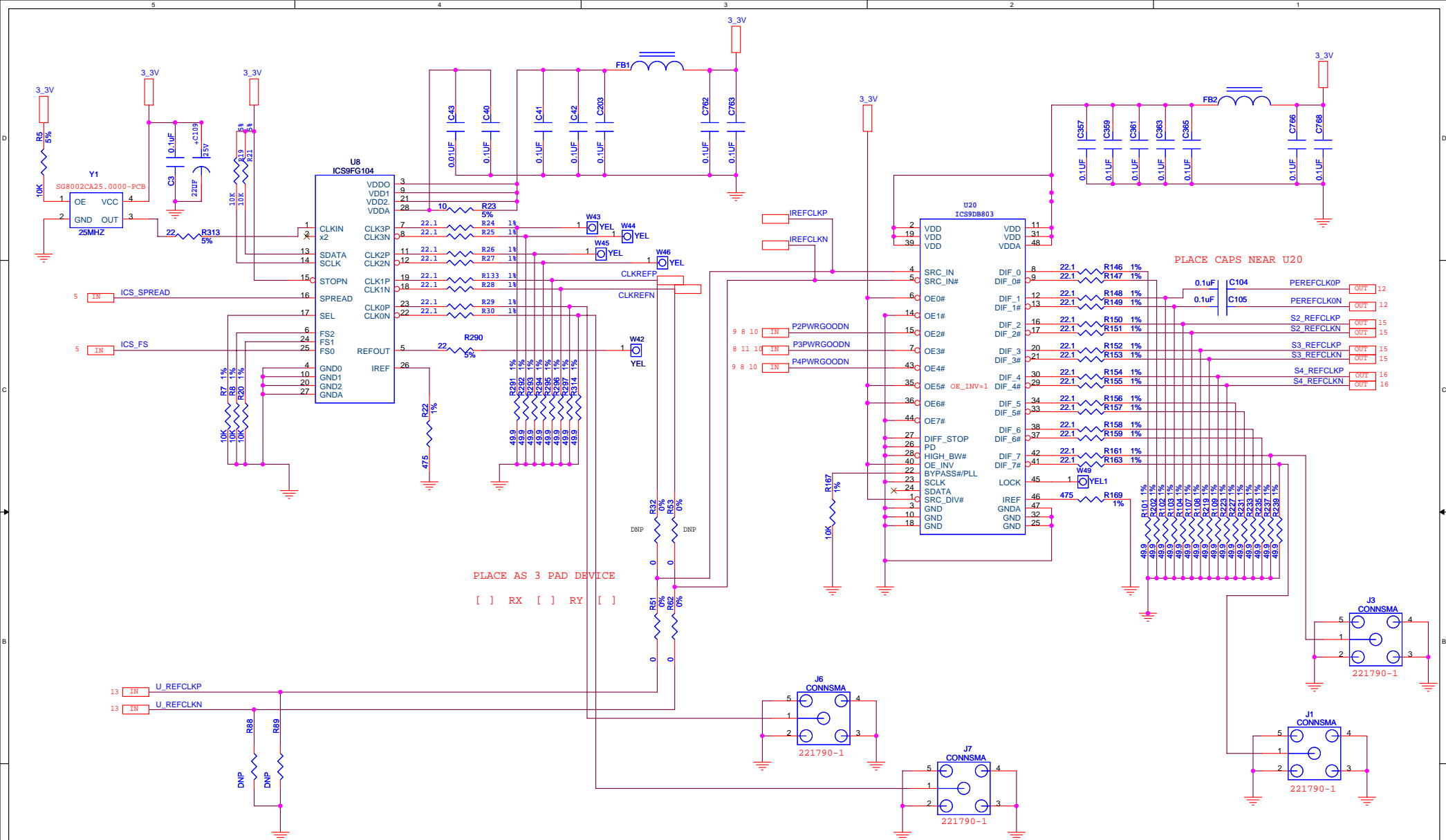
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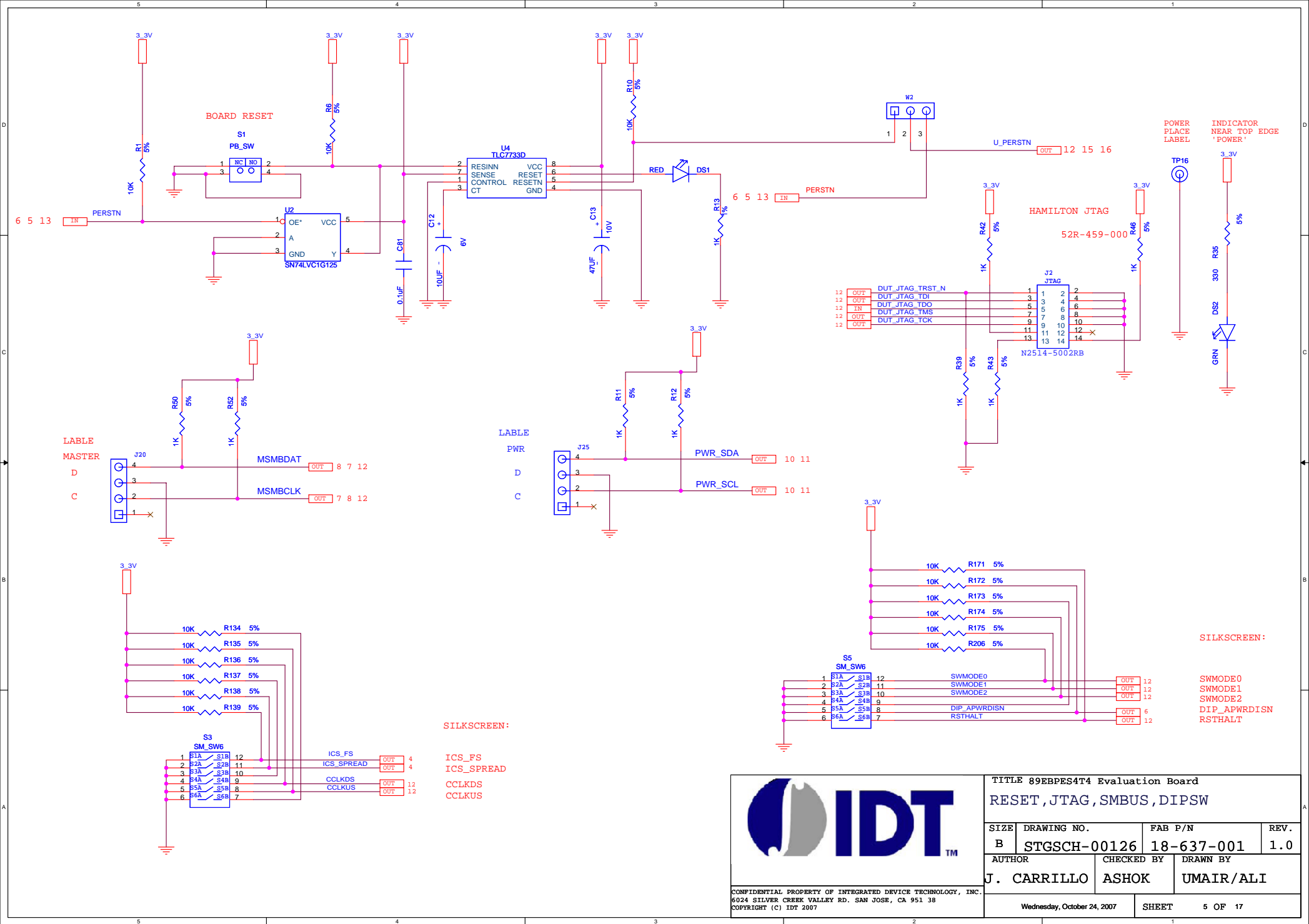
TITLE 89BPES4T4 Evaluation Board
CLOCKS


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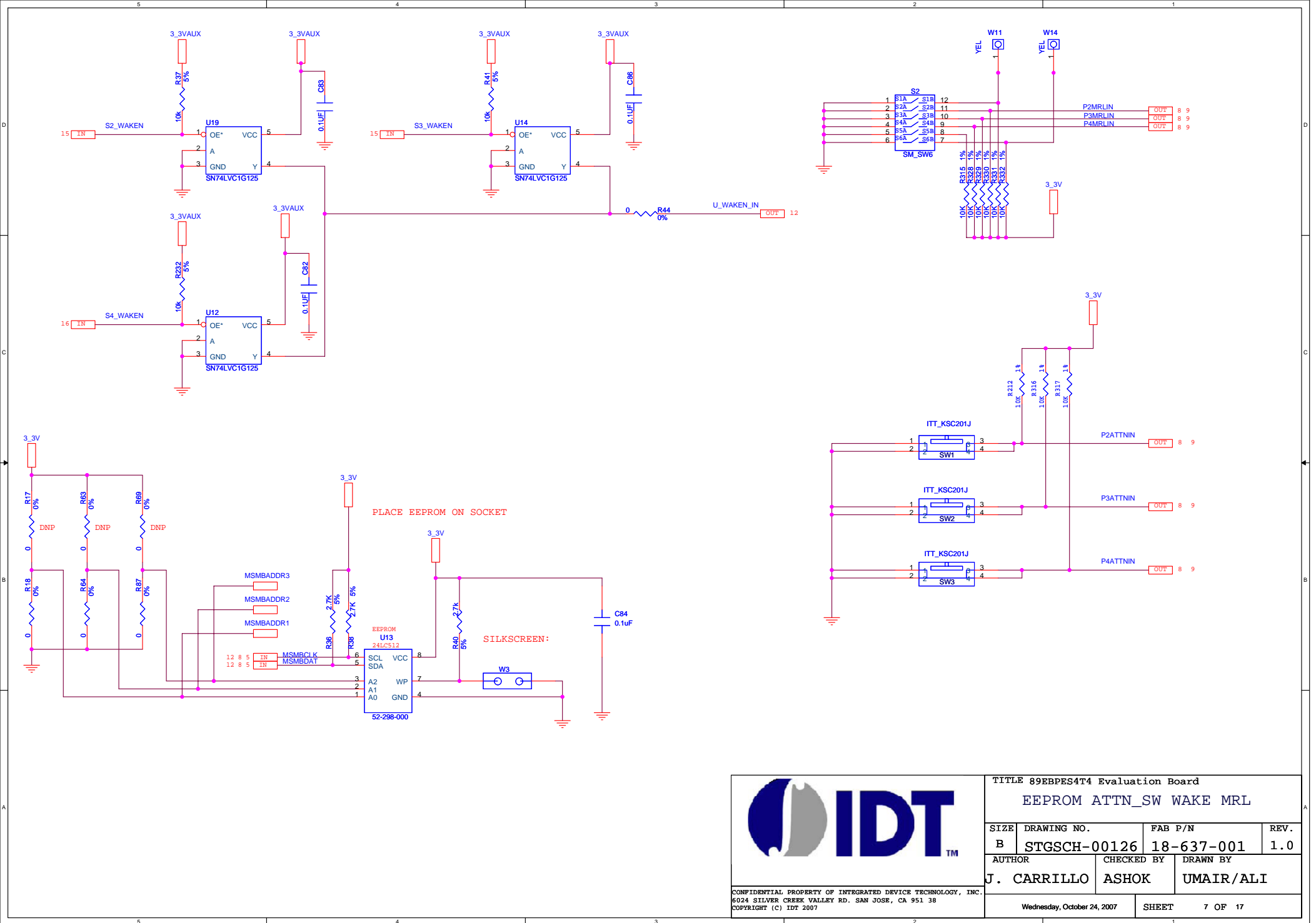
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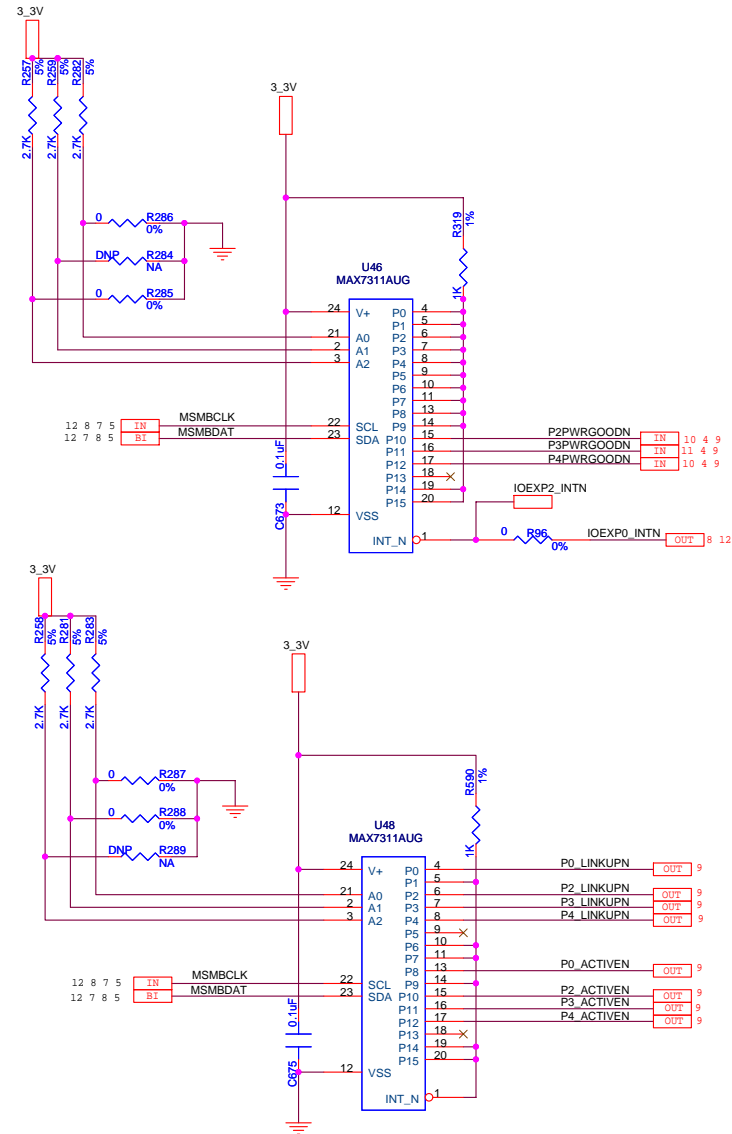
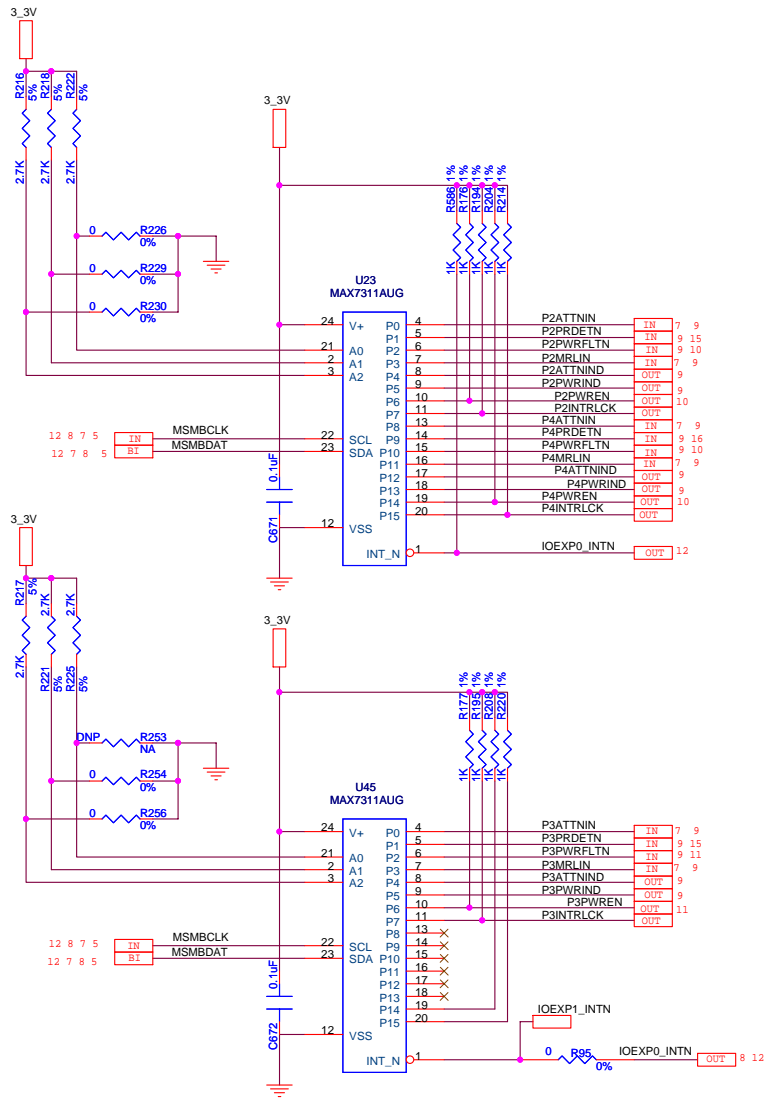
				TITLE 89EBPES4T4 Evaluation Board RESET, JTAG, SMBUS, DIPSW			
				SIZE B	DRAWING NO. STGSCH-00126	FAB P/N 18-637-001	REV. 1.0
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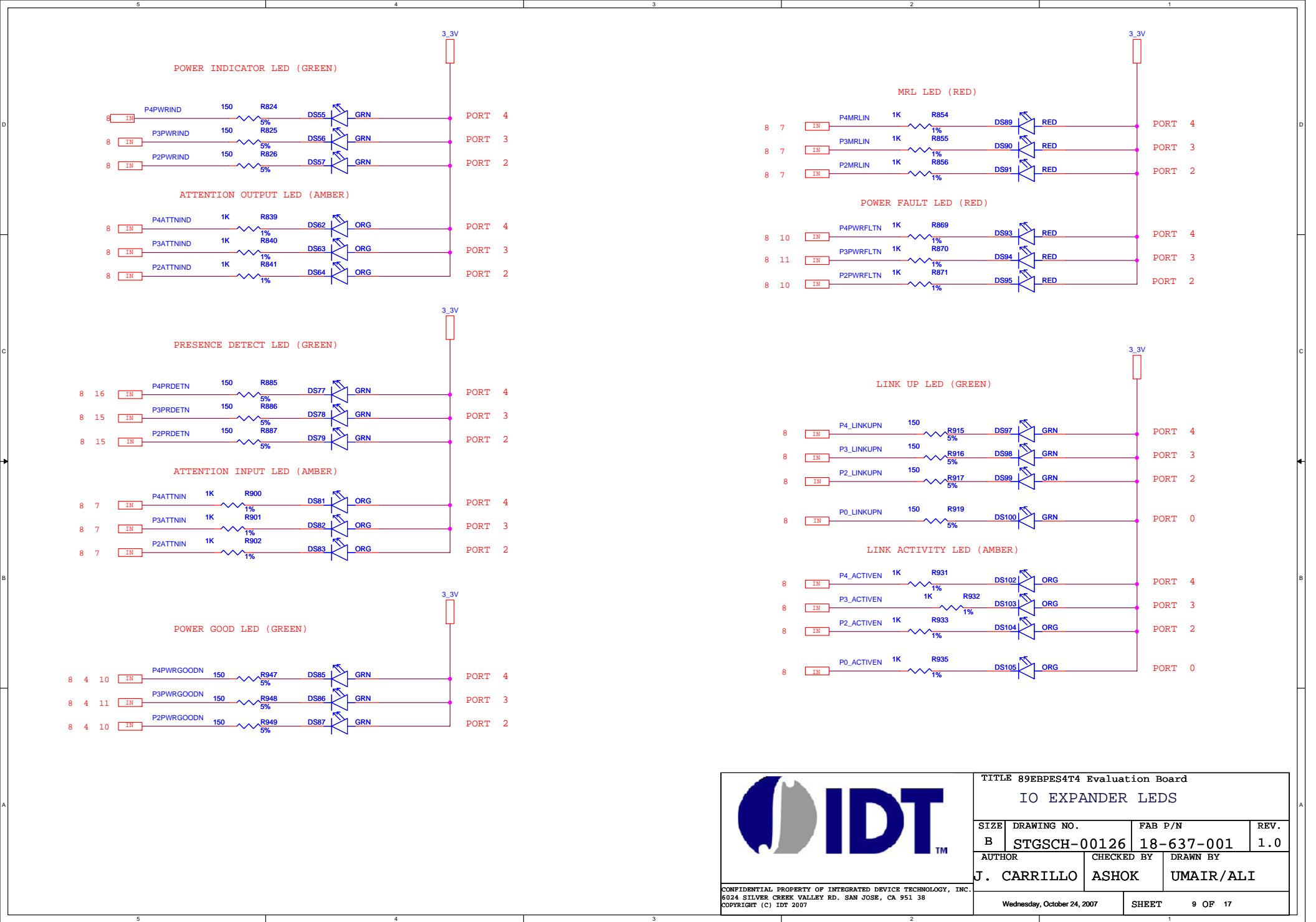
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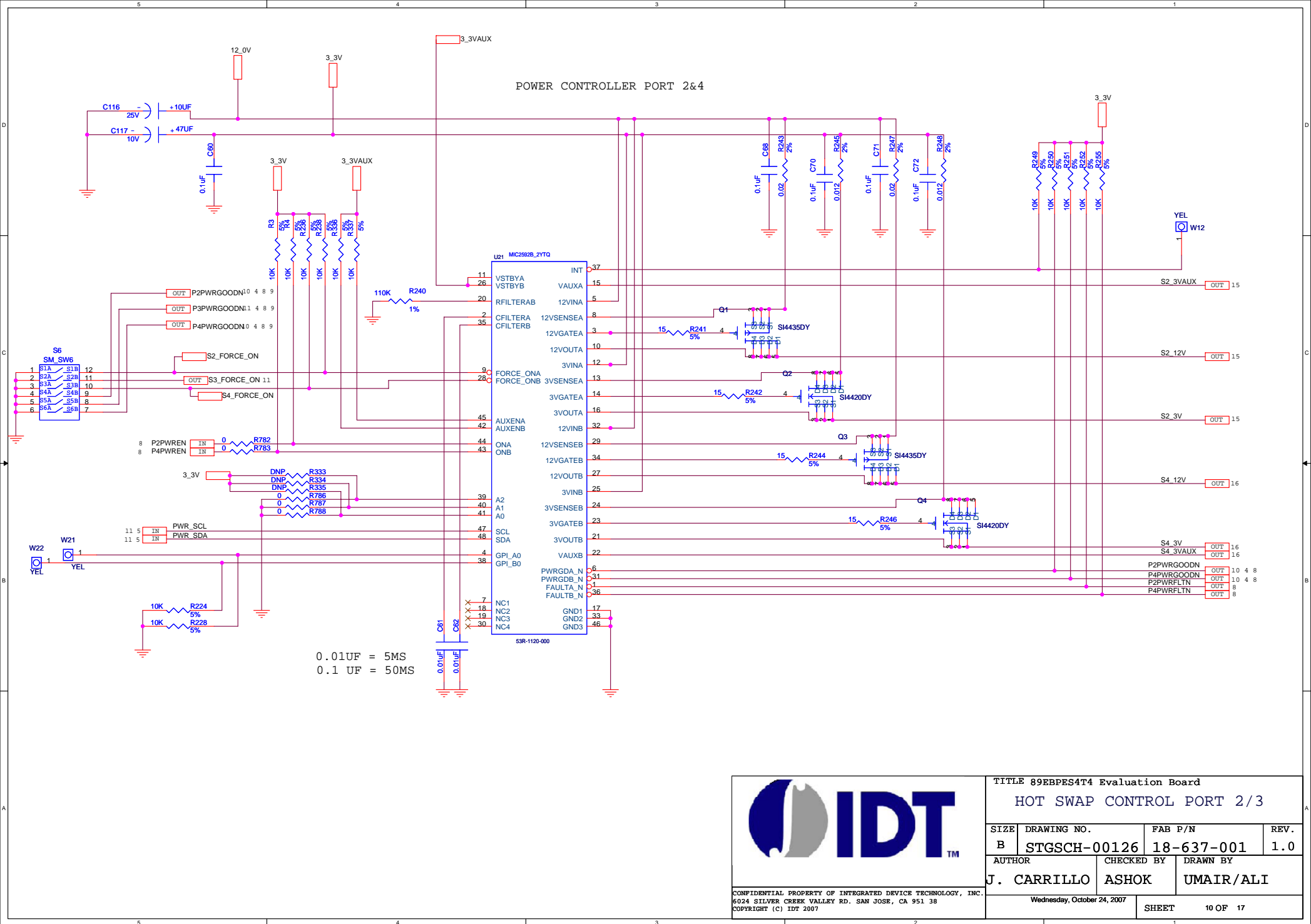
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EEPROM ATTN_SW WAKE MRL			
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IO EXPANDERS			
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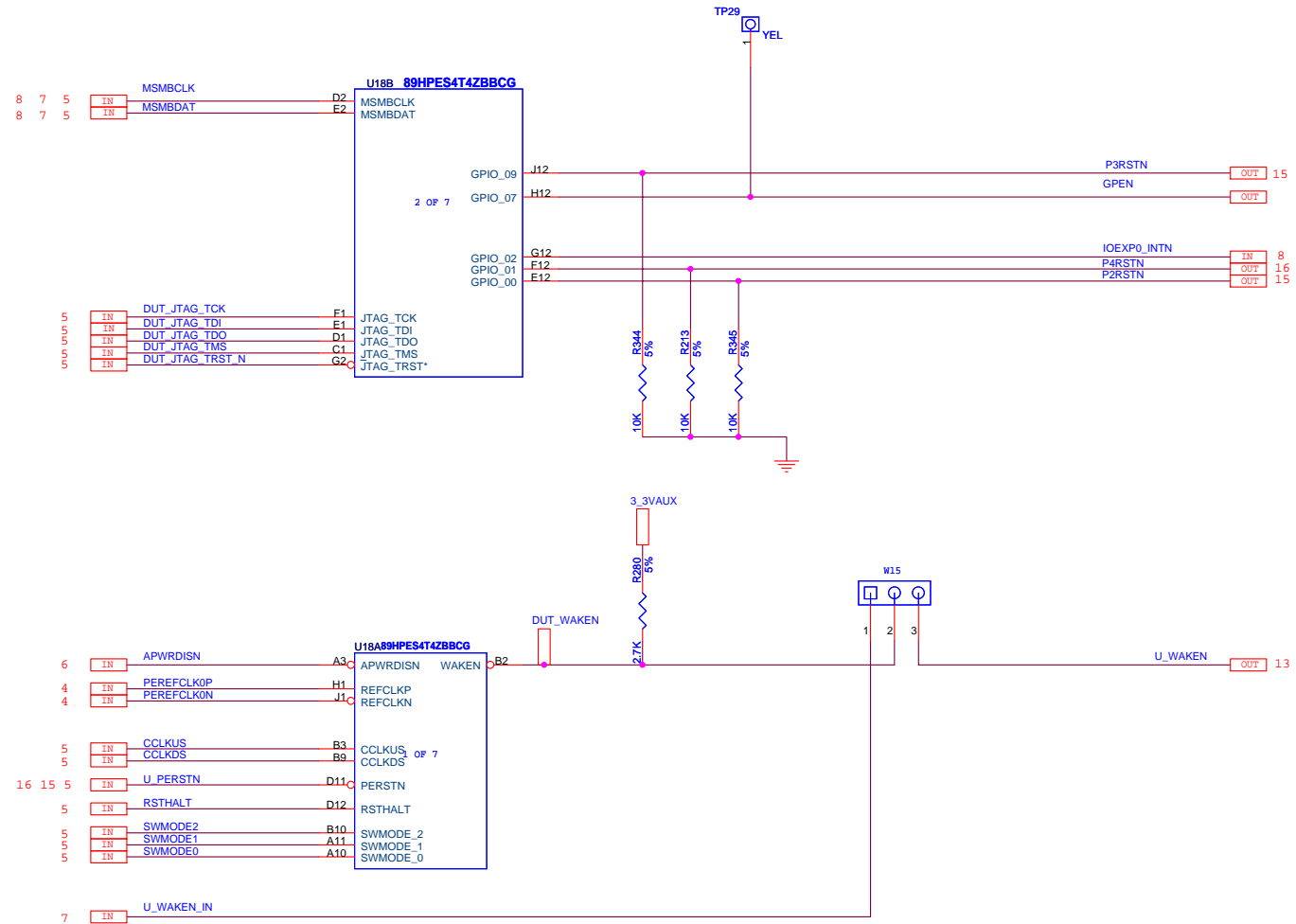
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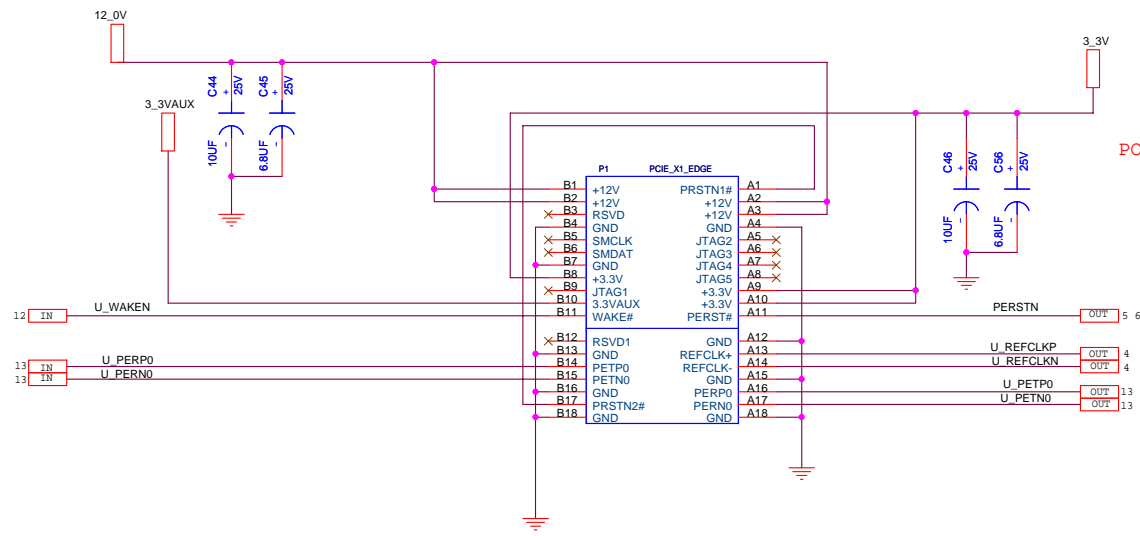
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HOT SWAP CONTROL PORT 2/3			
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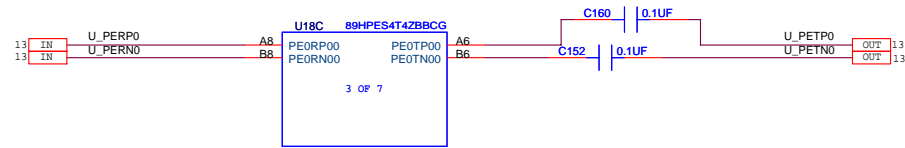


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PES4T4 CLOCK, SMBUS GPIO			
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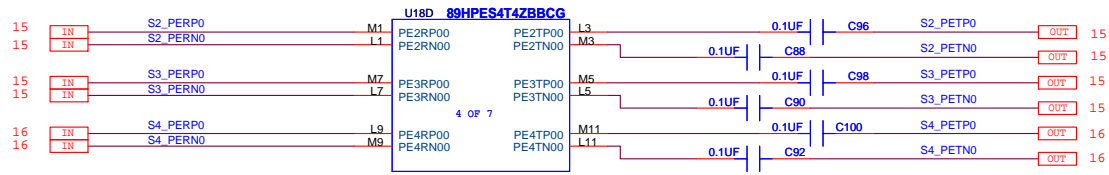



PORT 0 UPSTREAM EDGE CONNECTOR PROVIDES ALL POWER FOR BOARD

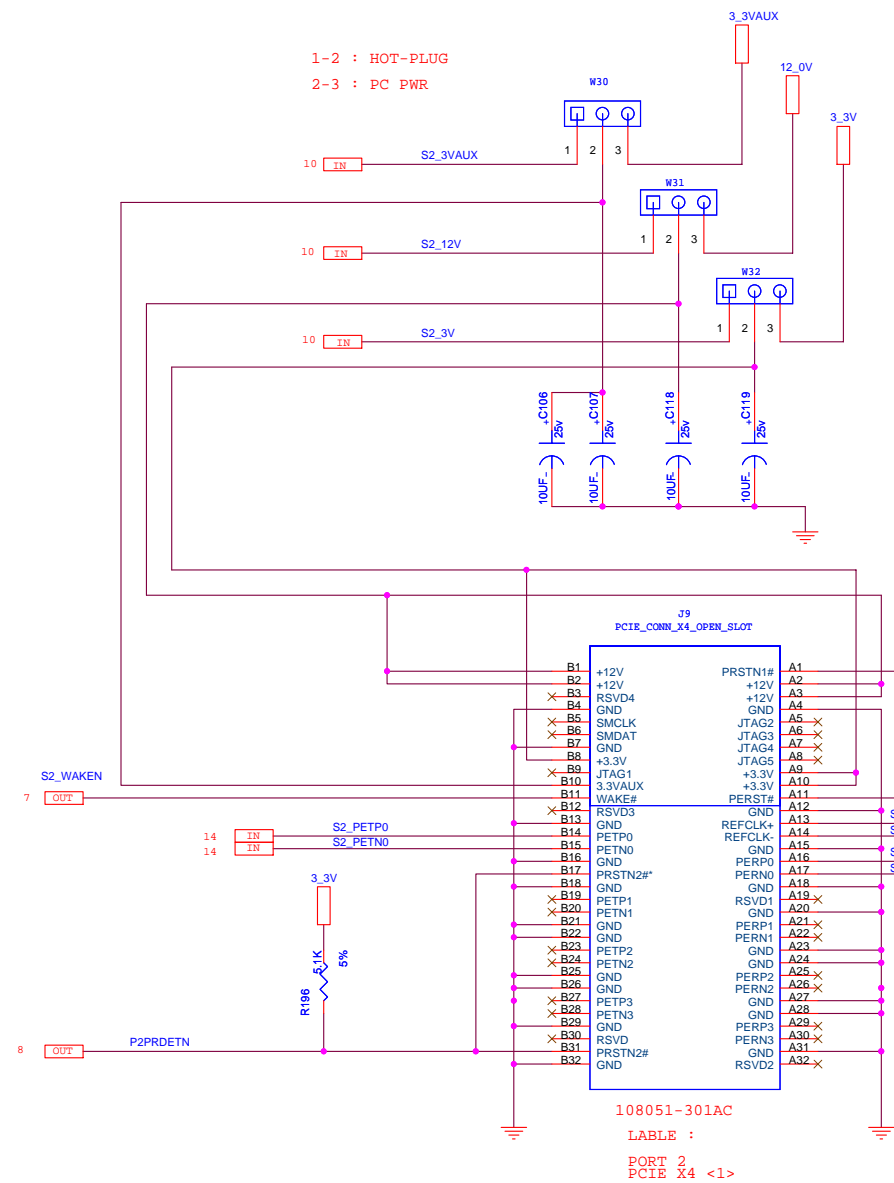


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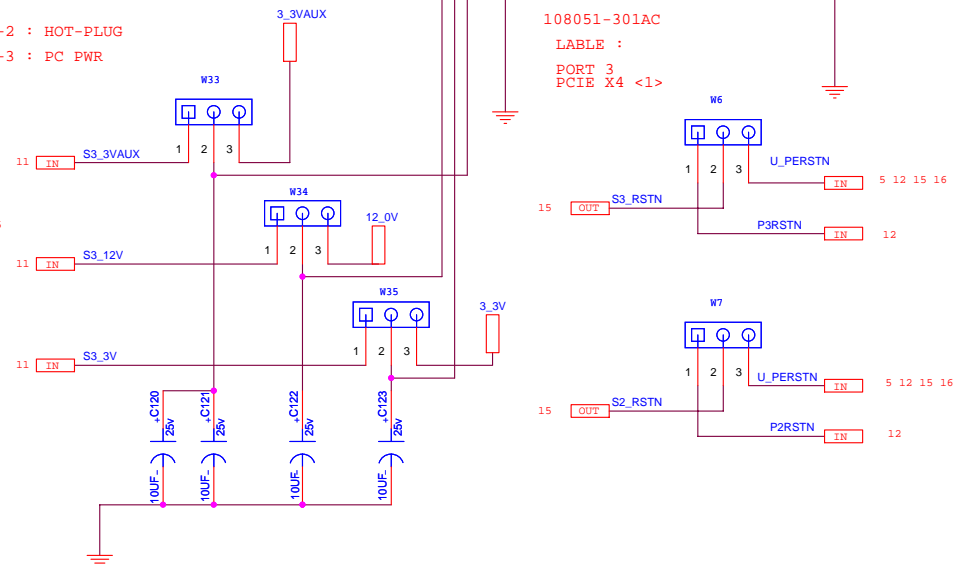
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SIZE B	DRAWING NO. STGSCH-00126	FAB P/N 18-637-001	REV. 1.0
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		TITLE 89EBPES4T4 Evaluation Board			
		PES4T4 DOWNSTREAM PORTS			
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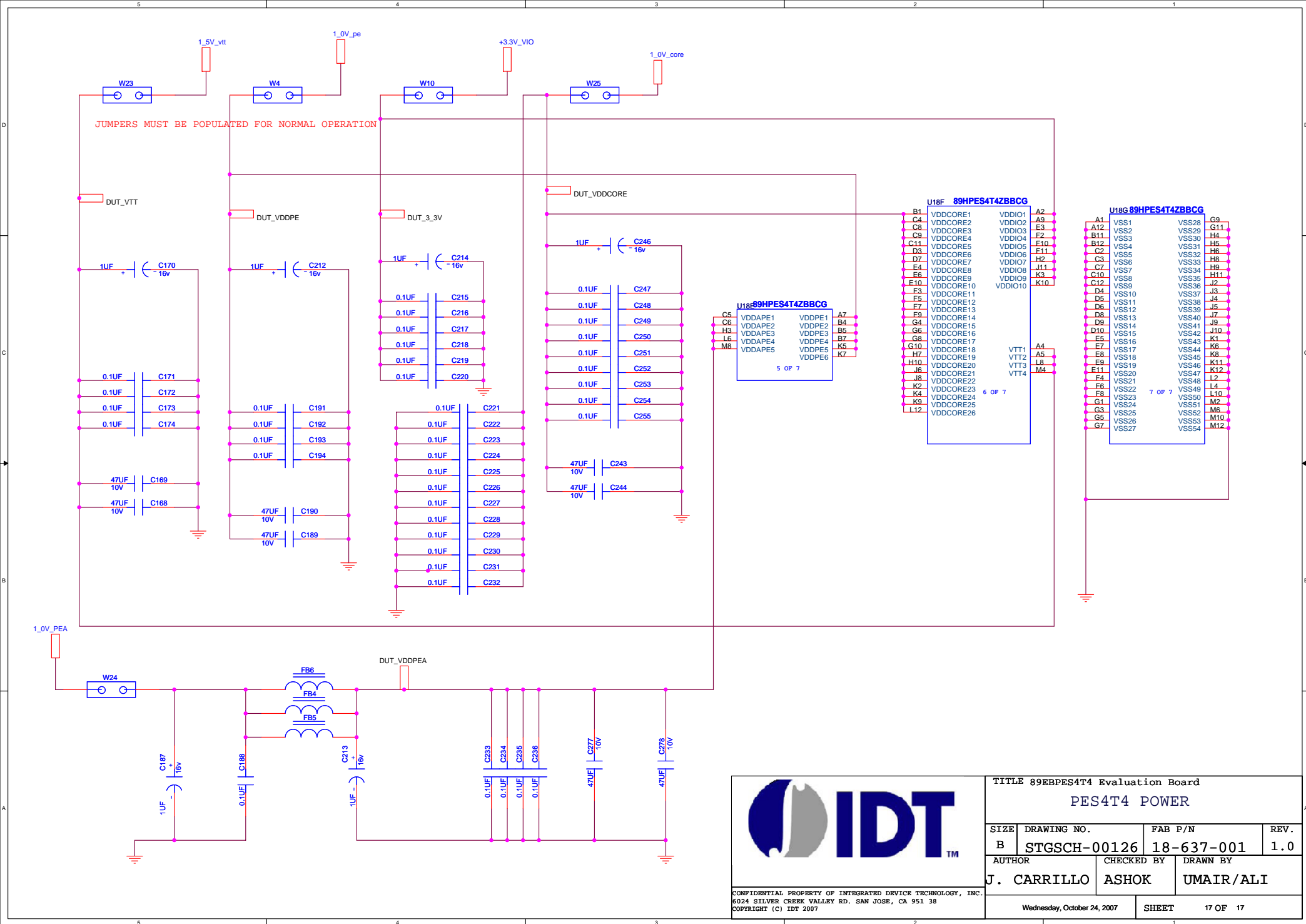


1-2 : HOT-PLUG
2-3 : PC PWR



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PORT 2 AND PORT 3			
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TITLE	89EBPES4T4 Evaluation Board
	PES4T4 POWER

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