


		REVISIONS		
DCN	REV	DESCRIPTION	DATE	CHANGE BY
STGC-0106R01	1.0	INITIAL RELEASE	2007-02-09	J.CARRILLO

SHEET	DESCRIPTION
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TITLE 89BPES8T5

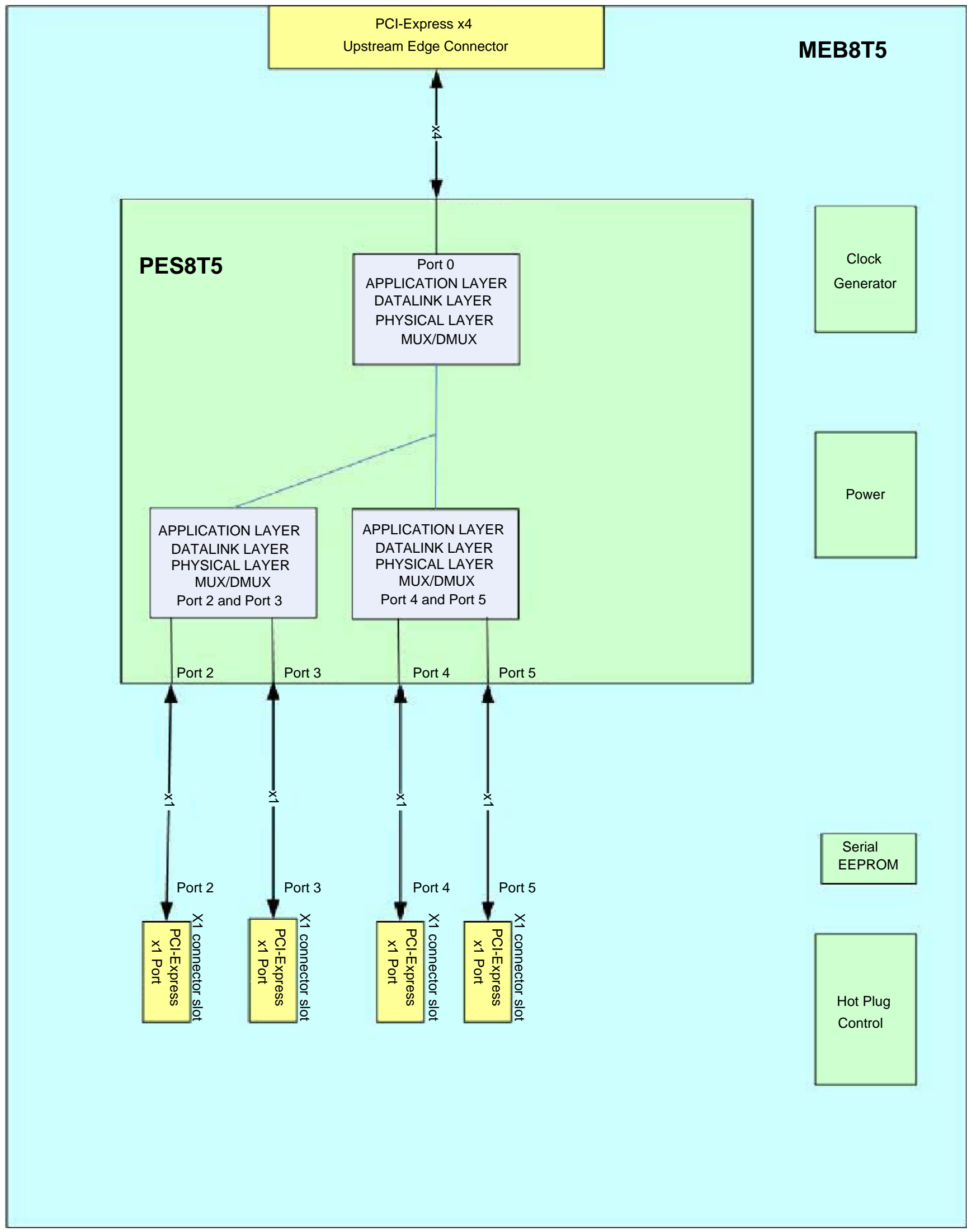
89HPES8T5 EVALUATION BOARD

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B	STGSCH-00106	18-613-001	1.0
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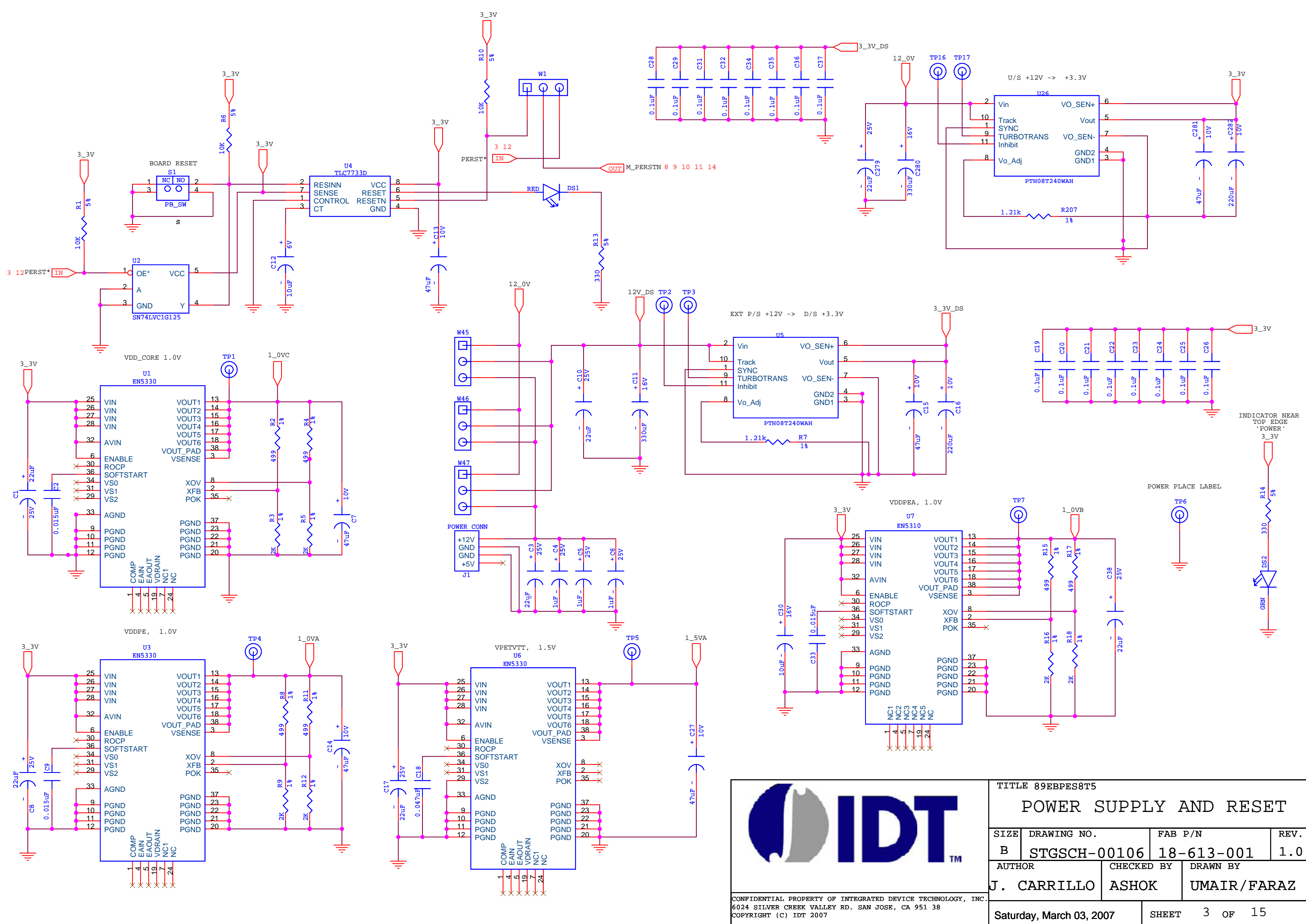
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BLOCK DIAGRAM

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	BLOCK DIAGRAM			
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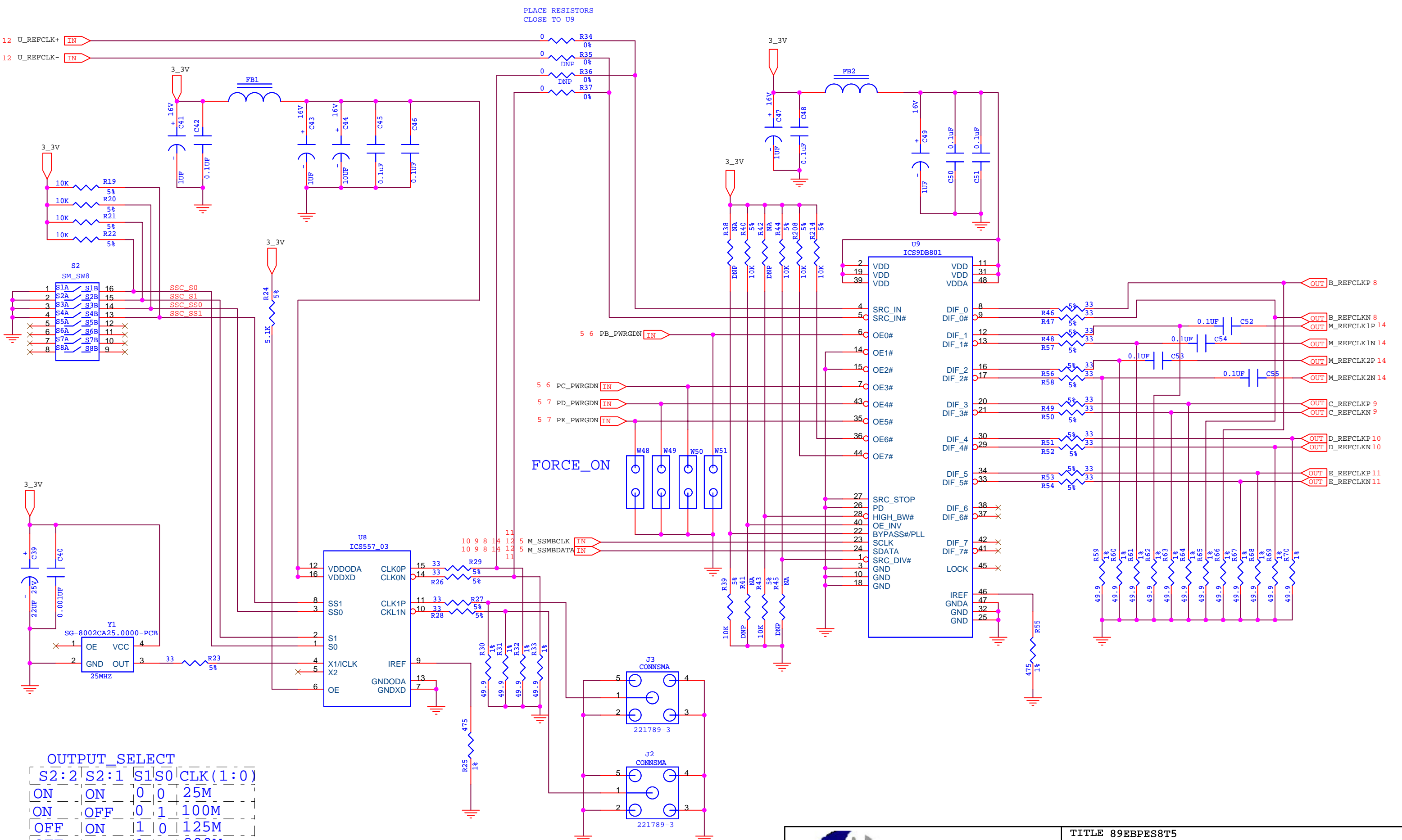


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POWER SUPPLY AND RESET

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OUTPUT_SELECT				
S2:2	S2:1	S1:S0	CLK(1:0)	
ON	ON	0 0	25M	
ON	OFF	0 1	100M	
OFF	ON	1 0	125M	
OFF	OFF	1 1	200M	

SPREAD_SELECTION				
S2:4	S2:3	SS1	SS0	SPREAD %
ON	ON	0	0	CENTER +/-0.25
ON	OFF	0	1	DOWN -0.5
OFF	ON	1	0	DOWN -0.75
OFF	OFF	1	1	NO SPREAD

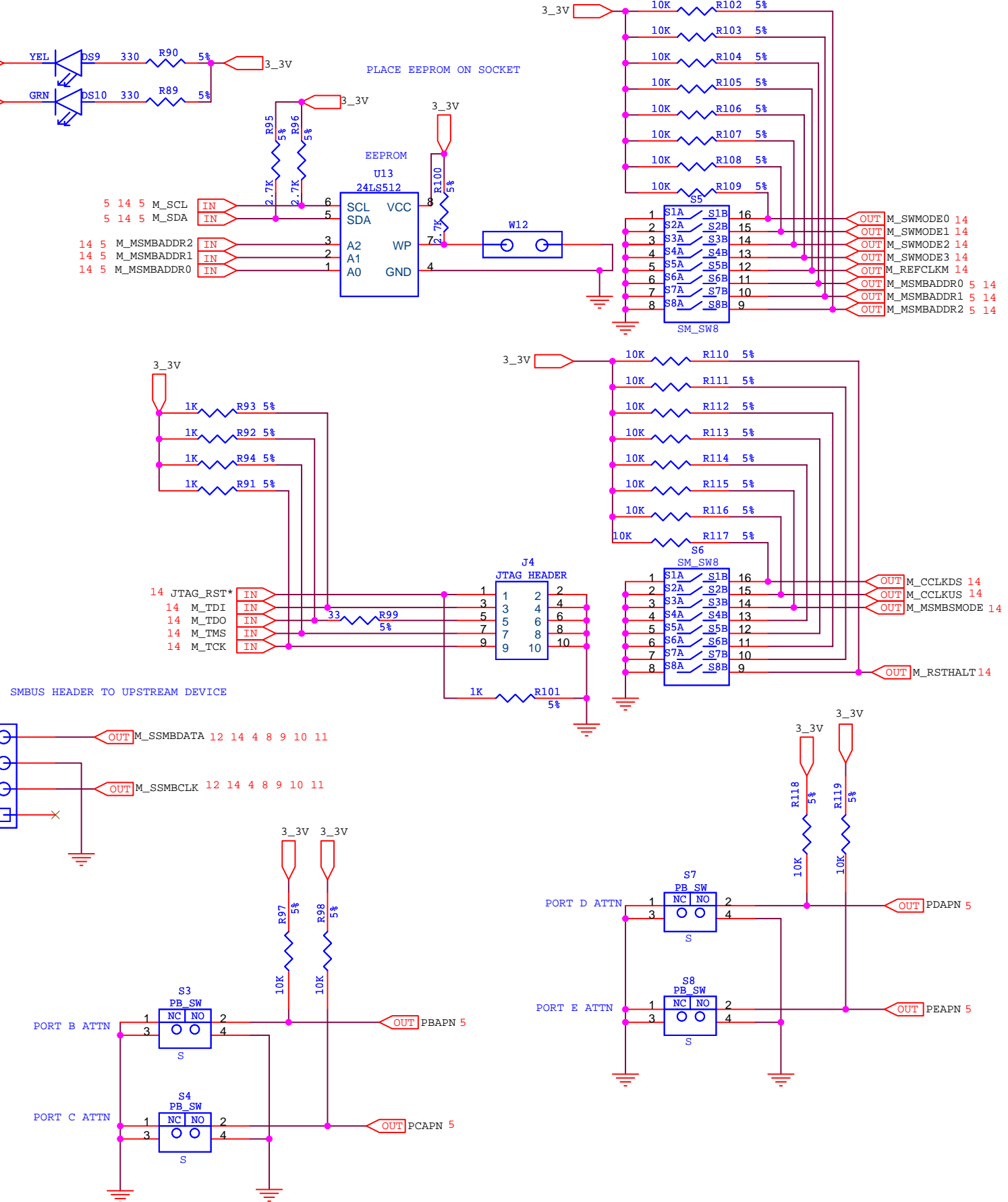
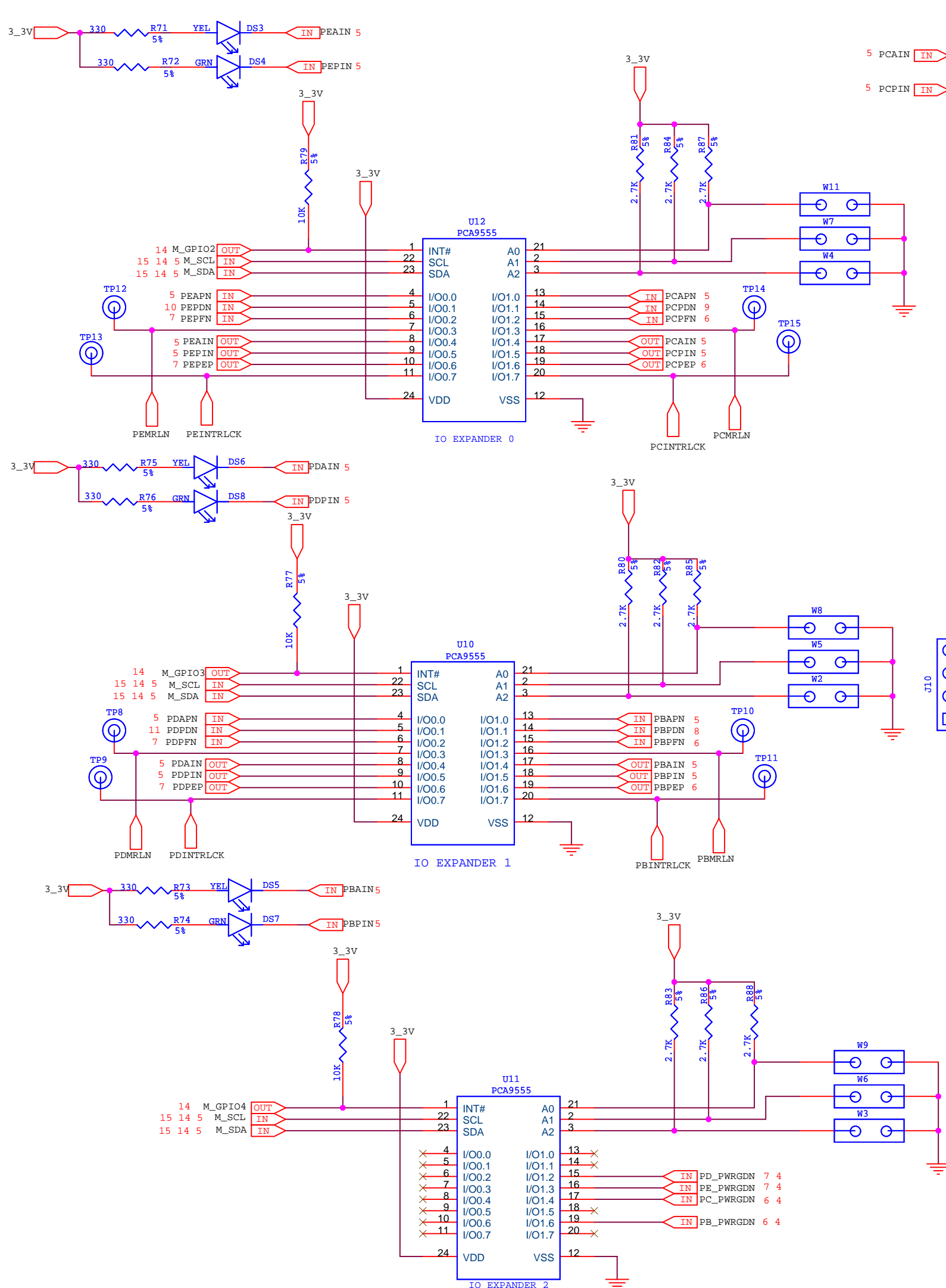


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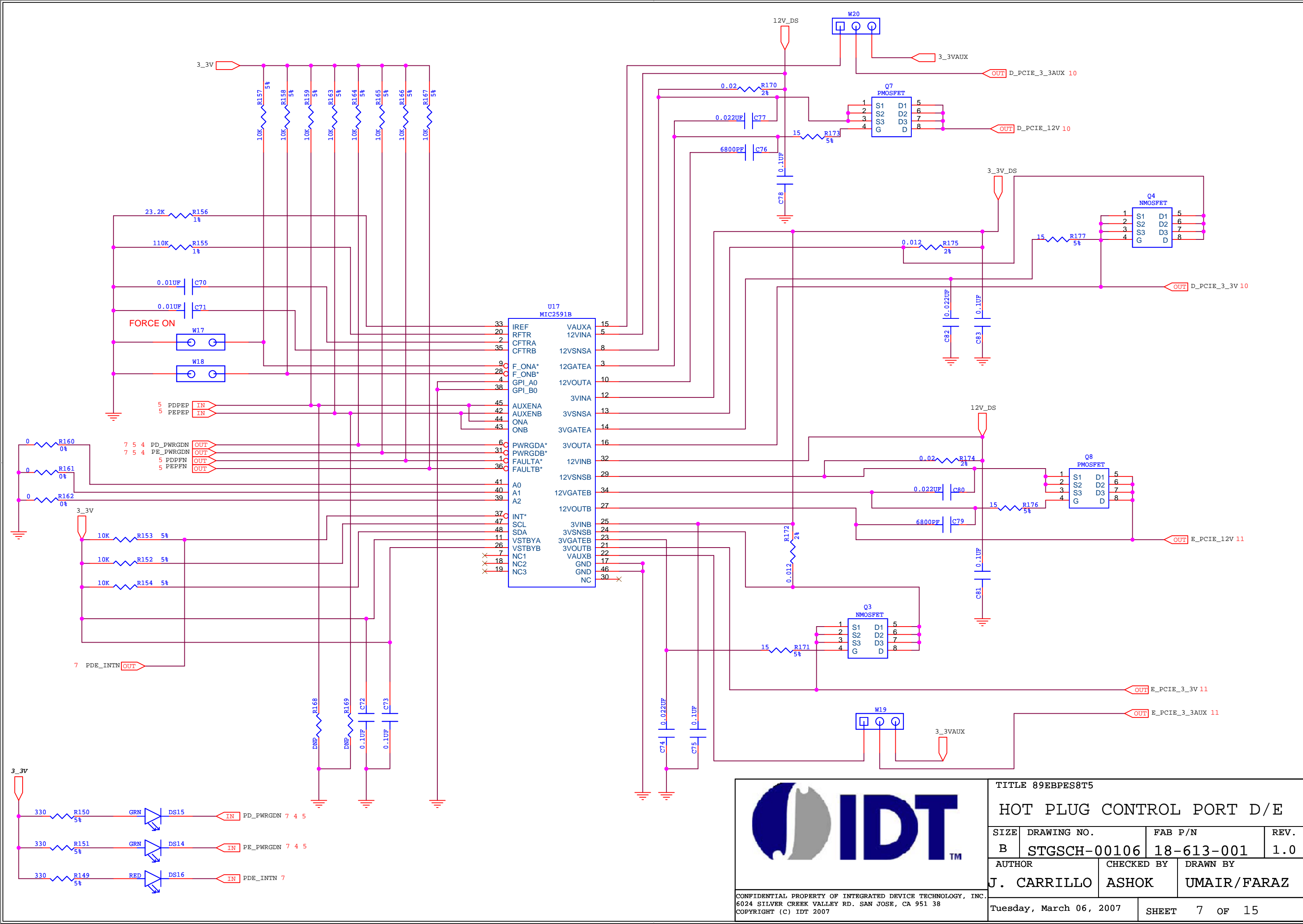
CLOCKS

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TITLE 89EBPES8T5			
SMBUS, JTAG, IO EXPANDER			
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TITLE 89EBPES8T5

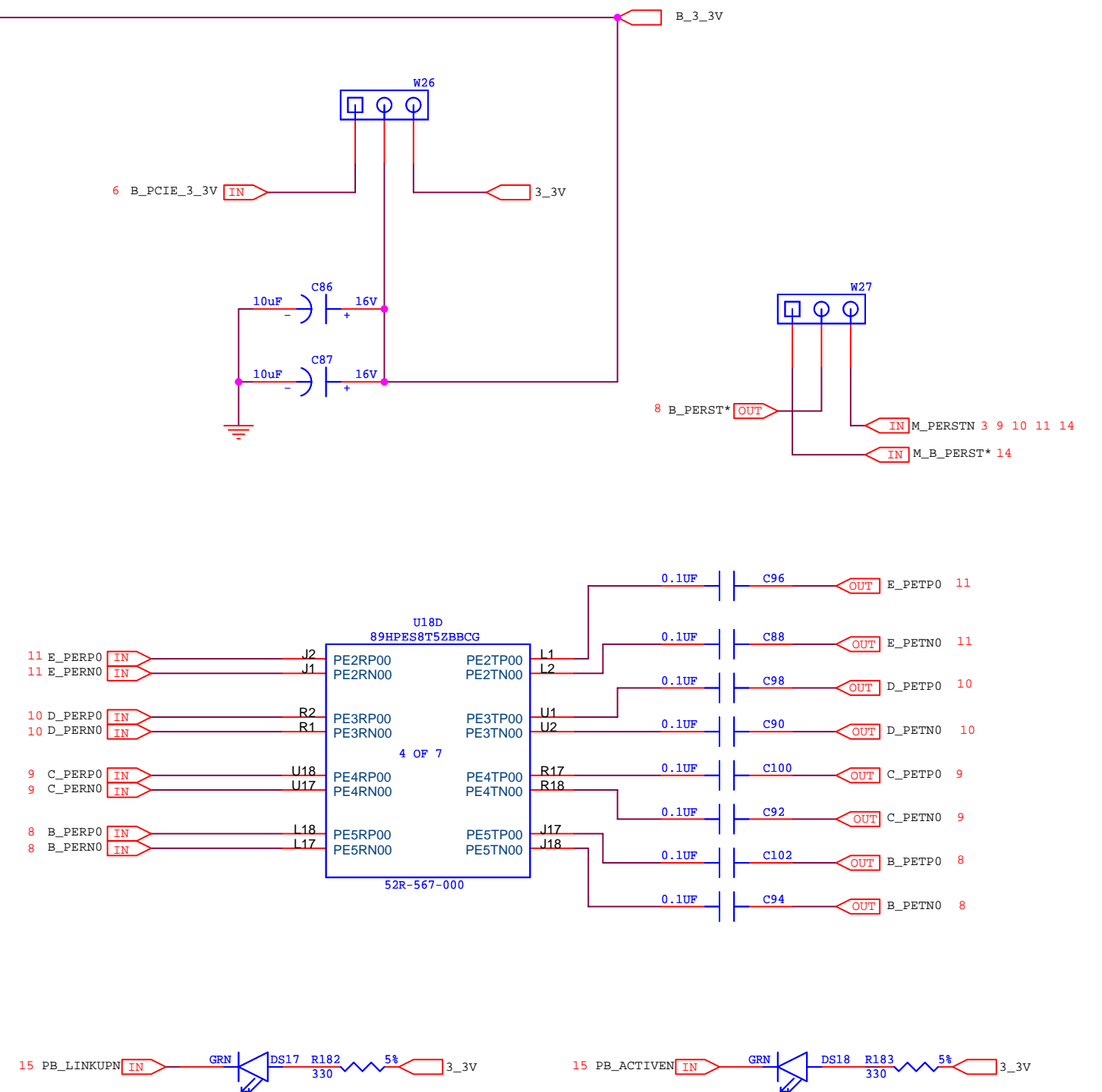
HOT PLUG CONTROL PORT D/E

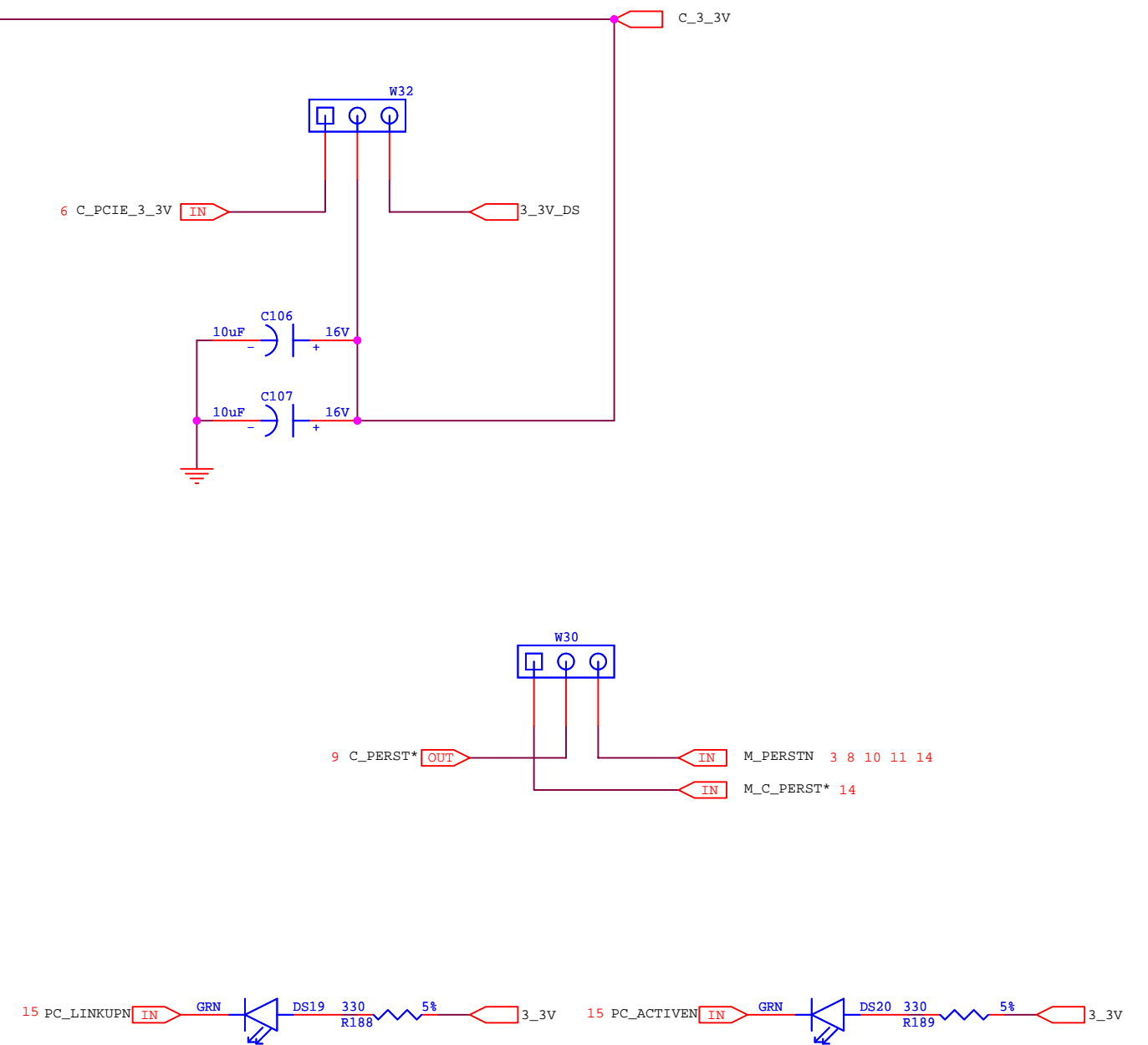
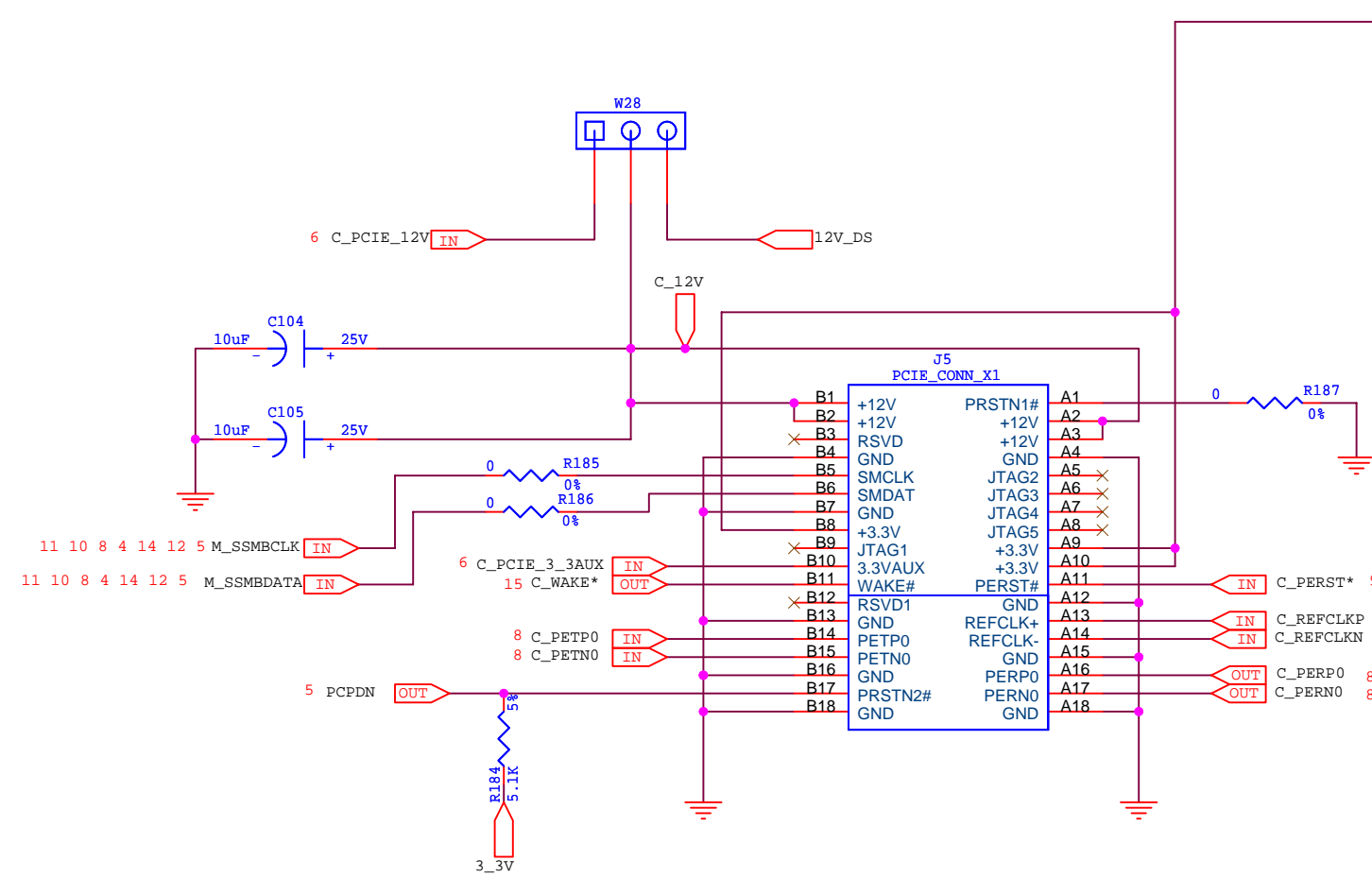
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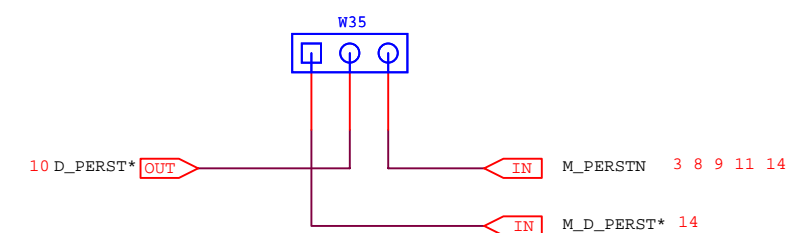
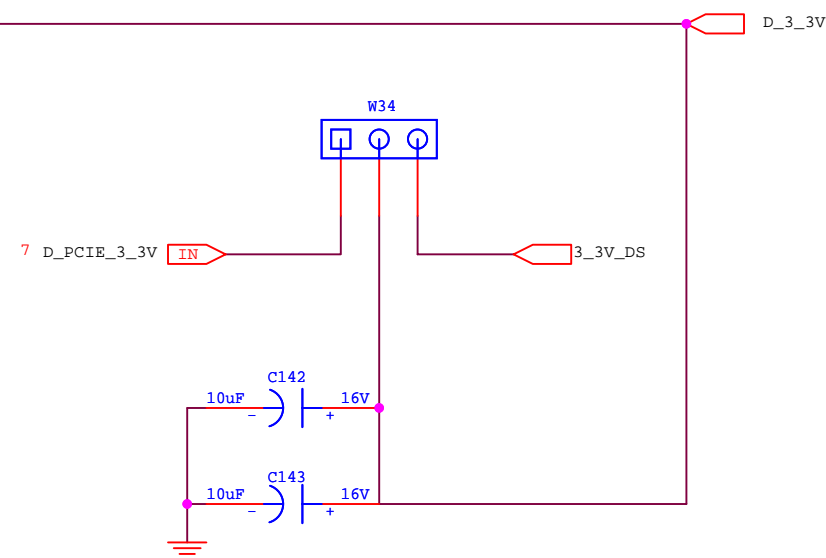
SHEET 7 OF 15

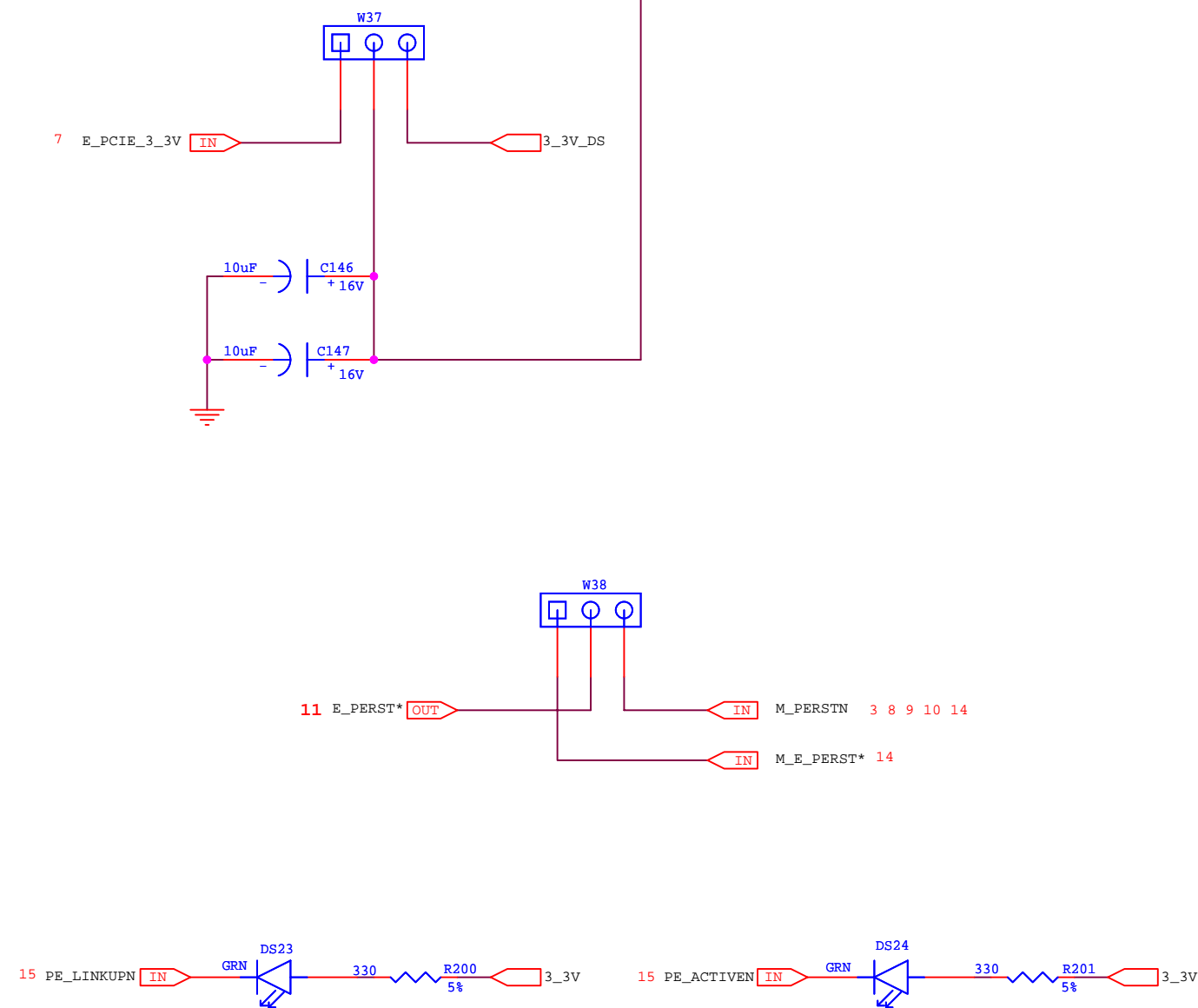
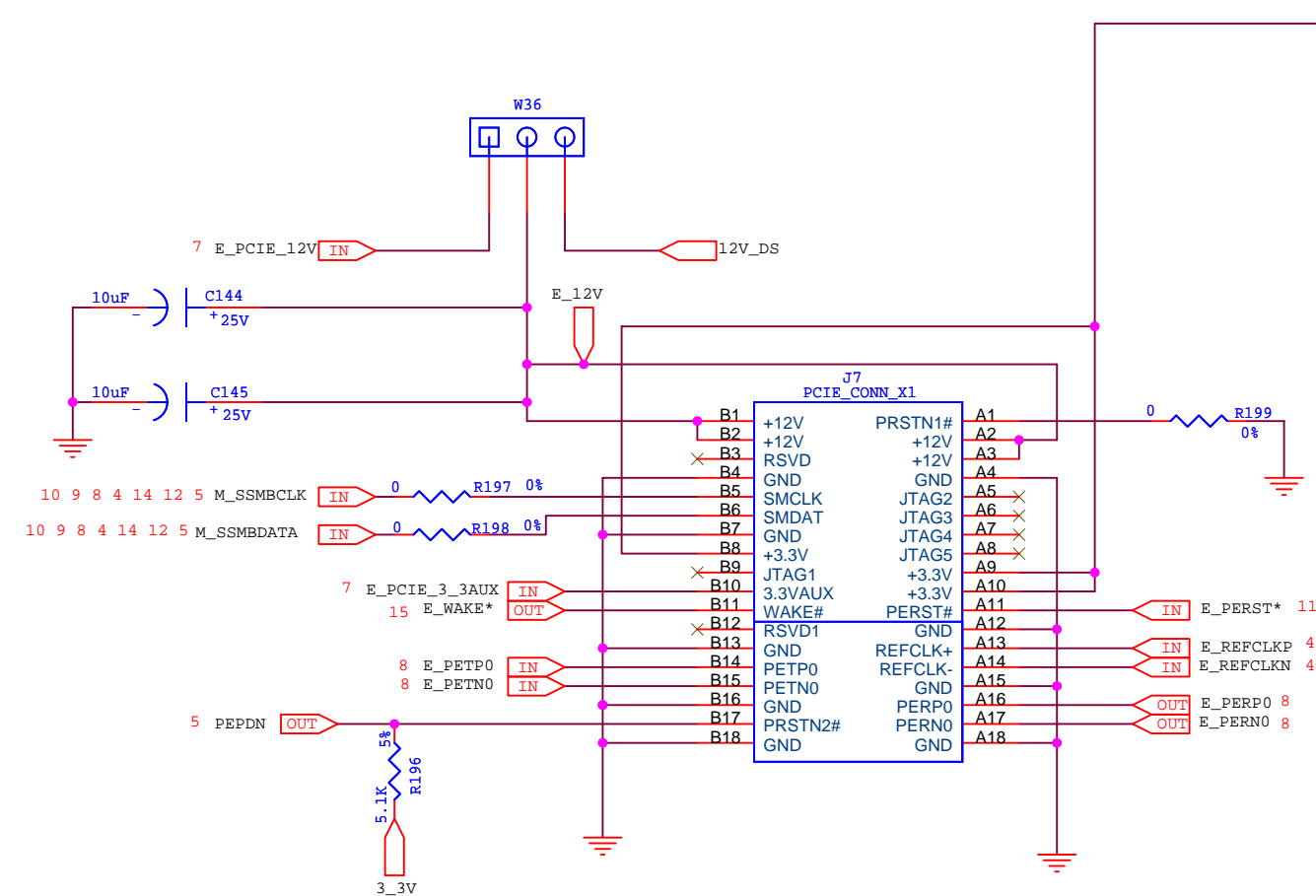





DOWNSTREAM (PORT 4) CONNECTOR

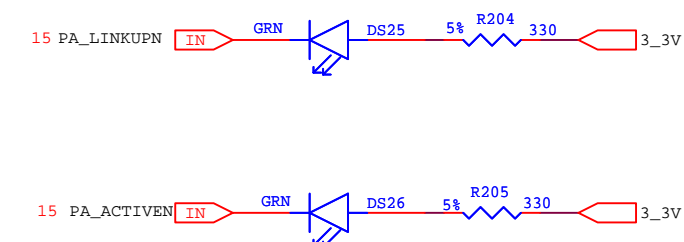
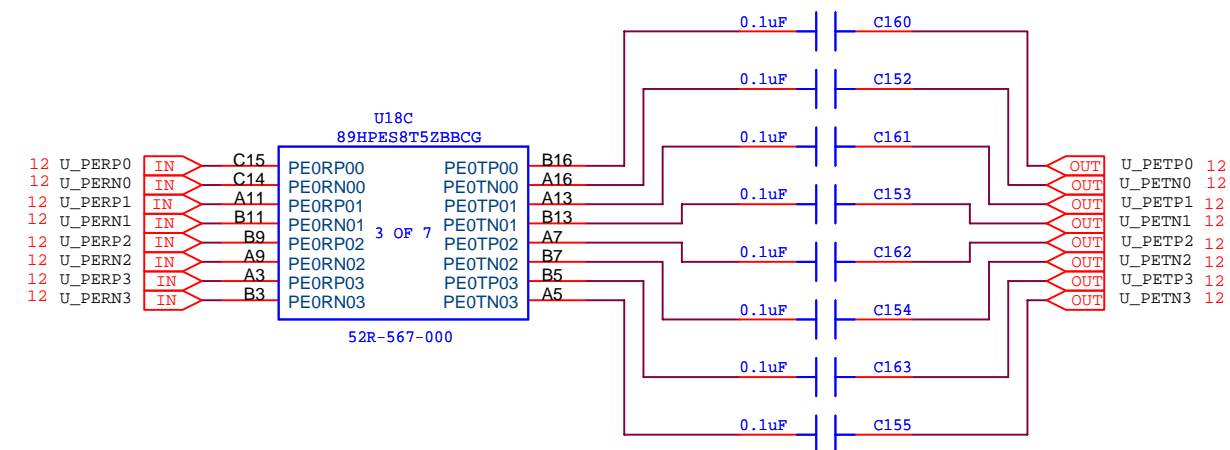
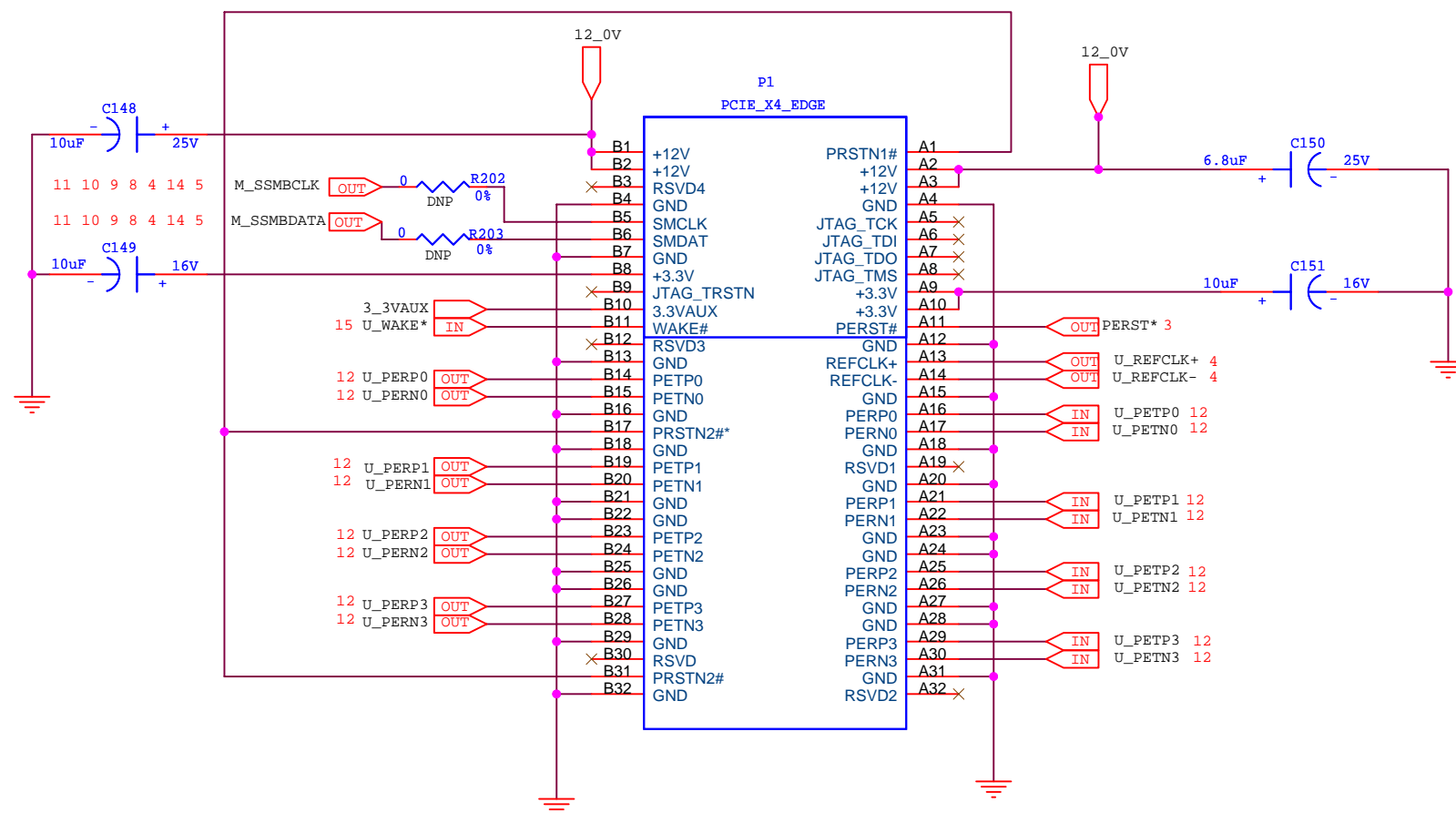
		TITLE 89EBPES8T5			
		PORT C (PORT 4) CONNECTOR			
		SIZE	DRAWING NO.	FAB P/N	REV.
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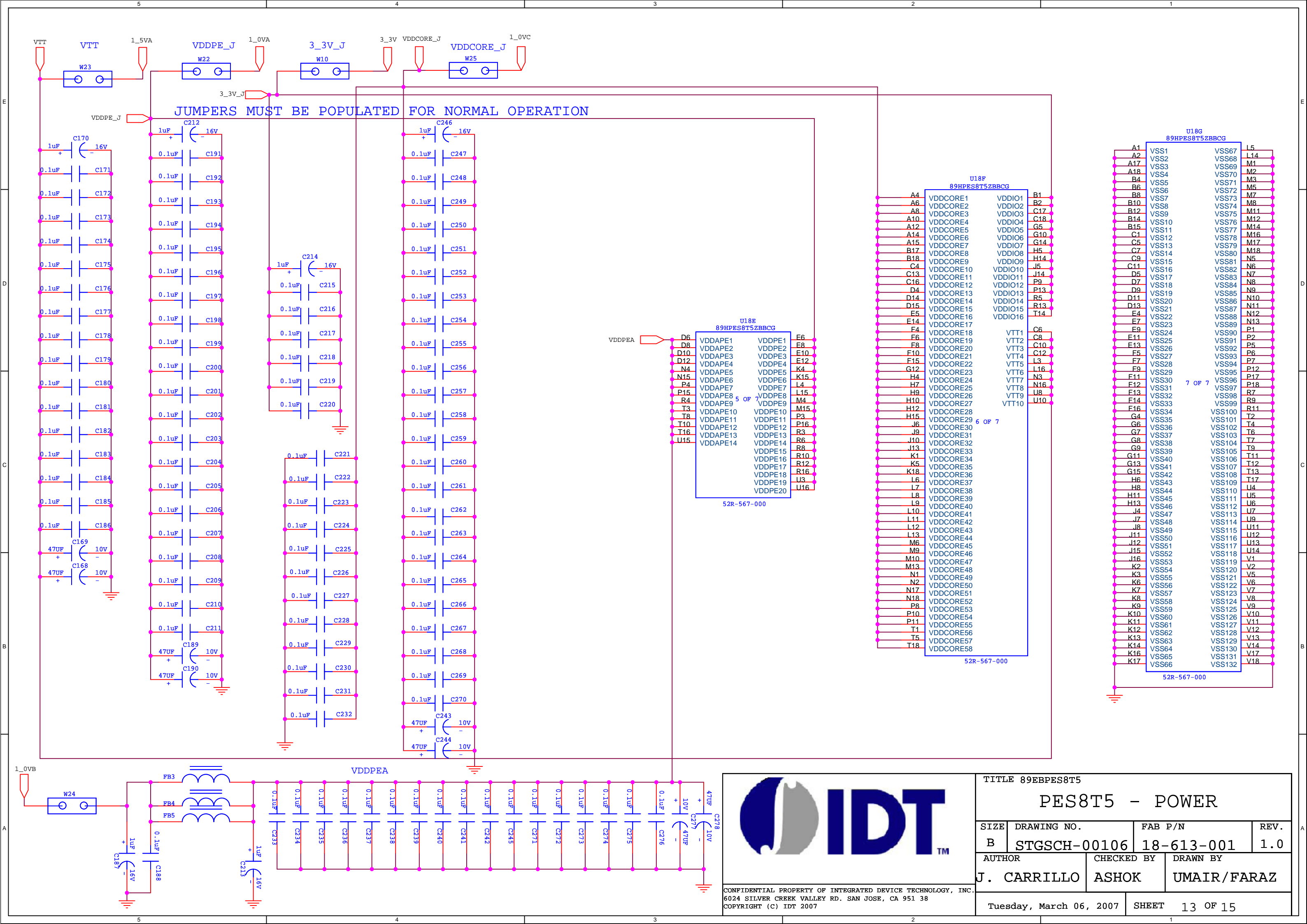
DOWNSTREAM (PORT 2) CONNECTOR

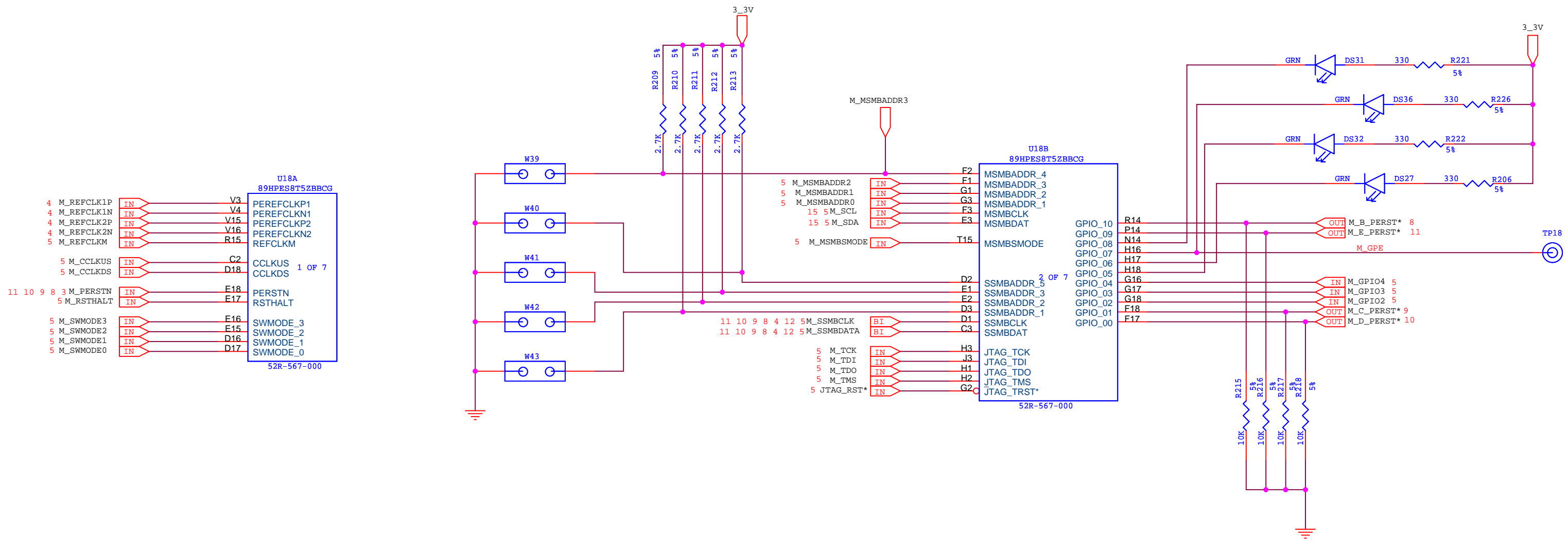
	TITLE 89EBPES8T5			
	PORT E (PORT 2) CONNECTOR			
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PORT 0 UPSTREAM (EDGE) CONNECTOR

	TITLE 89EBPES8T5			
	PORT A (PORT 0) CONN & 8T5			
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TITLE 89BPES8T5			
PES8T5 SMBUS REFCLKS			
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