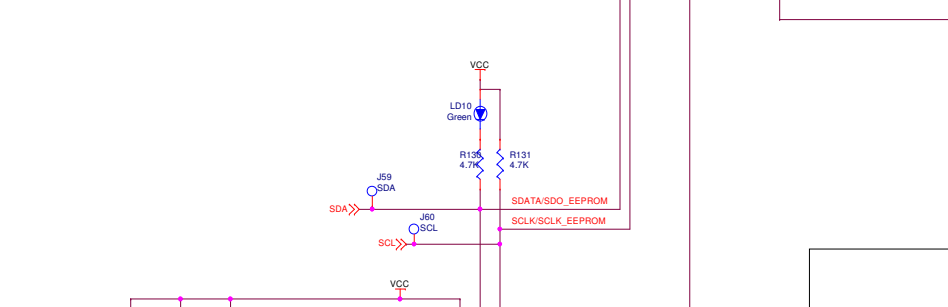
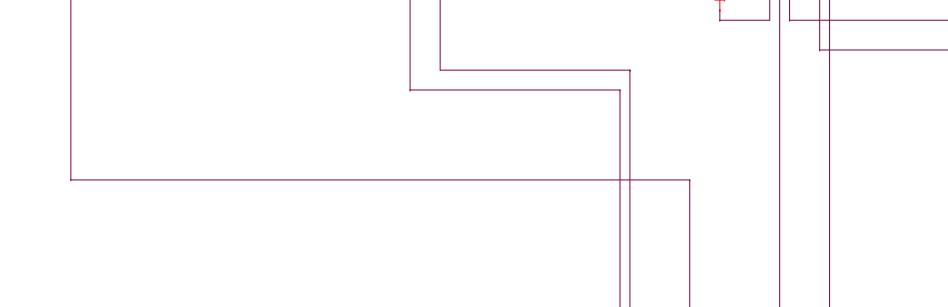
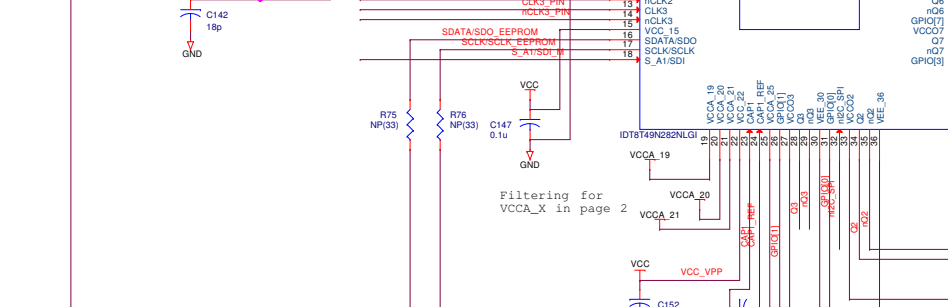
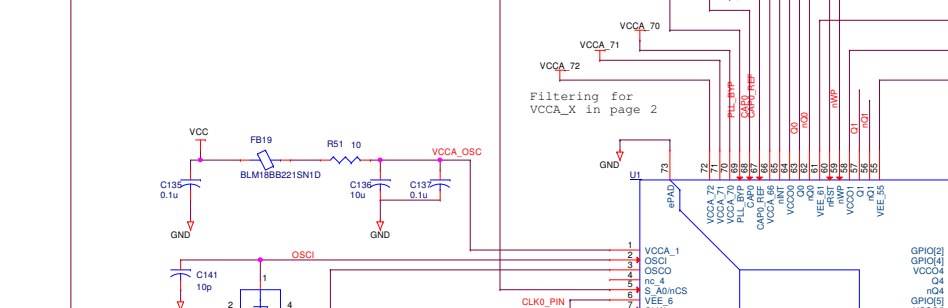
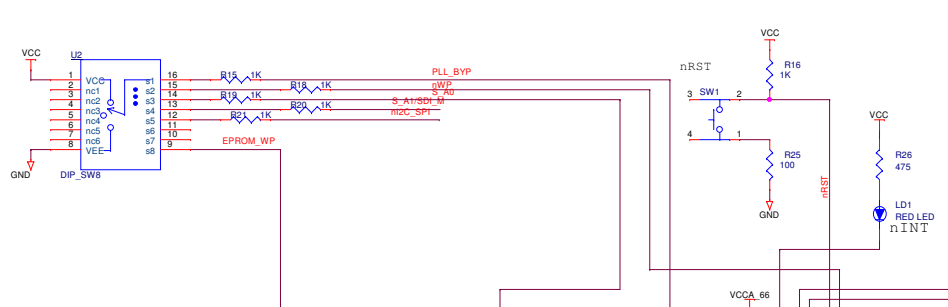
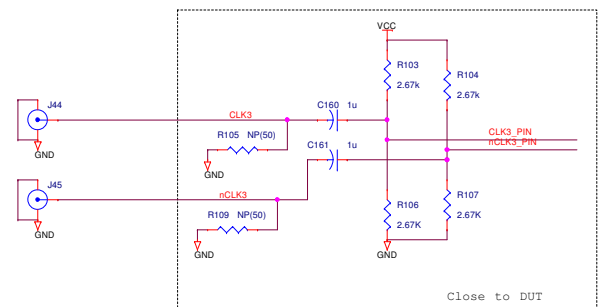
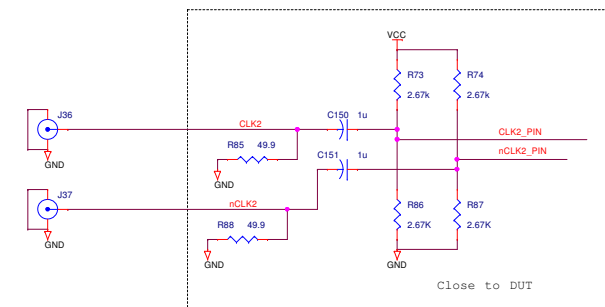
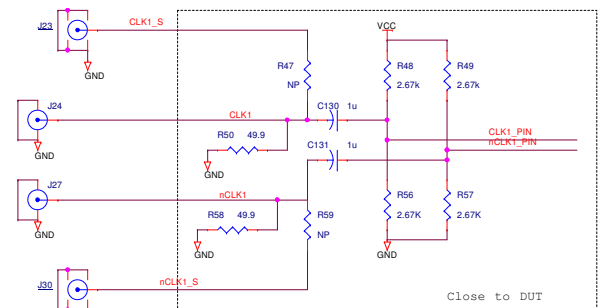
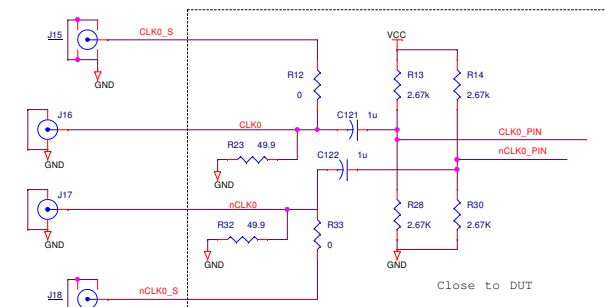
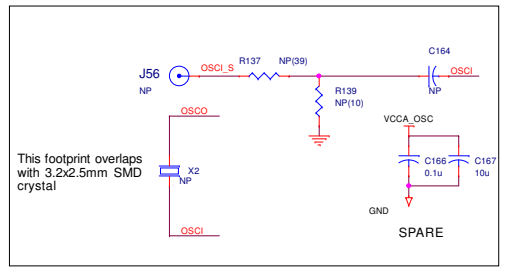
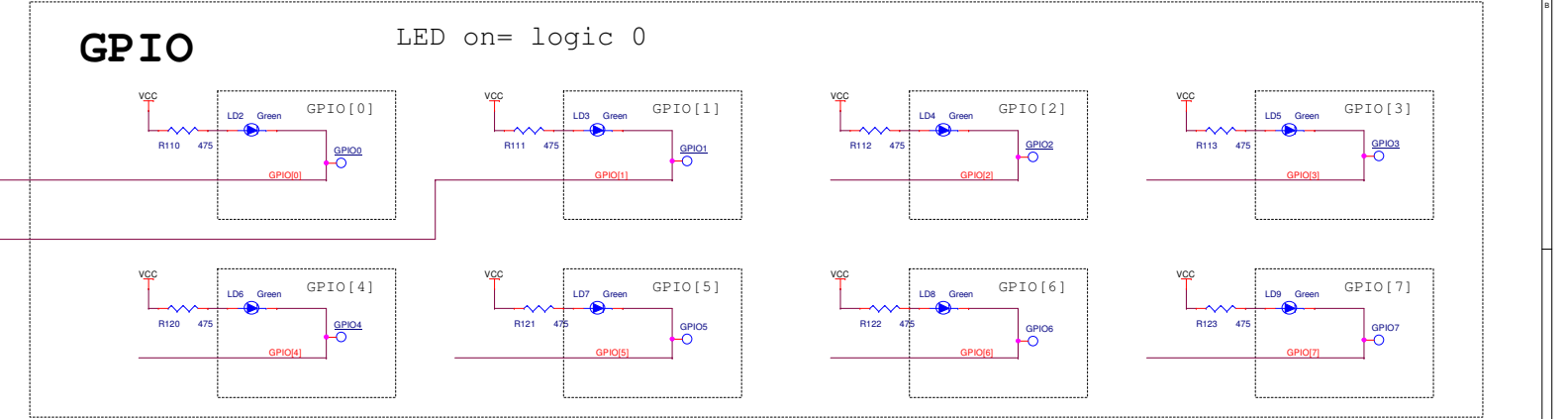
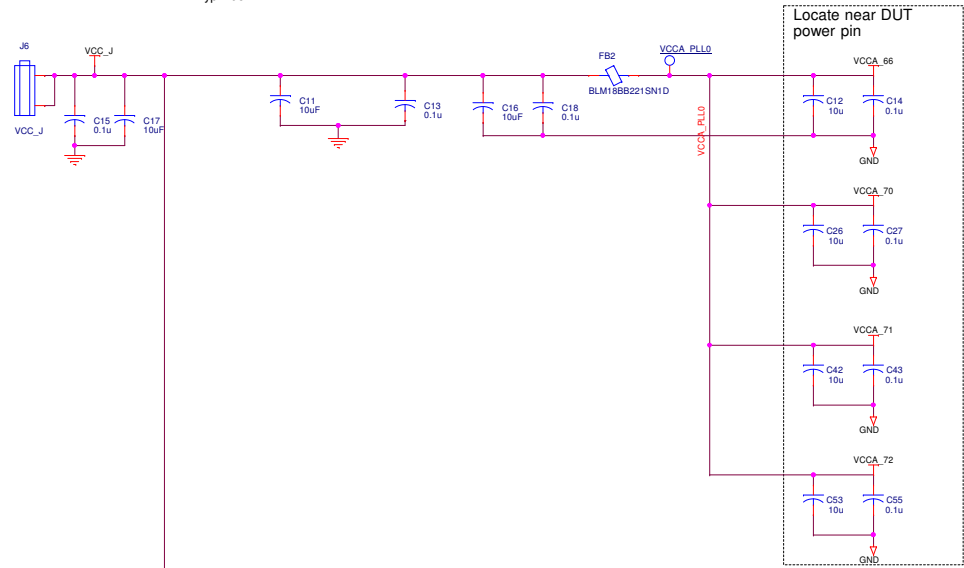


Outputs can be configured to LVPECL, LVDS or LVCMOS drivers

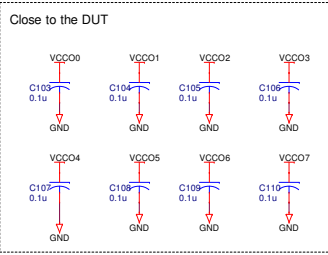
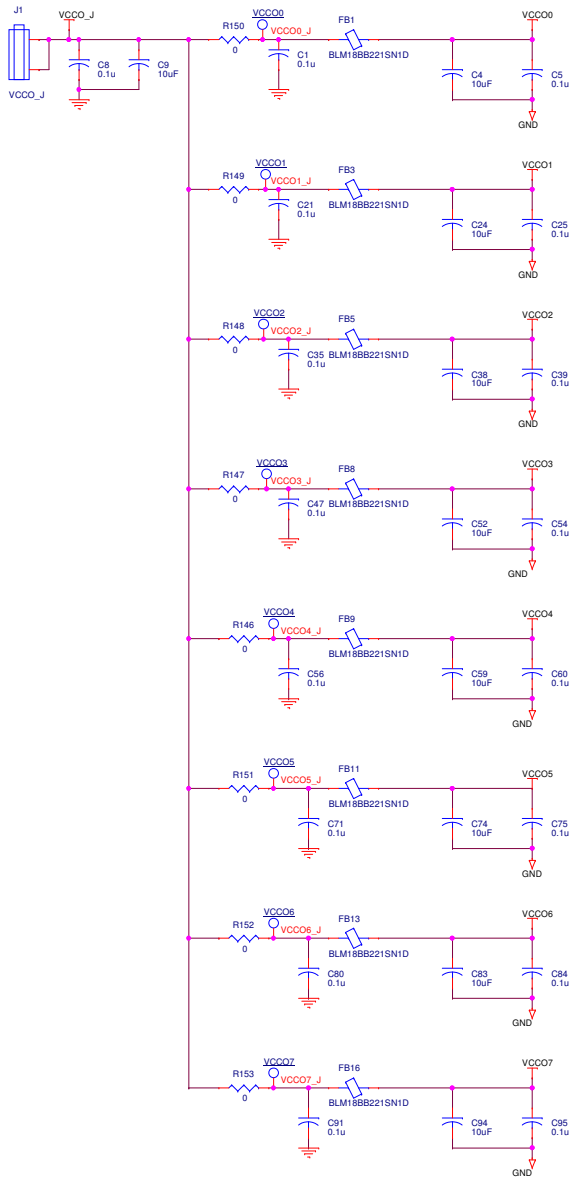
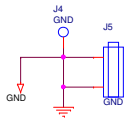
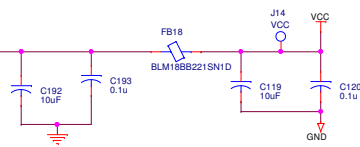
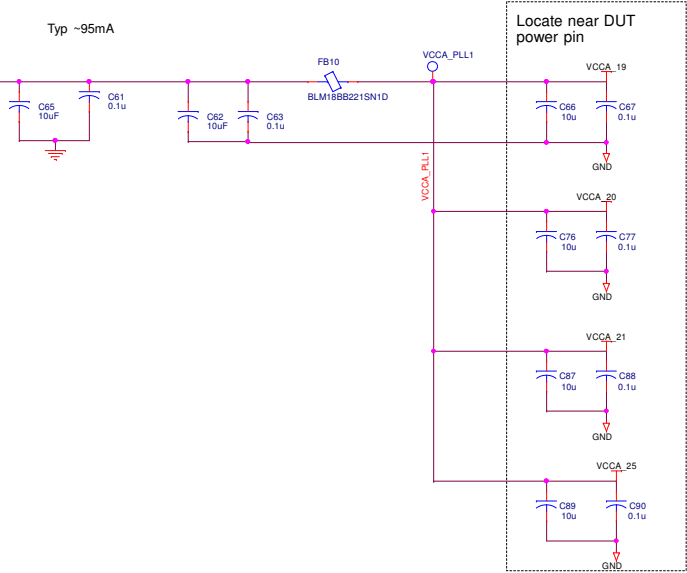
AC coupling capacitors space can be used for series termination for LVCMOS outputs configuration.



Add Lable on the jumpers
Typ ~95mA



Typ ~95mA



Silkscreen labels:
Jumpers
Connectors, SMA's, Banana Jacks
LED's
Push Button
Pin 1 location on IC's, Headers
Test Points