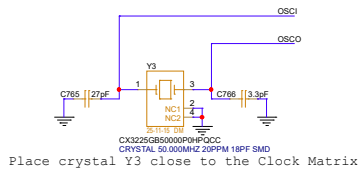


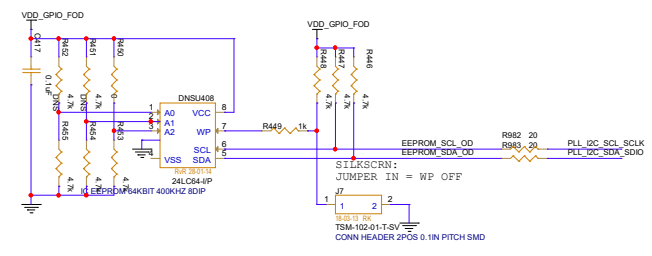
I2C Slave Address is 0x58
(01011000)

GPIO9 to high :
Main Serial port: I2C
Aux Serial port: SPI

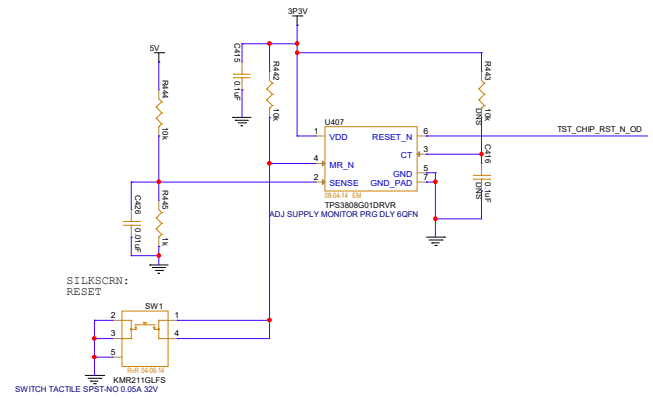


Place crystal Y3 close to the Clock Matrix

Place close to ClockMatrix



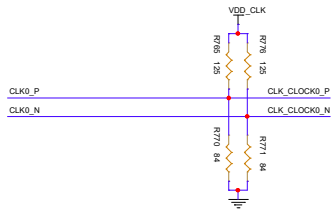
SILKSCREEN:
JUMPER IN = WP OFF



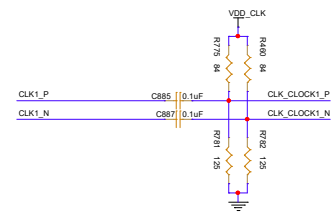
SILKSCREEN:
RESET

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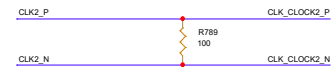
Input Termination Example: Put it close to RC32012A



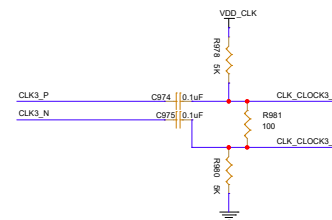
LVPECL DC-Coupled
 Note: if the driver is the 8A34x chip, do not use this termination scheme.
 Use the LVDS terminations scheme.



LVPECL AC-Coupled
 Note: if the driver is the 8A34x chip, then the 180pF pulldown resistors should not be used.

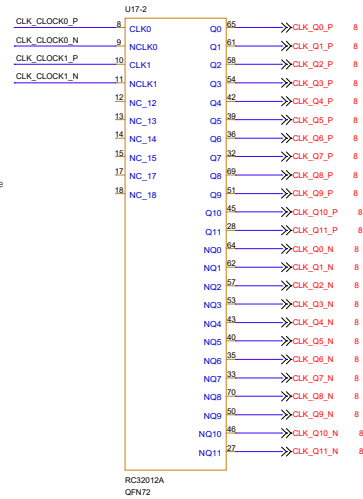


LVDS DC-Coupled

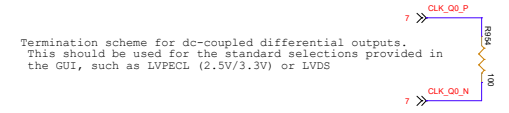


LVDS AC-Coupled

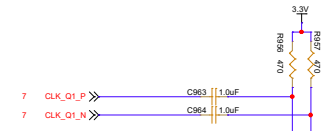
The above input termination of CLK0-CLK3 can be used for CLK0-CLK4 of RC38612



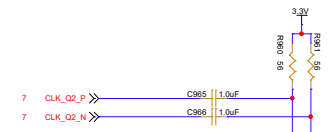
Output Termination Example: Put it close to the receiver side



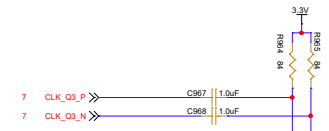
Termination scheme for dc-coupled differential outputs.
 This should be used for the standard selections provided in the GUI, such as LVPECL (2.5V/3.3V) or LVDS



Example Termination for HCSL
 (set the Amplitude to 750mV):



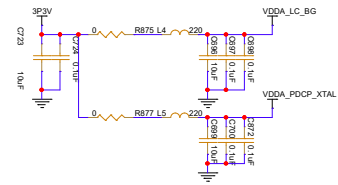
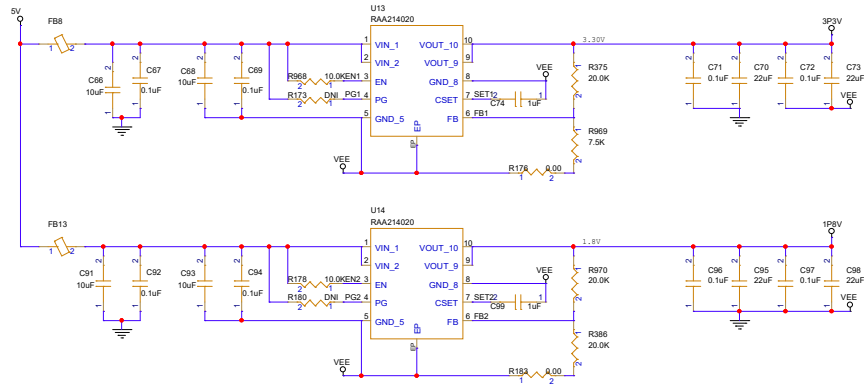
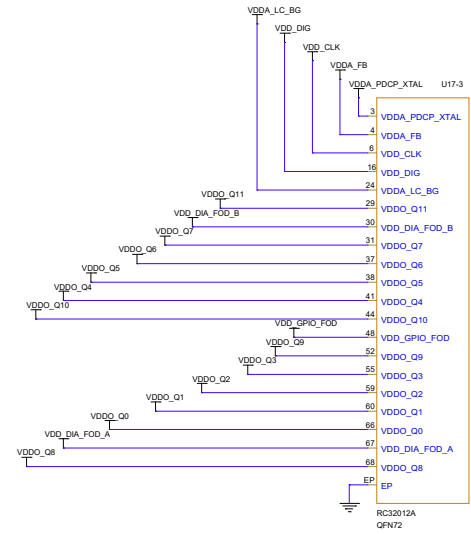
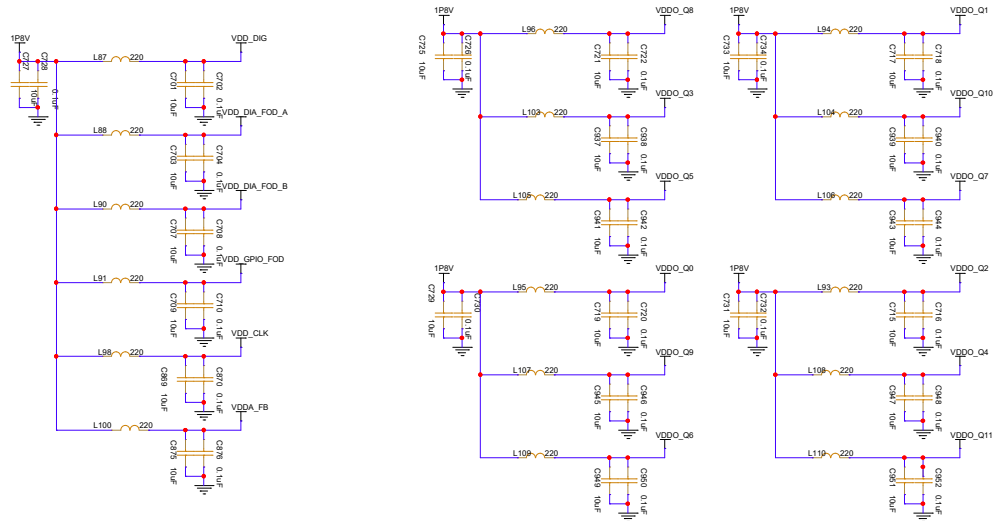
Example Termination for CML:
 (Set output to 750mV amplitude)



Example Termination for ac-coupling to a 3.3V LVPECL receiver.
 Note that no pull-downs are used at the driver.

The above output termination of Q0-Q3 can be used for Q0-Q11.

Please check the AN about Clock Matrix Power Supply Filtering Recommendations.



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