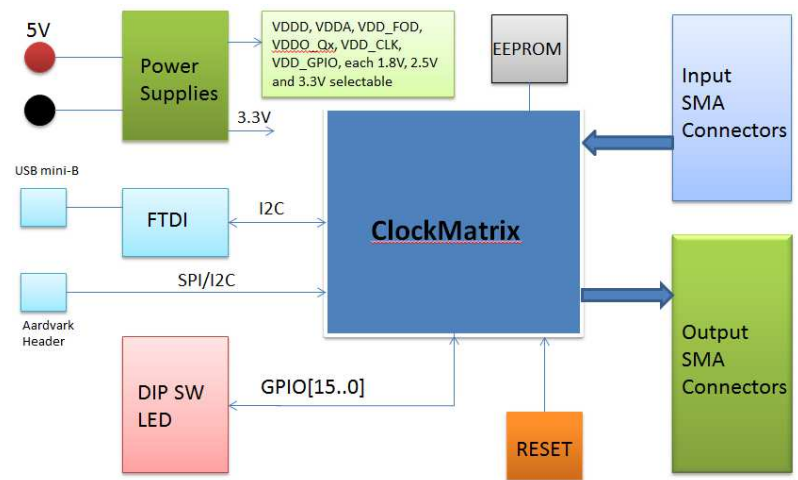
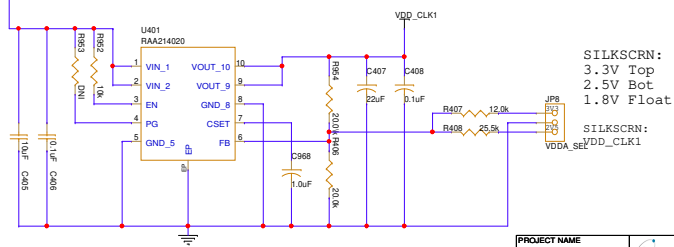
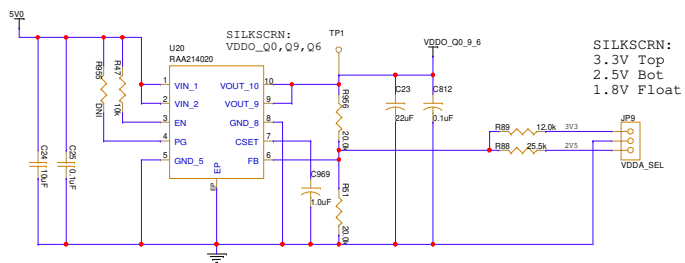
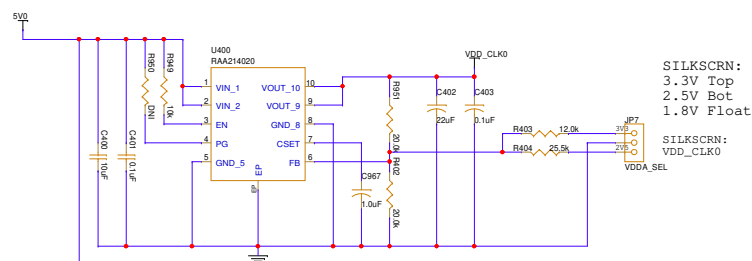
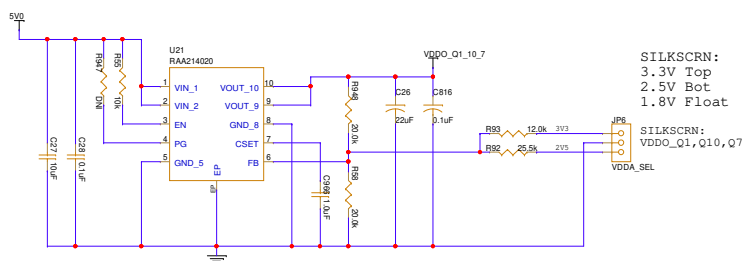
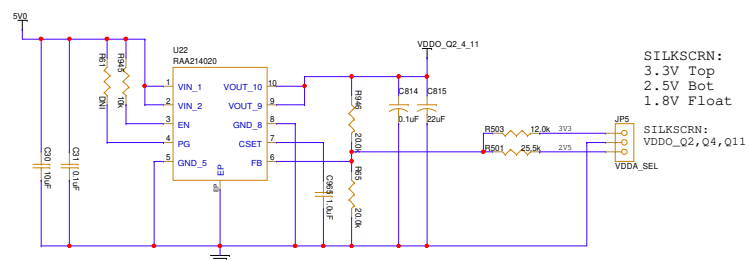
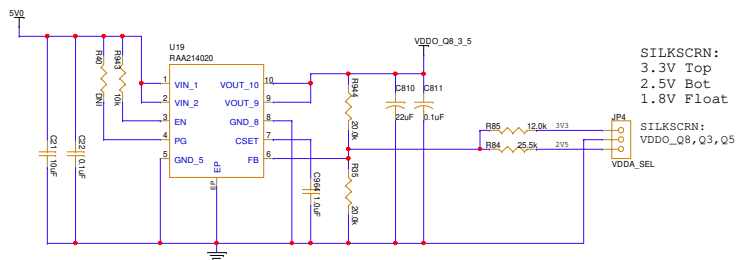




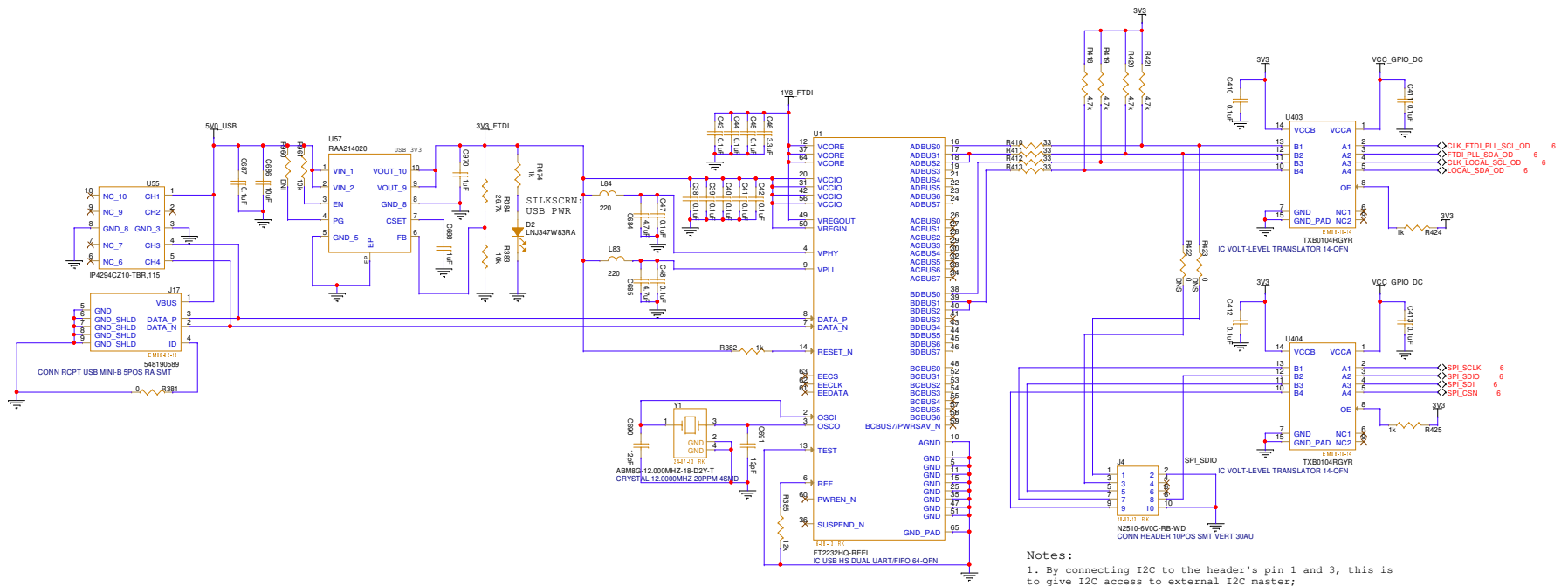
PROJECT NAME	Fidus Systems	
Indra3	375 Terry Fox Drive, Ottawa, ON K2K 0J8	
DESIGN DB	TITLE IDT8A34001	
DRAWN DB	SUBTITLE Block Diagram	
CHECK DB	DRAWING NUMBER SK-10290-01	REVISION 1.2
	RELEASE DATE 27 October 2016	SHEET 1 OF 9

[illegible]

PROJECT NAME	 fidus		Fidus Systems 3795 Terry Fox Drive, Ottawa, ON K2K 0J8	
DESIGN	TITLE			
DB	IDT8A34001			
DRAWN	SUBTITLE			
DB	Power Supplies 1			
CHECK	DRAWING NUMBER		REVISION	
DB	SK-10280-01		1.2	
	RELEASE DATE		SHEET	
	27 October 2016		2 OF 9	



PROJECT NAME	Indra3	Fidus Systems
TITLE	DB	376 Terry Fox Drive, Ottawa, ON K2K 0J8
DRAWN	DB	IDT8A34001
CHECK	DB	Power Supplies 2
REVISION	1.2	3 OF 9
RELEASE DATE	27 October 2016	

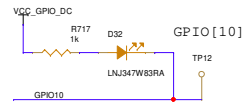
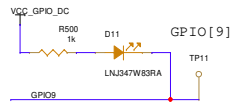
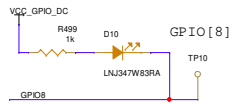
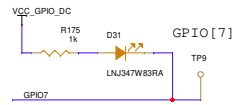
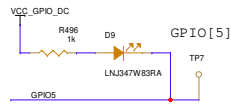
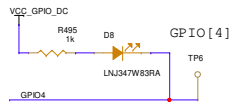
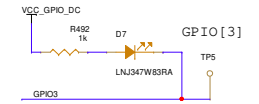
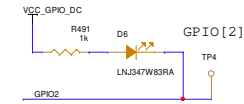
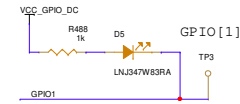
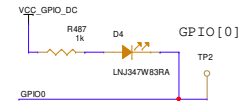
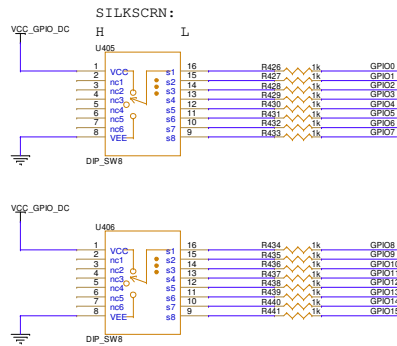


- Notes:
1. By connecting I2C to the header's pin 1 and 3, this is to give I2C access to external I2C master;
 2. By installing R926 and R927, FTDI can also access Aux I2C port (also removing R87 and R88). This feature is not expected to be used.
 3. Normal use: FTDI controls I2C port; Header is connected to Aux port in SPI configuration.

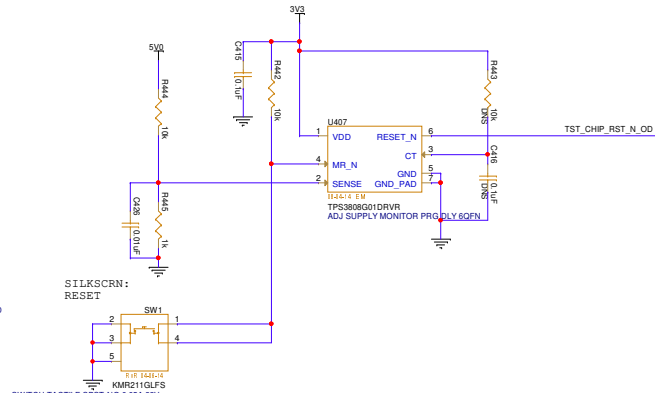
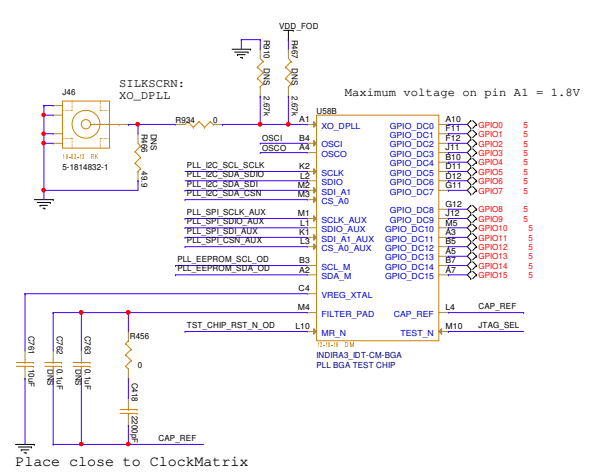
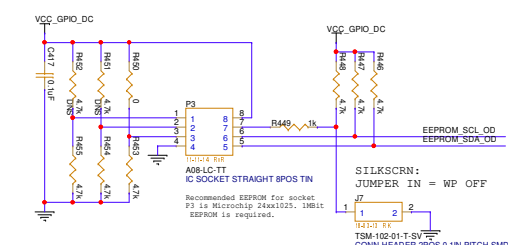
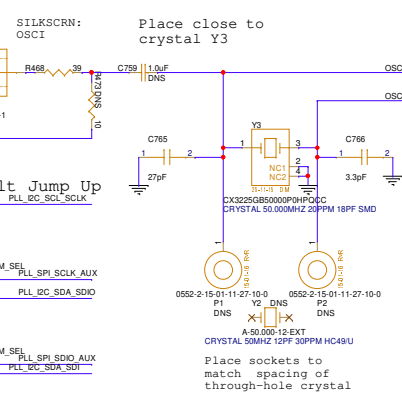
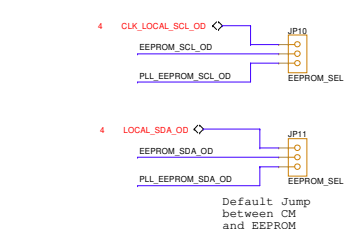
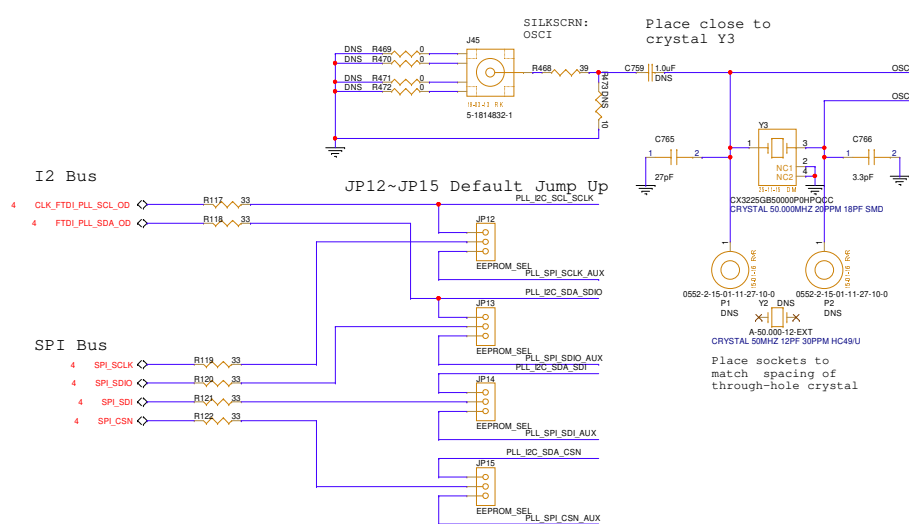
PROJECT NAME		Indra3		Fidus Systems	
TITLE		IDT8A34001		376 Terry Fox Drive, Ottawa, ON K2K 0J8	
DESIGN		DB			
DRAWN		SUBTITLE			
DB		FTDI USB Interface			
CHECK		DRAWING NUMBER		REVISION	
DB		SK-10280-01		1.2	
		RELEASE DATE		SHEET	
		27 October 2016		4 OF 9	

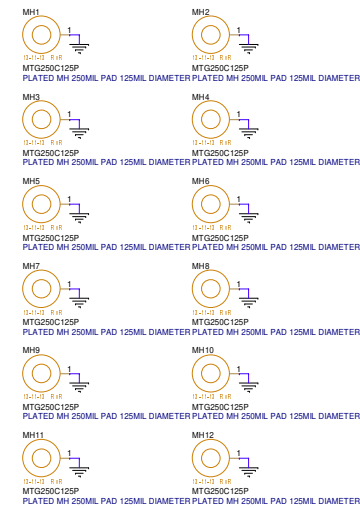
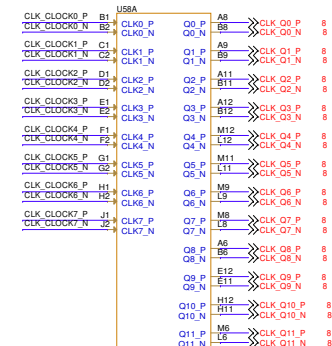
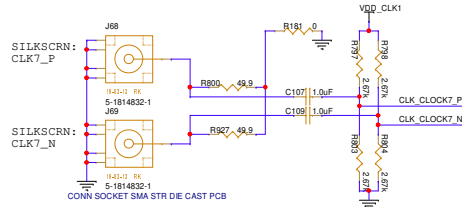
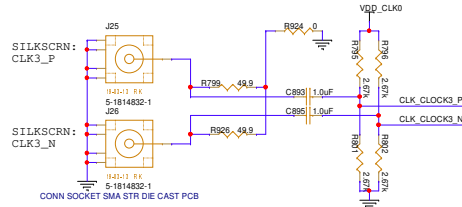
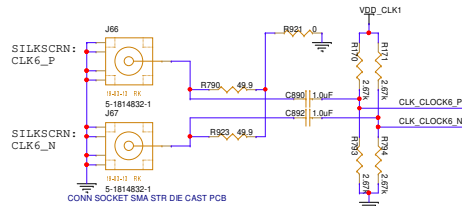
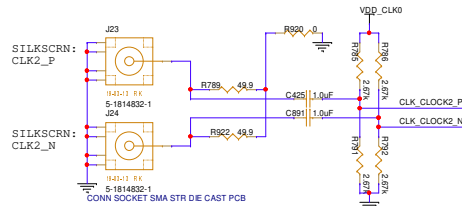
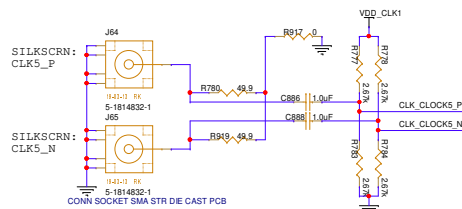
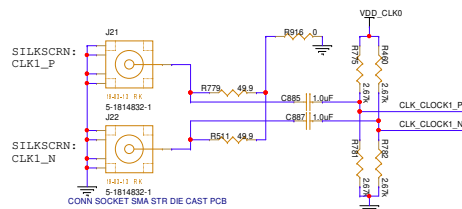
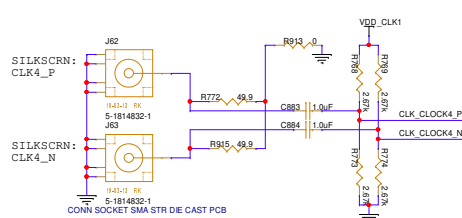
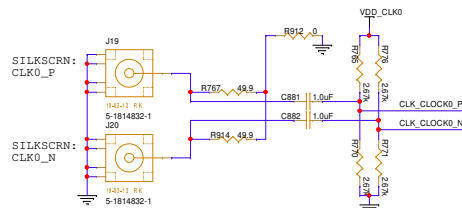
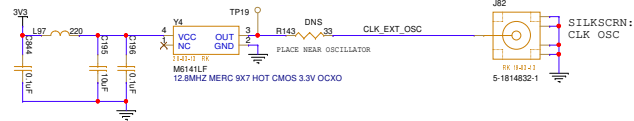
Note for Silkscreen:
Place GPIO[x] label close to each
corresponding LED and Test point.

- 6 GPIO0
- 6 GPIO1
- 6 GPIO2
- 6 GPIO3
- 6 GPIO4
- 6 GPIO5
- 6 GPIO6
- 6 GPIO7
- 6 GPIO8
- 6 GPIO9
- 6 GPIO10
- 6 GPIO11
- 6 GPIO12
- 6 GPIO13
- 6 GPIO14
- 6 GPIO15



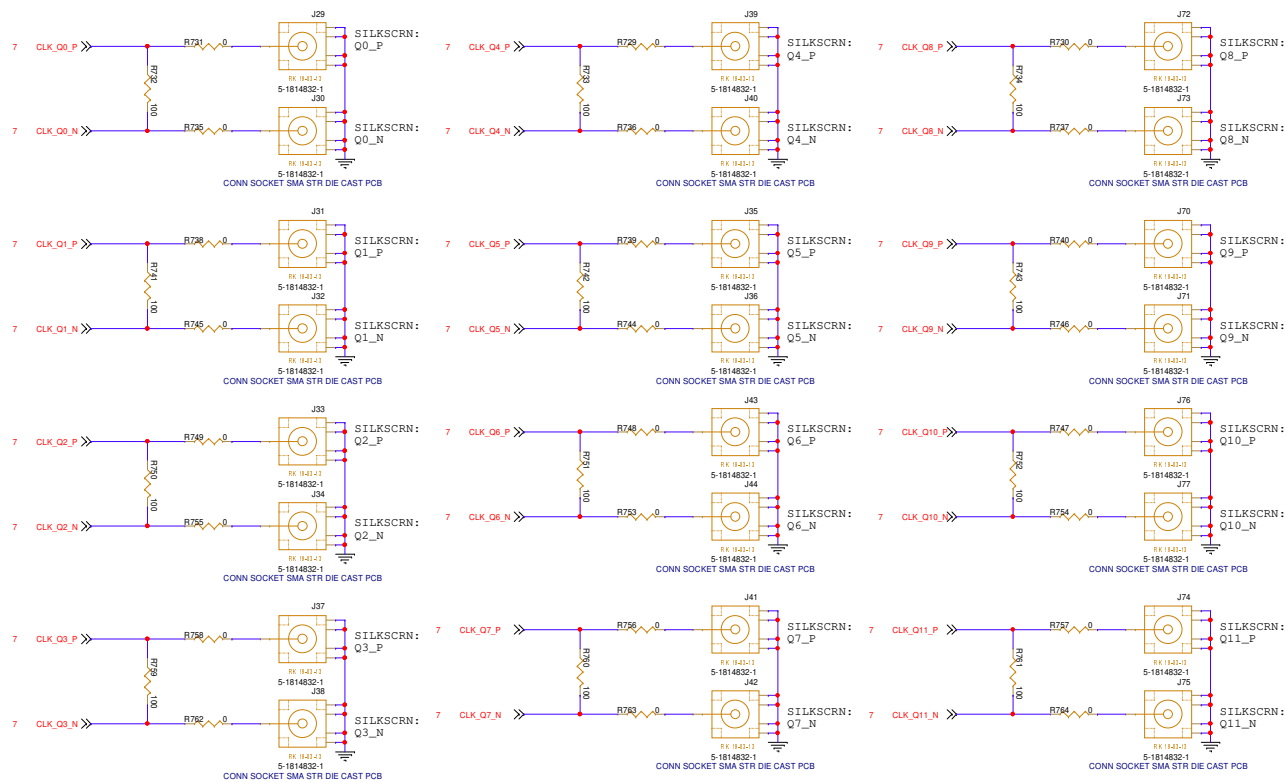
PROJECT NAME	Indra3	fidus	Fidus Systems
DESIGN	RK	IDT8A34001	376 Terry Fox Drive, Ottawa, ON K2K 0J8
DRAWN	RK	SUBTITLE	GPIO LEDs
CHECK	DB	DRAWING NUMBER	SK-10280-01
		RELEASE DATE	27 October 2016
		SHEET	5 OF 9
		REVISION	1.2





PROJECT NAME		Fidus Systems	
Indra3		376 Terry Fox Drive, Ottawa, ON K2K 0J8	
DESIGN	RK	TITLE	IDT8A34001
DRAWN	RK	SUBTITLE	PLL Input Clocks
CHECK	DB	DRAWING NUMBER	SK-10280-01
RELEASE DATE		REVISION	
27 October 2016		1.2	
SHEET		7 OF 9	

PLACE PARALLEL
TERMINATIONS
CLOSE TO U58



PROJECT NAME	Indra3	fidus	Fidus Systems
DESIGN	DB	TITLE	375 Terry Fox Drive, Ottawa, ON K2K 0J8
DRAWN	DB	SUBTITLE	IDT8A34001
CHECK	DB	DRAWING NUMBER	SK-10280-01
RELEASE DATE	27 October 2016	REVISION	1.2
		SHEET	8 OF 9

